

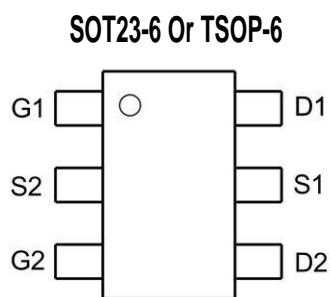
### Product Summary

- 20V, 3.5A,  $R_{DS(ON)} = 40m\Omega$  @ $V_{GS} = 4.5V$ .  
 $R_{DS(ON)} = 50m\Omega$  @ $V_{GS} = 2.5V$ .
- -20V, -2.8A,  $R_{DS(ON)} = 85m\Omega$  @ $V_{GS} = -4.5V$ .  
 $R_{DS(ON)} = 100m\Omega$  @ $V_{GS} = -2.5V$ .

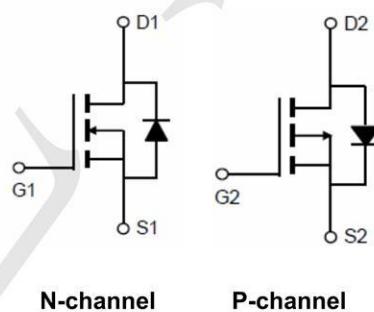
### Application

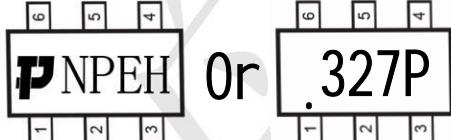
- DC-DC Converters.
- Load Switch.
- Power Management.

### Package and Pin Configuration



### Circuit diagram



**Marking:** 

### Absolute Maximum Ratings ( $T_A=25^\circ C$ unless otherwise noted)

Parameter	Symbol	N-Channel	P-Channel	Units
Drain-Source Voltage	$V_{DS}$	20	-20	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	$\pm 12$	V
Drain Current-Continuous	$I_D$	3.5	-2.8	A
Drain Current-Pulsed <sup>a</sup>	$I_{DM}$	14	10	A
Maximum Power Dissipation	$P_D$	1.14		W
Operating and Store Temperature Range	$T_J, T_{Stg}$	-55 to 150		°C

### Thermal Characteristic

Parameter	Symbol	Limit	Units
Thermal Resistance, Junction-to-Ambient <sup>b</sup>	$R_{JA}$	110	°C/W

**N-CH Electrical Characteristics ( $T_A=25^\circ\text{C}$  unless otherwise noted)**

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_{\text{D}} = 250\mu\text{A}$	20			V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 20\text{V}, V_{\text{GS}} = 0\text{V}$			1	$\mu\text{A}$
Gate Body Leakage Current, Forward	$I_{\text{GSSF}}$	$V_{\text{GS}} = 12\text{V}, V_{\text{DS}} = 0\text{V}$			100	nA
Gate Body Leakage Current, Reverse	$I_{\text{GSSR}}$	$V_{\text{GS}} = -12\text{V}, V_{\text{DS}} = 0\text{V}$			-100	nA
<b>On Characteristics <sup>c</sup></b>						
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{GS}} = V_{\text{DS}}, I_{\text{D}} = 250\mu\text{A}$	0.4		1.2	V
Static Drain-Source	$R_{\text{DS(on)}}$	$V_{\text{GS}} = 4.5\text{V}, I_{\text{D}} = 3.5\text{A}$		40	55	$\text{m}\Omega$
On-Resistance		$V_{\text{GS}} = 2.5\text{V}, I_{\text{D}} = 2.0\text{A}$		50	80	$\text{m}\Omega$
<b>Dynamic Characteristics <sup>d</sup></b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}} = 10\text{V}, V_{\text{GS}} = 0\text{V}, f = 1.0 \text{ MHz}$		380		pF
Output Capacitance	$C_{\text{oss}}$			90		pF
Reverse Transfer Capacitance	$C_{\text{rss}}$			60		pF
<b>Switching Characteristics <sup>d</sup></b>						
Turn-On Delay Time	$t_{\text{d(on)}}$	$V_{\text{DD}} = 10\text{V}, I_{\text{D}} = 3.5\text{A}, V_{\text{GS}} = 4.5\text{V}, R_{\text{GEN}} = 6\Omega$		16		ns
Turn-On Rise Time	$t_r$			16		ns
Turn-Off Delay Time	$t_{\text{d(off)}}$			32		ns
Turn-Off Fall Time	$t_f$			7		ns
Total Gate Charge	$Q_g$	$V_{\text{DS}} = 10\text{V}, I_{\text{D}} = 3.5\text{A}, V_{\text{GS}} = 3.3\text{V}$		3.6		nC
Gate-Source Charge	$Q_{\text{gs}}$			1.0		nC
Gate-Drain Charge	$Q_{\text{gd}}$			1.2		nC
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
Drain-Source Diode Forward Current <sup>b</sup>	$I_s$				1	A
Drain-Source Diode Forward Voltage <sup>c</sup>	$V_{\text{SD}}$	$V_{\text{GS}} = 0\text{V}, I_s = 1\text{A}$			1.1	V

**P-CH Electrical Characteristics ( $T_A=25^\circ\text{C}$  unless otherwise noted)**

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_{\text{D}} = -250\mu\text{A}$	-20			V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = -16\text{V}, V_{\text{GS}} = 0\text{V}$			-1	$\mu\text{A}$
Gate Body Leakage Current, Forward	$I_{\text{GSSF}}$	$V_{\text{GS}} = 12\text{V}, V_{\text{DS}} = 0\text{V}$			100	nA
Gate Body Leakage Current, Reverse	$I_{\text{GSSR}}$	$V_{\text{GS}} = -12\text{V}, V_{\text{DS}} = 0\text{V}$			-100	nA
<b>On Characteristics<sup>c</sup></b>						
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{GS}} = V_{\text{DS}}, I_{\text{D}} = -250\mu\text{A}$	-0.4		-1.2	V
Static Drain-Source On-Resistance	$R_{\text{DS(on)}}$	$V_{\text{GS}} = -4.5\text{V}, I_{\text{D}} = -2.5\text{A}$	85	100	145	$\text{m}\Omega$
		$V_{\text{GS}} = -2.5\text{V}, I_{\text{D}} = -1.5\text{A}$		100	145	$\text{m}\Omega$
<b>Dynamic Characteristics<sup>d</sup></b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}} = -10\text{V}, V_{\text{GS}} = 0\text{V}, f = 1.0 \text{ MHz}$		375		pF
Output Capacitance	$C_{\text{oss}}$			90		pF
Reverse Transfer Capacitance	$C_{\text{rss}}$			60		pF
<b>Switching Characteristics<sup>d</sup></b>						
Turn-On Delay Time	$t_{\text{d(on)}}$	$V_{\text{DD}} = -10\text{V}, I_{\text{D}} = -2.5\text{A}, V_{\text{GS}} = -4.5\text{V}, R_{\text{GEN}} = 3\Omega$		17		ns
Turn-On Rise Time	$t_r$			17		ns
Turn-Off Delay Time	$t_{\text{d(off)}}$			27		ns
Turn-Off Fall Time	$t_f$			7		ns
Total Gate Charge	$Q_g$	$V_{\text{DS}} = -10\text{V}, I_{\text{D}} = -2.0\text{A}, V_{\text{GS}} = -3.3\text{V}$		2.9		nC
Gate-Source Charge	$Q_{\text{gs}}$			0.46		nC
Gate-Drain Charge	$Q_{\text{gd}}$			1.19		nC
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
Drain-Source Diode Forward Current <sup>b</sup>	$I_s$				-1	A
Drain-Source Diode Forward Voltage <sup>c</sup>	$V_{\text{SD}}$	$V_{\text{GS}} = 0\text{V}, I_s = -1\text{A}$			-1.1	V

### N- Channel Typical Electrical and Thermal Characteristics

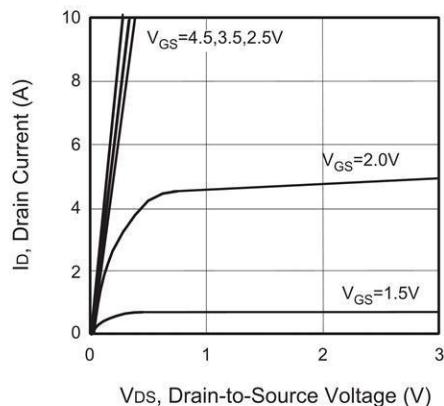


Figure 1. Output Characteristics

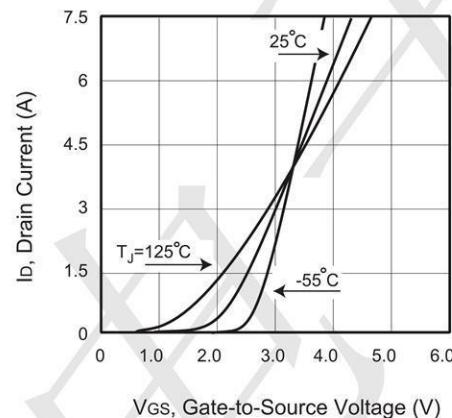


Figure 2. Transfer Characteristics

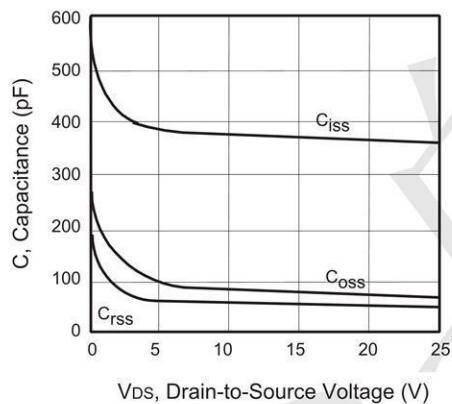


Figure 3. Capacitance

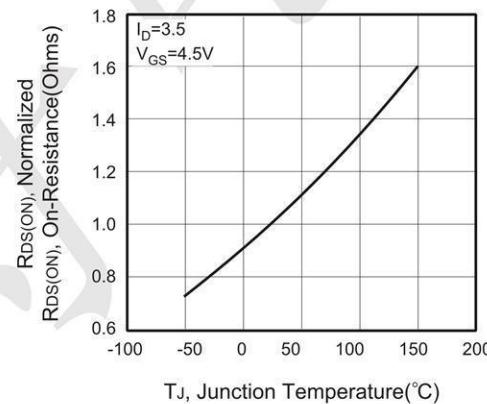


Figure 4. On-Resistance Variation with Temperature

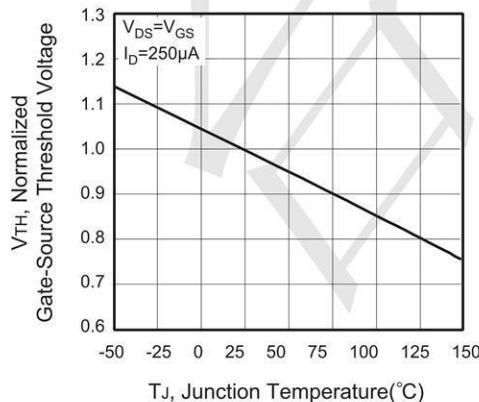


Figure 5. Gate Threshold Variation with Temperature

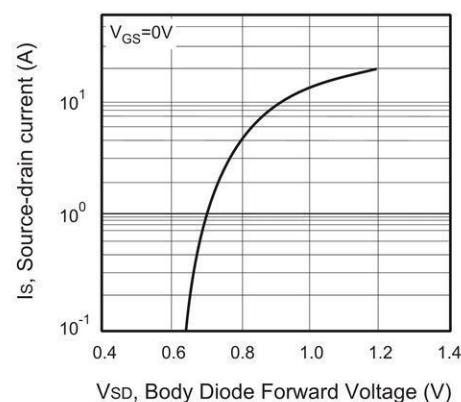


Figure 6. Body Diode Forward Voltage Variation with Source Current

**P-CHANNEL**

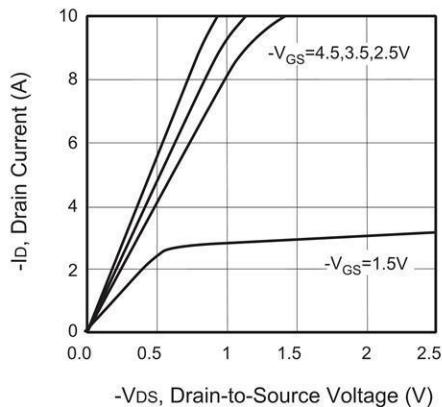


Figure 1. Output Characteristics

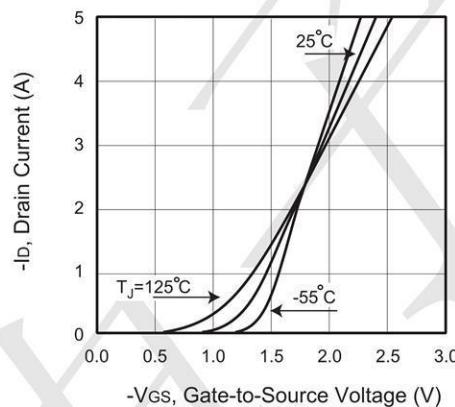


Figure 2. Transfer Characteristics

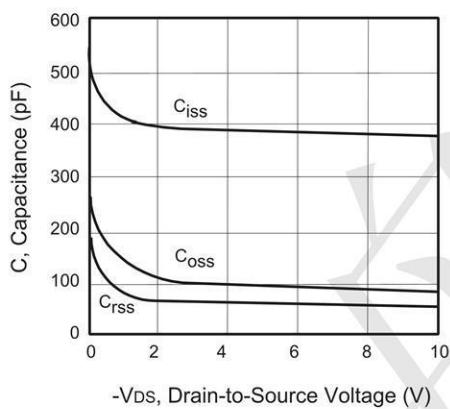


Figure 3. Capacitance

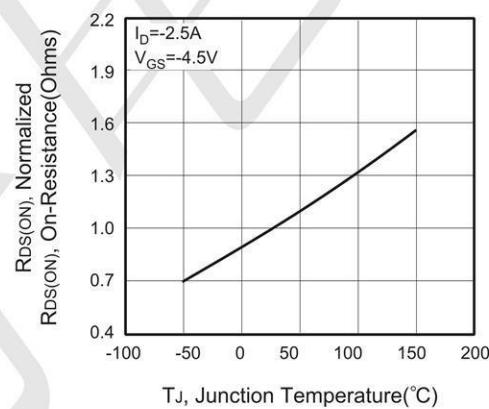


Figure 4. On-Resistance Variation with Temperature

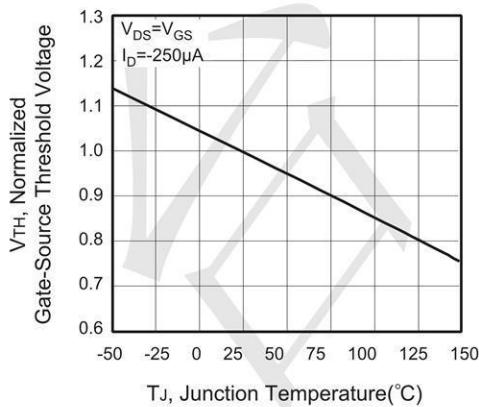


Figure 5. Gate Threshold Variation with Temperature

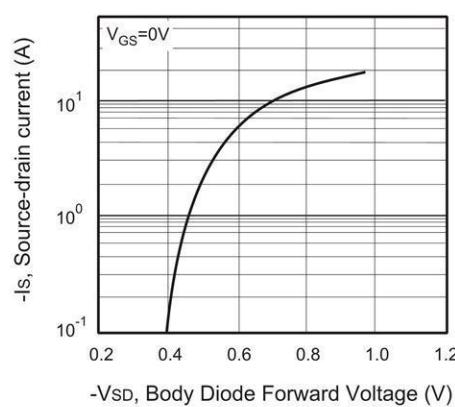


Figure 6. Body Diode Forward Voltage Variation with Source Current

**N-CHANNEL**

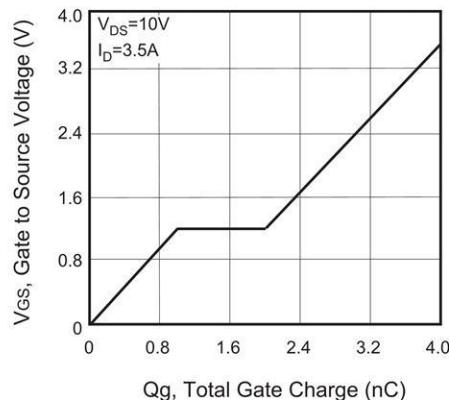


Figure 13. Gate Charge

**P-CHANNEL**

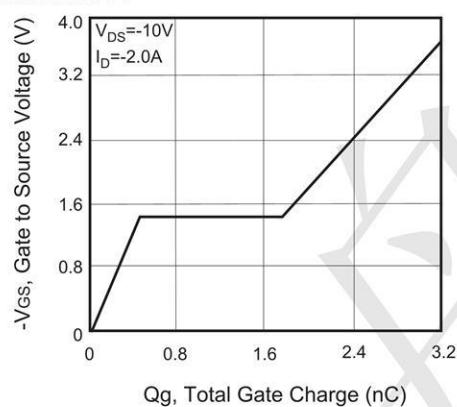


Figure 15. Gate Charge

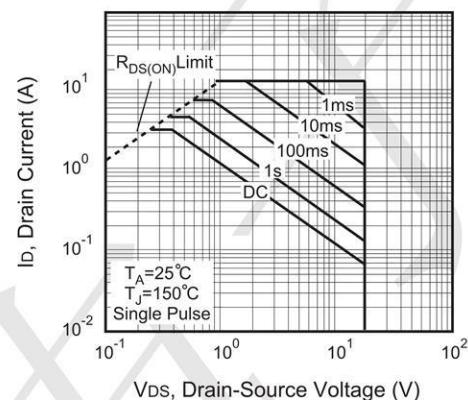


Figure 14. Maximum Safe Operating Area

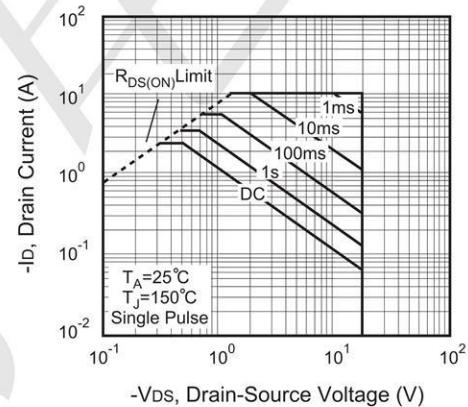


Figure 16. Maximum Safe Operating Area

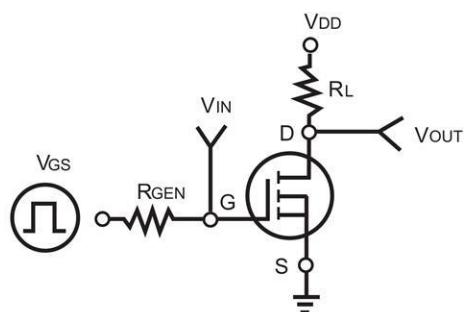


Figure 17. Switching Test Circuit

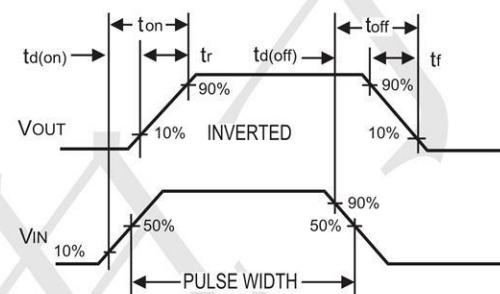


Figure 18. Switching Waveforms

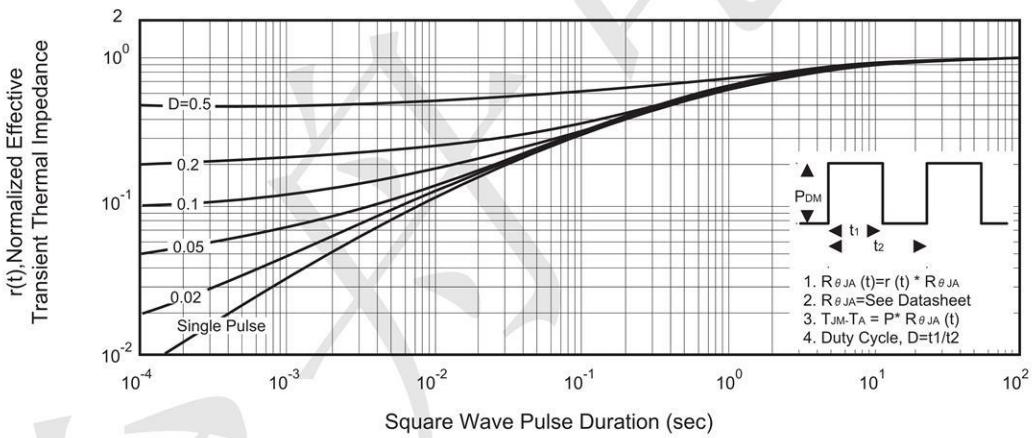
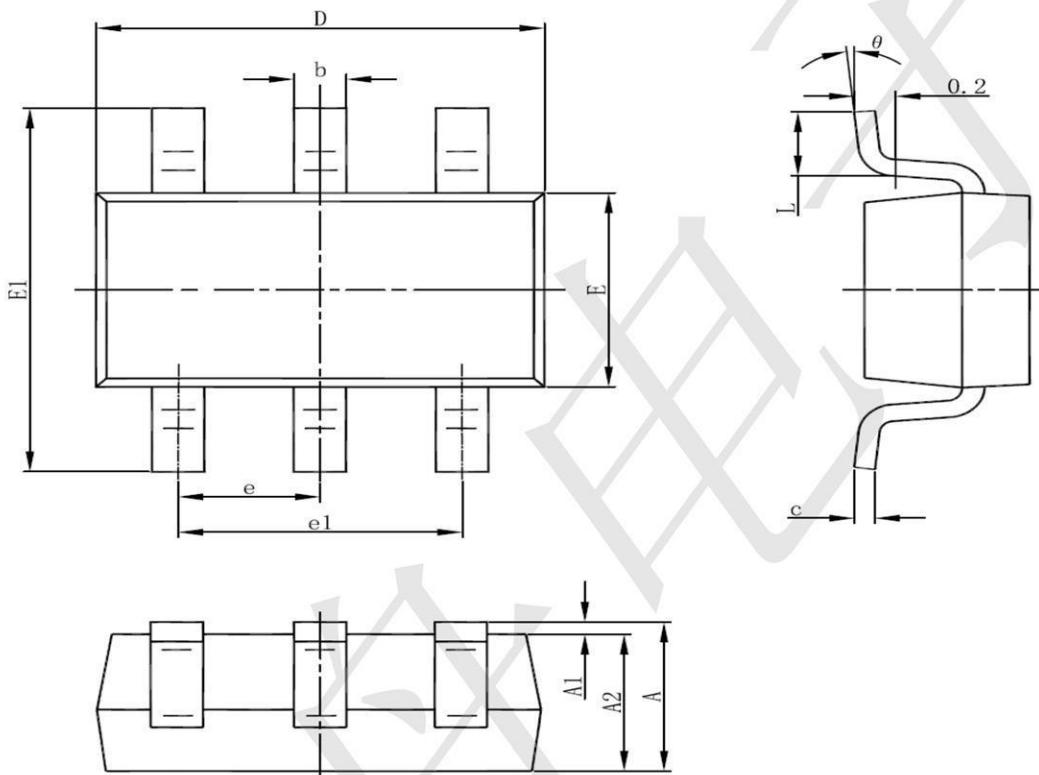


Figure 19. Normalized Thermal Transient Impedance Curve

**SOT23-6 Package Information**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

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