

TPM30V6NS6 30V N-Channel Enhancement Mode MOSFET

General Features

- Vds=30V Id=6A
- $R_{DS(ON)} < 23m\Omega$, $V_{GS}@10V$,
- $R_{DS(ON)} < 32m\Omega, V_{GS}@4.5V,$

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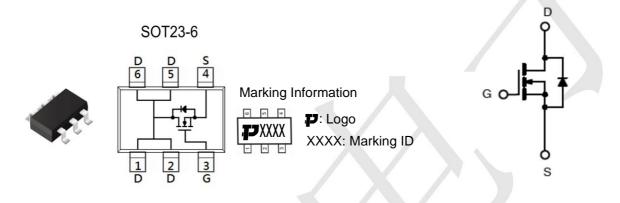
Application

Load/Power Switching

Block Diagram

- Interfacing Switching
- Battery Management for Ultra Small Portable Electronics
- Logic Level Shift

Package and Pin Configuration



Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	30	V	
Gate-Source Voltage	V _{GS}	<u>+</u> 20			
Continuous Drain Current (Note 4)		١D	6	A	
Pulsed Drain Current (Note 1)		I _{DM}	25		
Power Dissipation	Ta=25°C	PD	2	w	
	Derate above 25°C		16	mW/°C	
Operating Junction and Storage Temperature Range		TJ,TSTG	-55~150	°C	
Typical Thermal Resistance - Junction to Ambient ^(Note 3)		Reja	62.5	°C/W	



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Parameter	Conditions	Symbol	Min	Тур	Max	Unit
Static	1					
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_{D} = 250 \mu A$	BV _{DSS}	30	<u> </u>		V
Drain-Source On-State Resistance	$V_{GS} = 10V, I_D = 6A$	R _{DS(on)}		18	23	mΩ
	$V_{GS} = 4.5V, I_D = 4A$			22	32	
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	V _{GS(TH)}	1.0	1.5	2.5	V
Zero Gate Voltage Drain Current	V _{DS} = 30V, V _{GS} = 0V	I _{DSS}			1	μA
	V _{DS} = 24V, T _J = 125°C		-		10	
Gate Body Leakage	$V_{GS} = \pm 20V, V_{DS} = 0V$	I _{GSS}			±100	μA
Forward Transconductance (Note 3)	$V_{DS} = 10V, I_{D} = 4A$	g _{fs}		6.5		S
Dynamic						
Total Gate Charge (Note 3,4)	$V_{DS} = 15V, I_D = 6A,$	Qg	4	4.1		nC
Gate-Source Charge (Note 3,4)		Q _{gs}	1	1		
Gate-Drain Charge (Note 3,4)	$V_{GS} = 4.5V$	Q _{gd}		2.1		
Input Capacitance	$V_{DS} = 25V, V_{GS} = 0V,$	C _{iss}		345		pF
Output Capacitance		C _{oss}		55		
Reverse Transfer Capacitance	f = 1.0MHz	C _{rss}		32		
Switching						
Turn-On Delay Time (Note 3,4)	$V_{DD} = 15V, I_D = 1A,$ $V_{GS} = 10V, R_G = 6\Omega$	t _{d(on)}		2.8		ns
Turn-On Rise Time (Note 3,4)		t _r		7.2		
Turn-Off Delay Time (Note 3,4)		t _{d(off)}		15.8		
Turn-Off Fall Time (Note 3,4)		t _f		4.6		
Source-Drain Diode Ratings and Ch	aracteristic					
Maximum Continuous Drain-Source Diode Forward Current	Integral reverse diode in the MOSFET	I _S			6	А
Maximum Pulse Drain-Source Diode Forward Current		I _{SM}			25	А
Diode-Source Forward Voltage	V _{GS} = 0V, I _S = 1A	V _{SD}			1	V

Note:

1. Pulse width limited by safe operating area

2. L = 1mH, I_{AS} = 8A, V_{DD} = 25V, R_G = 25 Ω , Starting T_J = 25°C

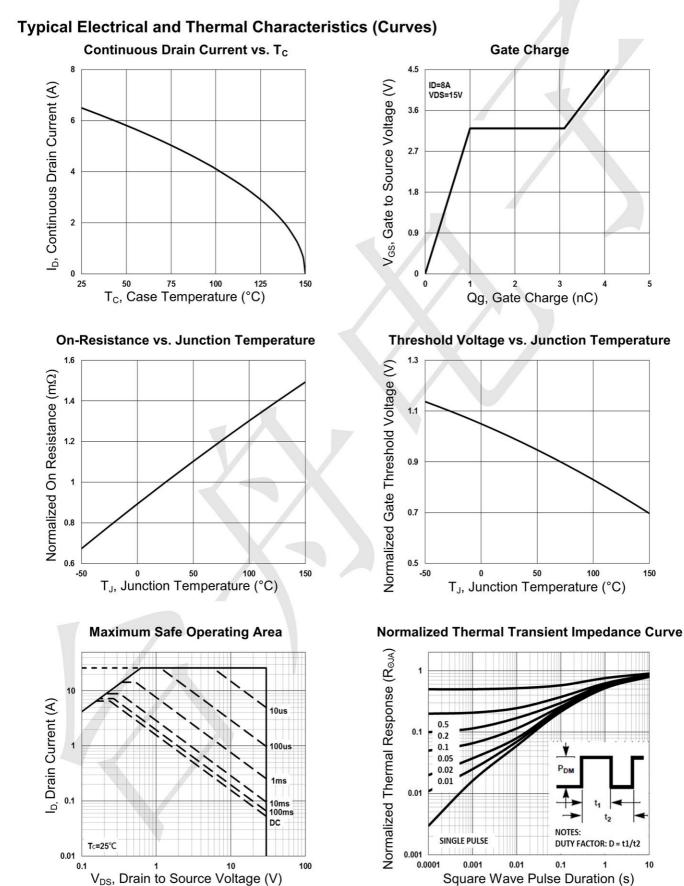
3. Pulse test: pulse width \leq 300µs, duty cycle \leq 2%

4. Switching time is essentially independent of operating temperature.



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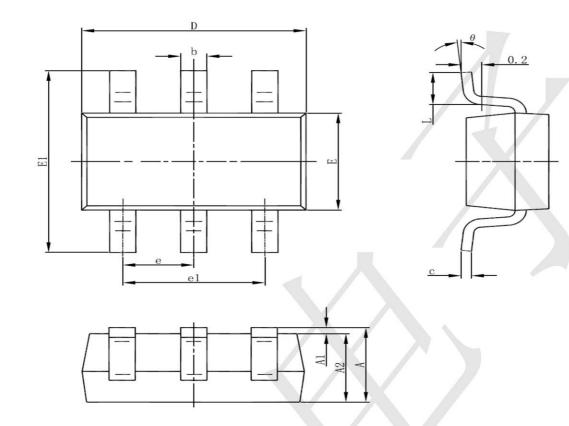


Square Wave Pulse Duration (s)



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SOT23-6 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min	Max	Min	Max	
А	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
С	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
E	1.500	1.700	0.059	0.067	
E1	2.650	2.950	0.104	0.116	
е	0.950(BSC)		0.037(BSC)		
e1	1.800	2.000	0.071	0.079	
L	0.300	0.600	0.012	0.024	
θ	0°	8°	0°	8°	

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