

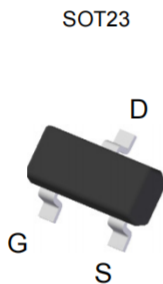
### General Features

- $V_{DS} = 100V, I_D = 3.5A$
- $R_{DS(ON)} < 130m\Omega @ V_{GS}=10V$  (96m $\Omega$  Typ)
- $R_{DS(ON)} < 180m\Omega @ V_{GS}=4.5V$  (140m $\Omega$  Typ)

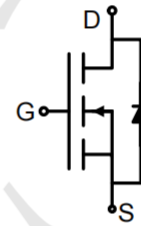
### Application

- Load/Power Switching
- Interfacing Switching
- Battery Management for Ultra Small Portable
- Logic Level Shift

### Package and Pin Configuration



### Circuit diagram



### Marking:



“P” is TECHPUBLIC LOGO  
“3N” is Part number, fixed  
“xx” is internal code

### Absolute Maximum Ratings ( $T_A=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous	$I_D$	3.5	A
Drain Current-Pulsed <sup>(Note 1)</sup>	$I_{DM}$	20	A
Maximum Power Dissipation	$P_D$	1.5	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 175	$^\circ C$

### Thermal Characteristic

Thermal Resistance, Junction-to-Ambient <sup>(Note 2)</sup>	$R_{\theta JA}$	100	$^\circ C/W$
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**Electrical Characteristics ( $T_A=25^{\circ}\text{C}$  unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	100	-	-	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=100V, V_{GS}=0V$	-	-	1	$\mu A$
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	$\pm 100$	nA
<b>On Characteristics</b> (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	1.5	2.0	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=3A$	-	96	130	m $\Omega$
		$V_{GS}=4.5V, I_D=1A$	-	140	180	
Forward Transconductance	$g_{FS}$	$V_{DS}=5V, I_D=3A$	-	5	-	S
<b>Dynamic Characteristics</b> (Note 4)						
Input Capacitance	$C_{iss}$	$V_{DS}=50V, V_{GS}=0V,$ $F=1.0\text{MHz}$	-	650	-	PF
Output Capacitance	$C_{oss}$		-	24	-	PF
Reverse Transfer Capacitance	$C_{rss}$		-	20	-	PF
<b>Switching Characteristics</b> (Note 4)						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=50V, R_L=19\Omega$ $V_{GS}=10V, R_G=3\Omega$	-	6	-	nS
Turn-on Rise Time	$t_r$		-	4	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	20	-	nS
Turn-Off Fall Time	$t_f$		-	4	-	nS
Total Gate Charge	$Q_g$	$V_{DS}=50V, I_D=3A,$ $V_{GS}=10V$	-	20	-	nC
Gate-Source Charge	$Q_{gs}$		-	2.1	-	nC
Gate-Drain Charge	$Q_{gd}$		-	3.3	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 3)	$V_{SD}$	$V_{GS}=0V, I_S=3A$	-	-	1.2	V
Diode Forward Current (Note 2)	$I_S$			3.5		A

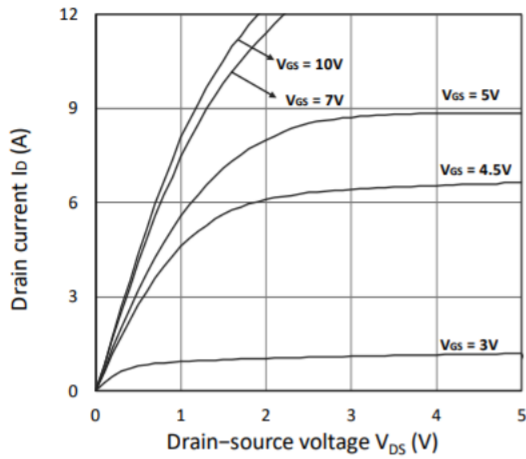


Figure 1. Output Characteristics

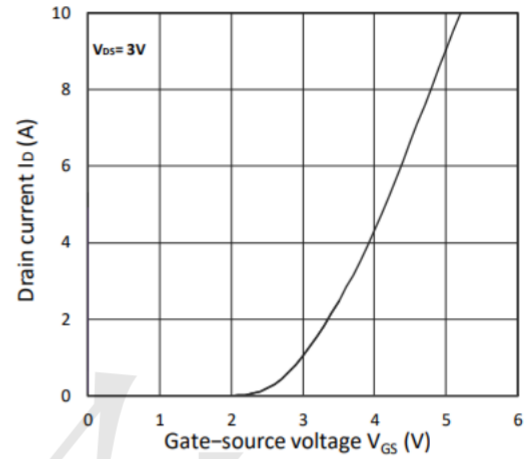


Figure 2. Transfer Characteristics

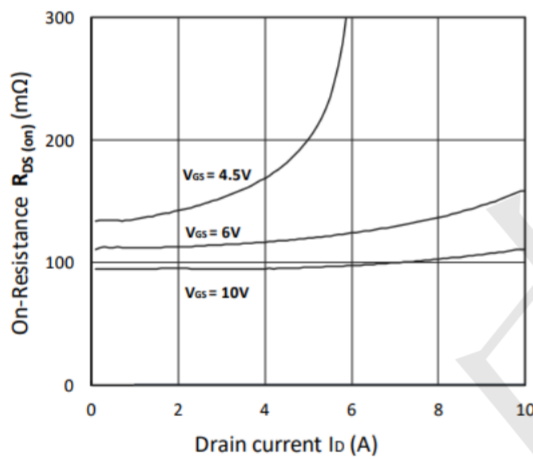


Figure 3.  $R_{DS(ON)}$  VS.  $I_D$

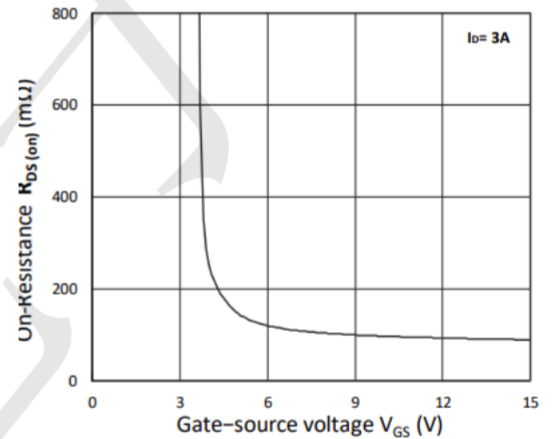


Figure 4.  $R_{DS(ON)}$  VS.  $V_{GS}$

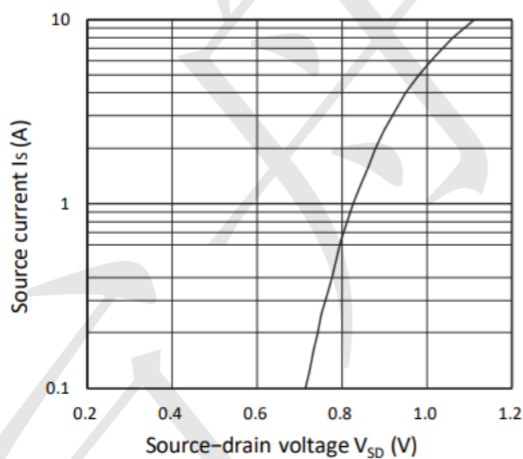


Figure 5.  $I_S$  VS.  $V_{SD}$

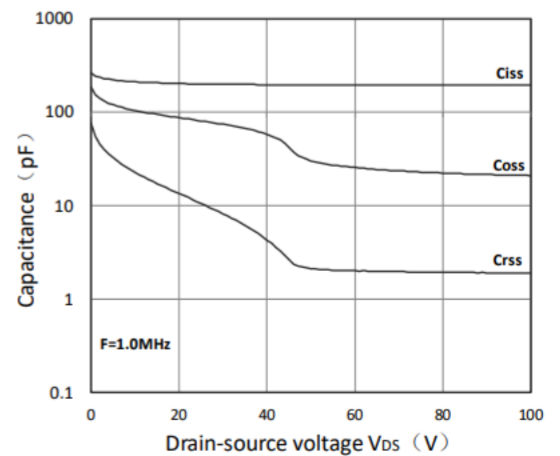
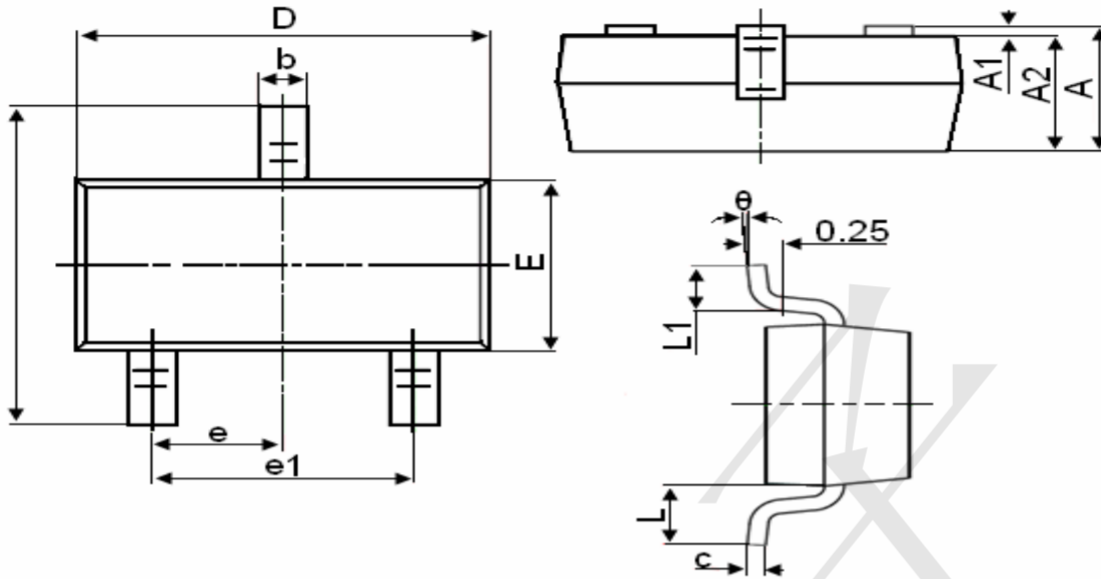


Figure 6. Capacitance Characteristics

**Package Outline Dimensions (SOT-23)**



Symbol	Dimensions in Millimeters	
	MIN.	MAX.
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
c	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
e	0.950TYP	
e1	1.800	2.000
L	0.550REF	
L1	0.300	0.500
$\theta$	0°	8°

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