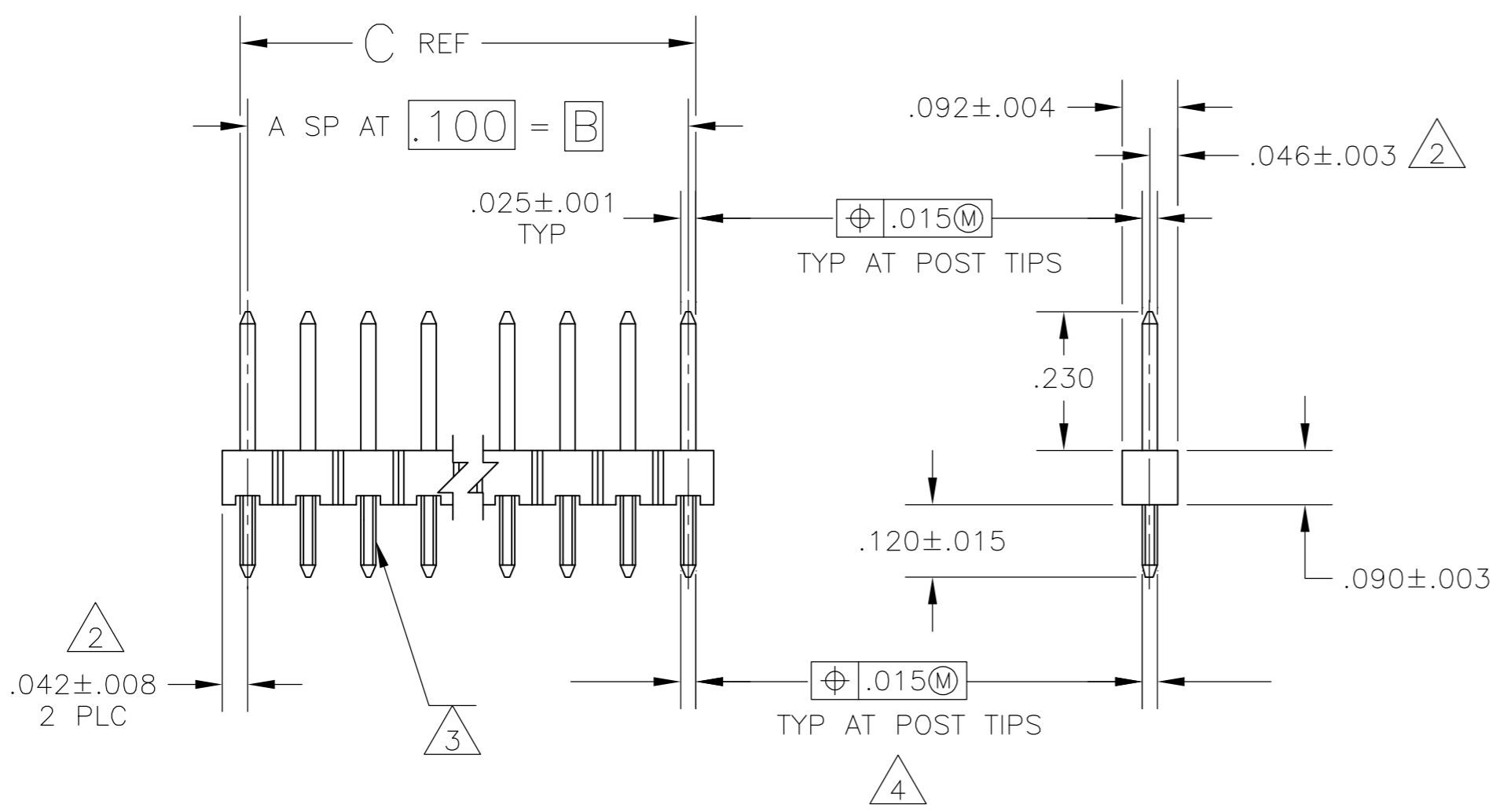
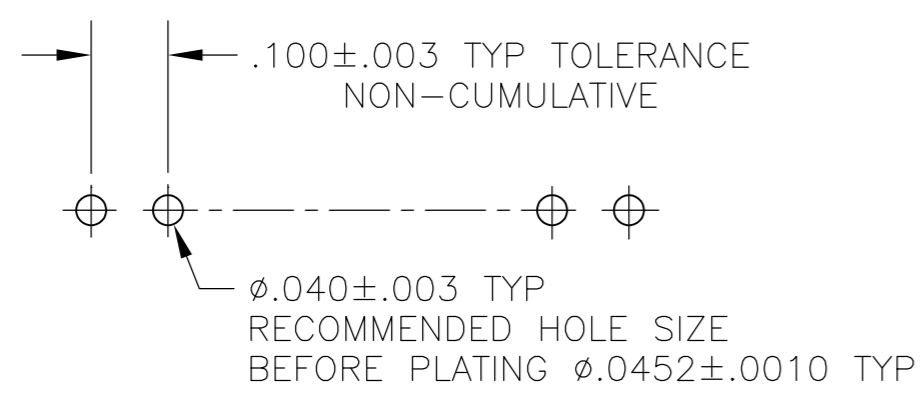


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LOC		DIST		REVISIONS				
AD	00	P	LTR	DESCRIPTION	DATE	DWN	APVD	
			K1	REVISED PER ECO-09-021826	27AUG09	KK	AEG	



1. TRUE POSITION TOLERANCE OF THE POST TIPS APPLIES WHEN THE HEADERS ARE HELD FLAT AGAINST THE PRINTED CIRCUIT BOARD.
 2. THE NOTED DIMENSIONS APPLY AT THE INTERSECTION OF THE POST AND THE HOUSING.
- △ 3 RETENTION FEATURES ON SOLDER TAILS, LOCATED AT MANUFACTURERS OPTION.
 △ 4 $\text{M} \text{ } \phi .020$ FOR KINKED TAILS
 △ 5 OBSOLETE PARTS: OBSOLETE CIS STREAMLINING PER D.RENAUD/D.SINISI



RECOMMENDED HOLE LAYOUT

OBSOLETE	3.984	3.900	39	40	2-104346-7
	3.484	3.400	34	35	2-104346-6
	2.984	2.900	29	30	2-104346-5
	2.584	2.500	25	26	2-104346-4
	2.484	2.400	24	25	2-104346-3
	2.384	2.300	23	24	2-104346-2
	2.284	2.200	22	23	2-104346-1
	2.184	2.100	21	22	2-104346-0
	2.084	2.000	20	21	1-104346-9
	1.984	1.900	19	20	1-104346-8
OBSOLETE	1.884	1.800	18	19	1-104346-7
	1.784	1.700	17	18	1-104346-6
	1.684	1.600	16	17	1-104346-5
	1.584	1.500	15	16	1-104346-4
	1.484	1.400	14	15	1-104346-3
	1.384	1.300	13	14	1-104346-2
	1.284	1.200	12	13	1-104346-1
	1.184	1.100	11	12	1-104346-0
	1.084	1.000	10	11	1-104346-9
	.984	.900	9	10	104346-8
OBSOLETE	.884	.800	8	9	104346-7
	.784	.700	7	8	104346-6
	.684	.600	6	7	104346-5
	.584	.500	5	6	104346-4
	.484	.400	4	5	104346-3
	.384	.300	3	4	104346-2
	.284	.200	2	3	104346-1
	C	B	A	NO OF POSN	ASSEMBLY PART NUMBER

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN H MOLL 08FEB88	Tyco Electronics Corporation Harrisburg, PA 17105-3608	
DIMENSIONS: INCHES		CHK R. FEIDT 08FEB88	NAME	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD R. FEIDT 08FEB88	PRODUCT SPEC	
0 PLC ± -		APPLICATION SPEC		
1 PLC ± -		WEIGHT -		
2 PLC ± -		SCALE A2		
3 PLC ± .005		SHEET 1 of 1		
4 PLC ± -		REV K1		
ANGLES ± -		CUSTOMER DRAWING		
MATERIAL HOUSING: FLAME RETARDANT THERMOPLASTIC COLOR-BLACK POSTS: PHOSPHOR BRONZE		FINISH POSTS: .000100-.000200 MATTE TIN-LEAD, ALL OVER .000050 NICKEL		

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