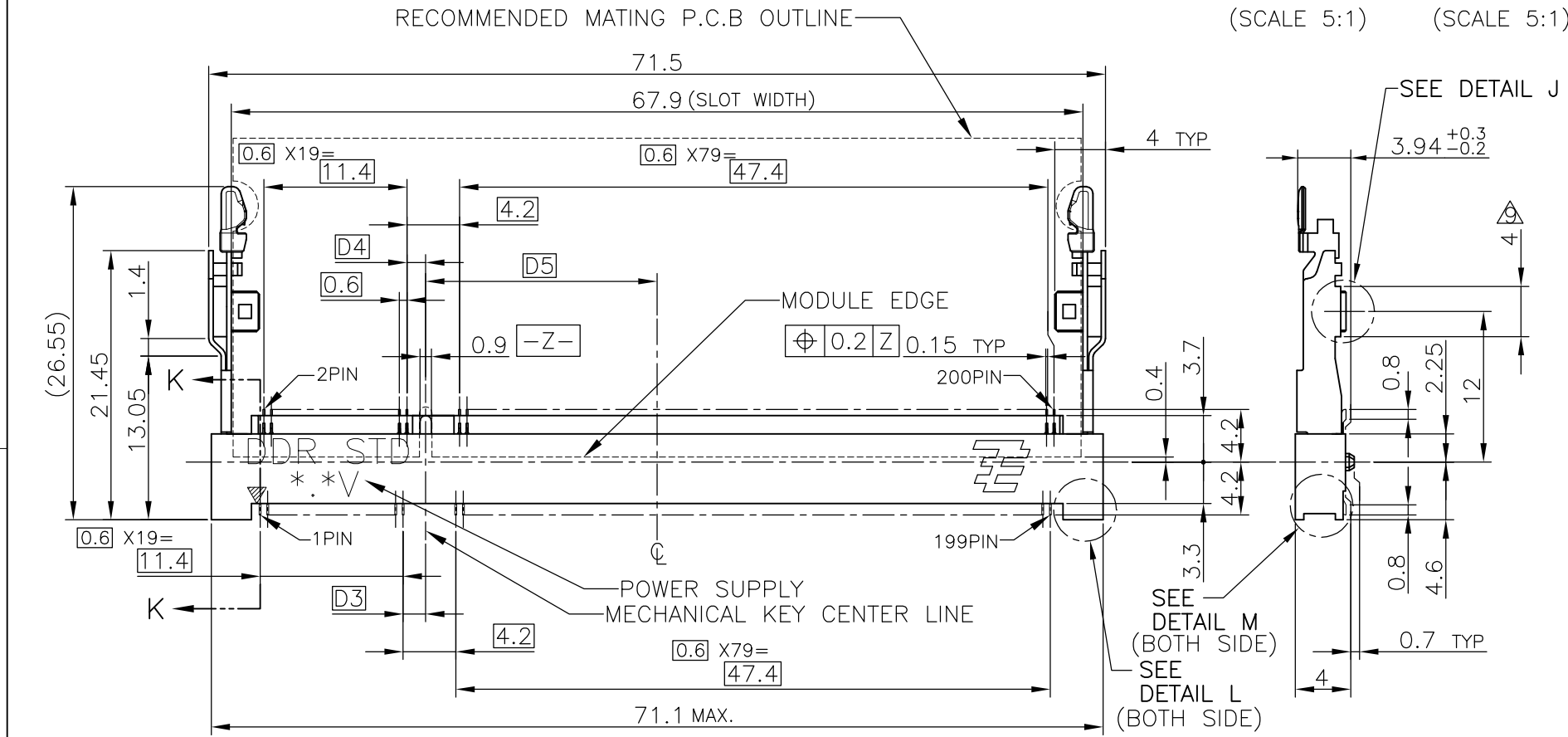
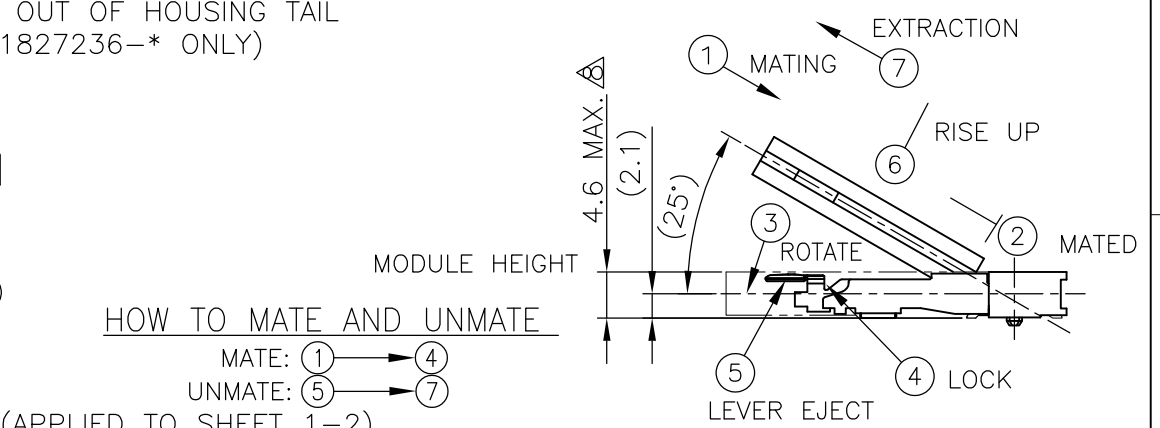
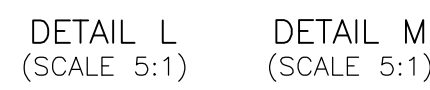
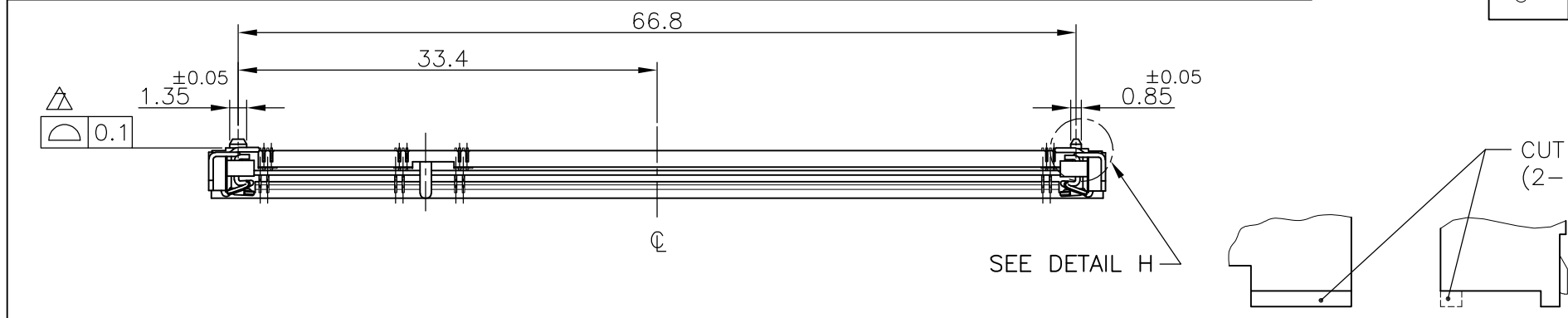
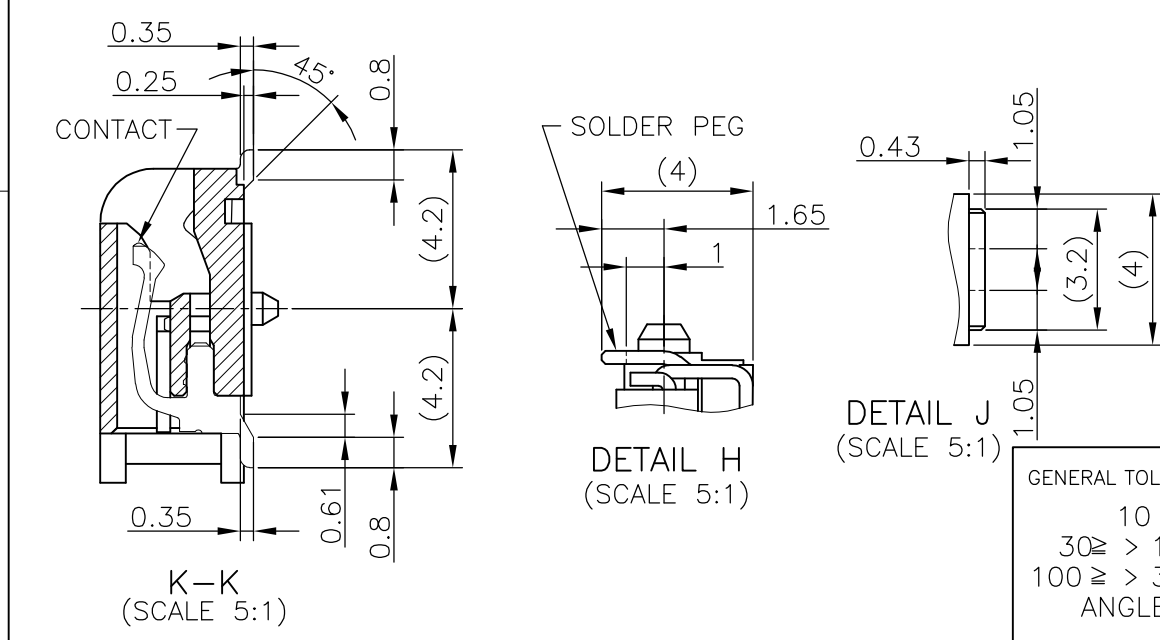


THIS DRAWING IS UNPUBLISHED. RELEASED FOR PUBLICATION NOV. 2004.
 © COPYRIGHT 2004 By - ALL RIGHTS RESERVED.

LOC	DIST	REVISIONS			
P	LTR	DESCRIPTION	DATE	DWN	APVD
J		R2	REVISED PER ECO-11-005033	26MAR11	RK HMR



1. MATERIAL ; HOUSING: HIGH TEMPERATURE THERMO PLASTIC UL94V-0
 CONTACT: COPPER ARROY
 LOCK LEVER: STAINLESS STEEL
 △ FINISH ; CONTACT AREA: GOLD FLASH ON 0.0013MIN ALL OVER NICKEL.
 SOLDERING AREA: GOLD FLASH PLATING.
 LOCK LEVER: TIN PLATING.
 △ MECHANICAL KEY POSITION OF CONNECTOR SEE TABLE.
 △ TOLERANCES NON-CUMULATIVE.
 △ NOT TO SCALE.
 △ INSIDE MUST BE RESIST COAT EXCEPT SOLDER PATTERN.
 △ COPLANARITY : 0.1 MAX.
 △ THE FLOATING VALUE BY SOLDERING IS NOT INCLUDED.
 △ NOT MATING CONDITION.
 10 IF THE MODULE DOESN'T INSERT AND LOCK IN THE LATCH SMOOTHLY,
 OPEN WIDE BOTH LATCHES BY MANUAL. AND THE MODULE MUST BE
 INSERTED AND LOCKED IN THE LATCH.
 △ FINISH ; CONTACT AREA: GOLD 0.00076MIN PLATING ON 0.0013MIN ALL
 OVER NICKEL.
 SOLDERING AREA: GOLD FLASH PLATING.
 LOCK LEVER: TIN PLATING.



FINISH	TOOLING STATUS	REMARK	POWER SUPPLY	D5	D4	D3	POS	PART NO.
△	AVAILABLE	POROSITY SEALING	1.8V (DDR2)	17.55	2.4	2.7	200	1827236-6
△	AVAILABLE	CUT OUT OF POROSITY SEALING	1.8V (DDR2)	17.55	2.4	2.7	200	3-1827236-4
△	AVAILABLE	CUT OUT OF HOUSING TAIL	1.8V (DDR2)	17.55	2.4	2.7	200	2-1827236-4
△	AVAILABLE	BOSSLESS	1.8V (DDR2)	17.55	2.4	2.7	200	1-1827236-4
△	AVAILABLE	— —	1.8V (DDR2)	17.55	2.4	2.7	200	1827236-4
— —	NOT AVAILABLE	— —	2.5V (DDR1)	18.45	1.5	1.8	200	—

THIS DRAWING IS A CONTROLLED DOCUMENT.

DWN	B.WONG	20JUL10
CHK	C.WANG	20JUL10
APVD	S.YAO	20JUL10

PRODUCT SPEC: 108-5701
 APPLICATION SPEC

STE TE Connectivity

NAME: DDR1 & DDR2 SODIMM SOCKET 0.6mm PITCH
 200POS LOW PROFILE STANDARD TYPE
 (LATCH DIRECT SOLDERING TYPE)

SIZE	CAGE CODE	DRAWING NO	RESTRICTED TO
A3	00779	C-1827236	

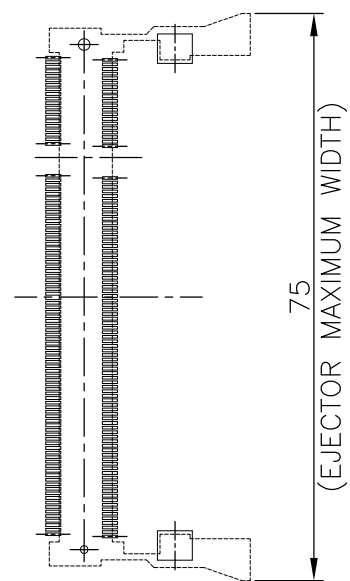
WEIGHT: 3.2 g

CUSTOMER DRAWING

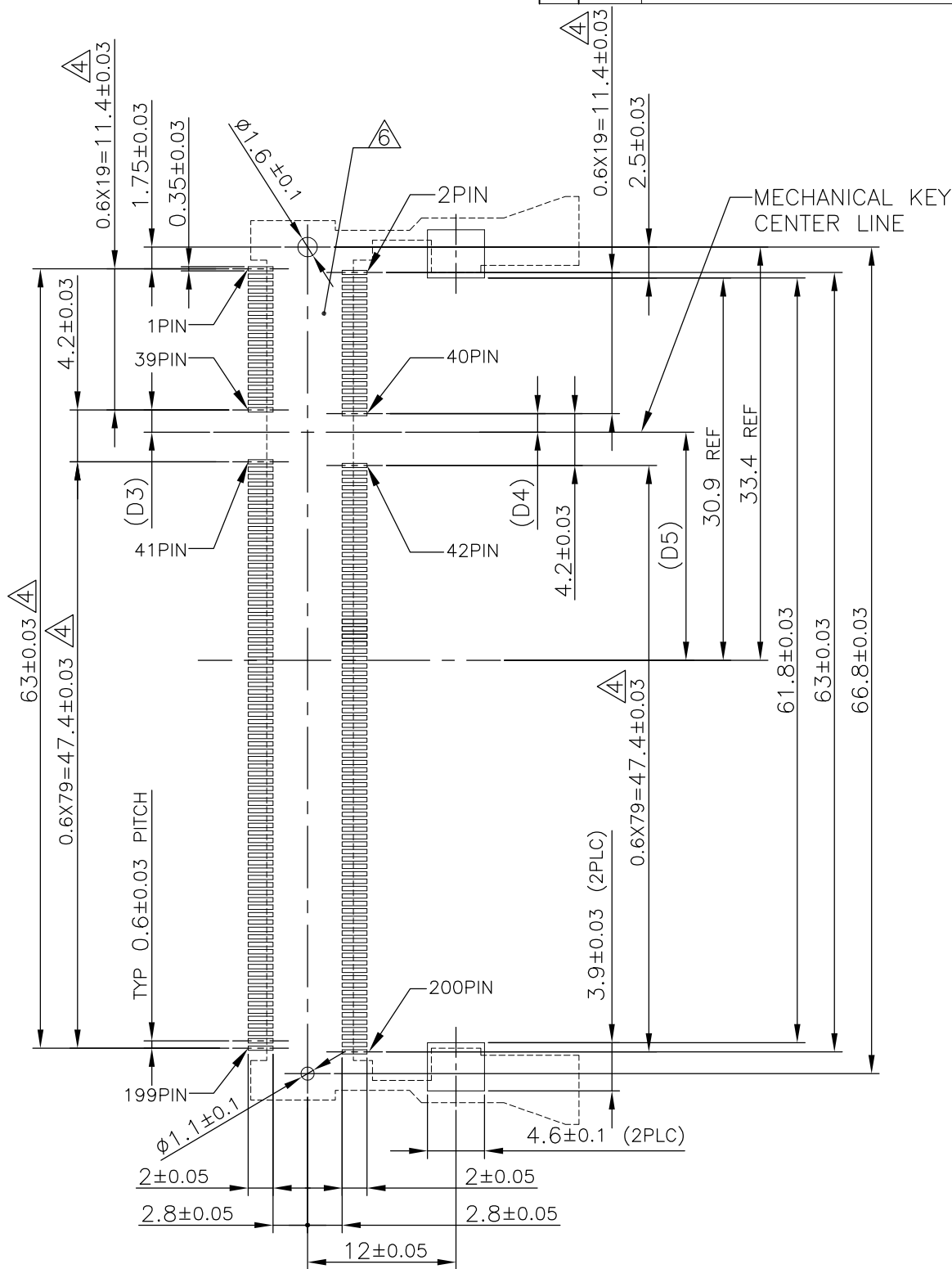
SCALE: 2:1 SHEET: 1 OF 4 REV: R2

LOC	DIST	REVISIONS		
J		P	LTR	DESCRIPTION
		-	-	SEE SHEET 1
				DATE
				DWN
				APVD

REFERENCE P.C. BOARD PATTERN LAYOUT (CONNECTOR MOUNTING SIDE)



REAL SIZE (SCALE 1:1)

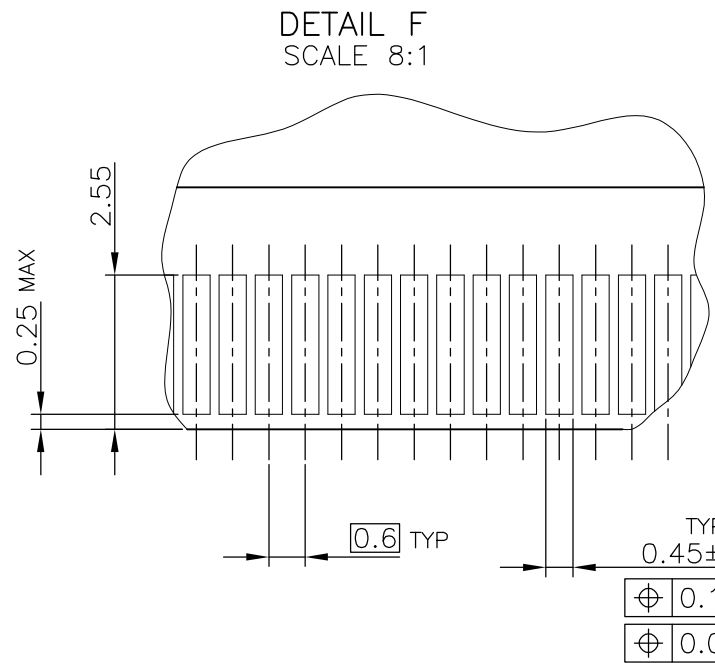
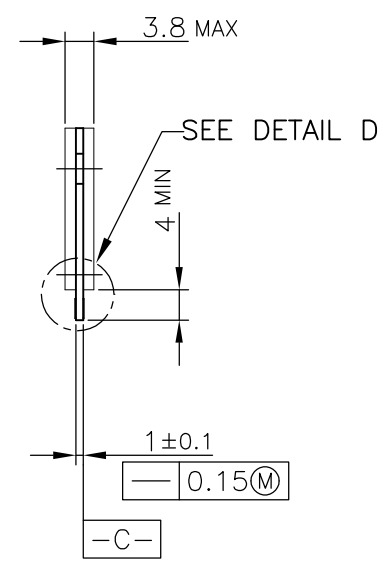
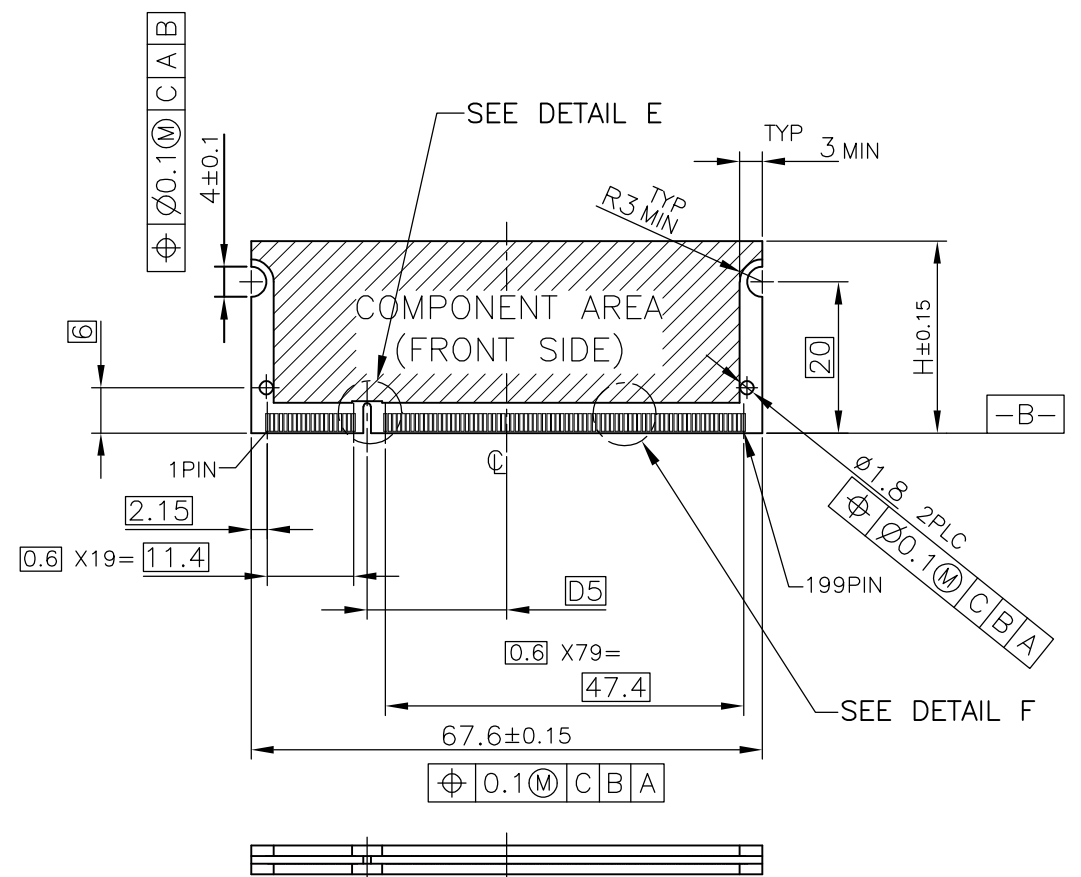


1.8V	17.55	2.4	2.7
2.5V	18.45	1.5	1.8
POWER SUPPLY	D5	D4	D3

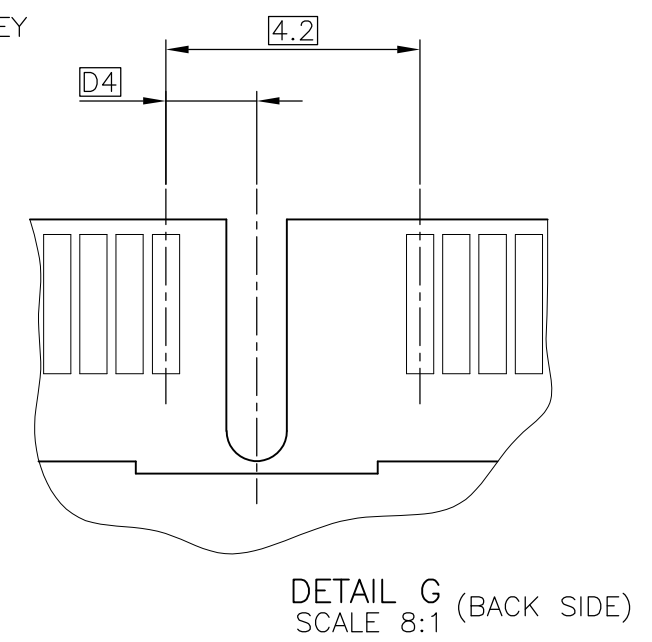
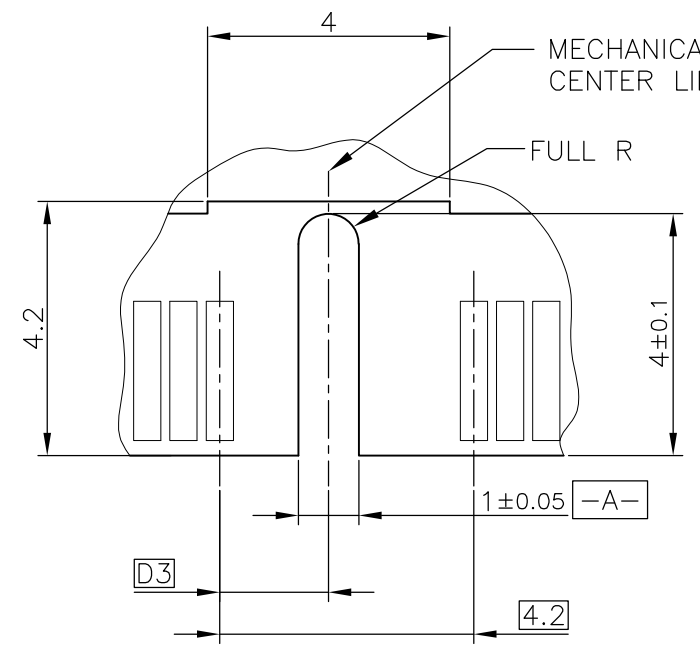
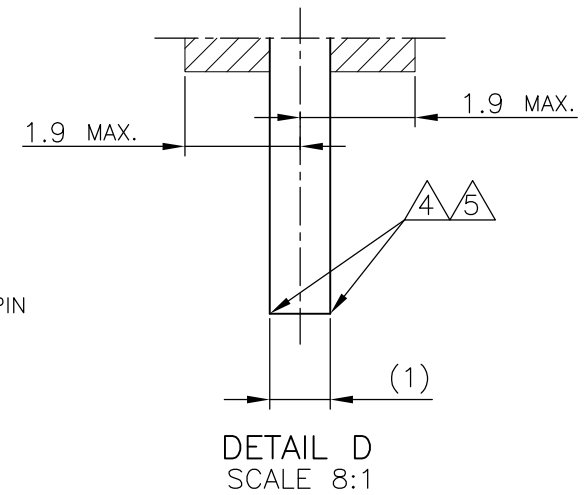
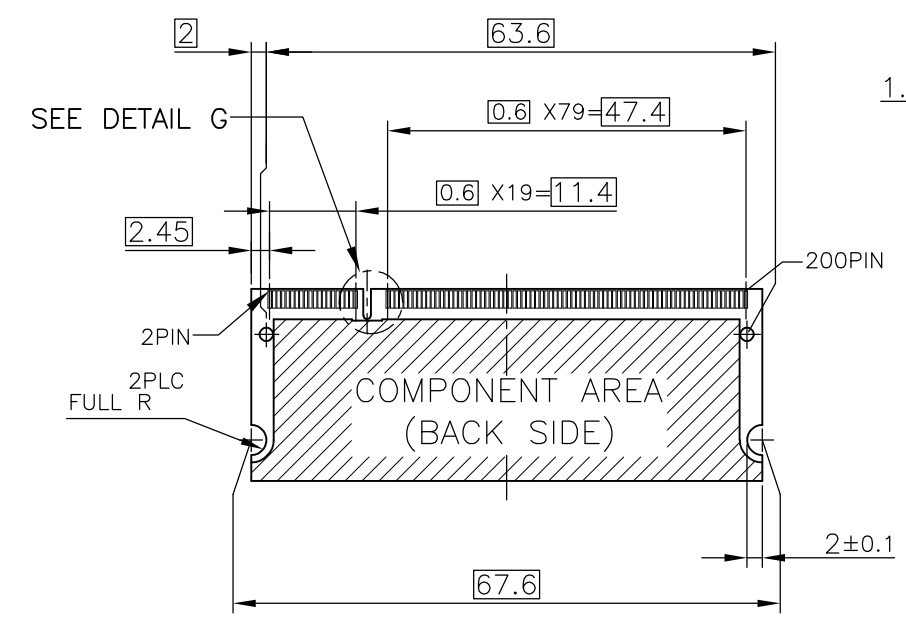
THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN	STE TE Connectivity	
DIMENSIONS: MM		CHK		
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD	NAME	
0 PLC ±		PRODUCT SPEC	DDR1 & DDR2 SODIMM SOCKET 0.6mm PITCH	
1 PLC ±		APPLICATION SPEC	200POS LOW PROFILE STANDARD TYPE	
2 PLC ±		WEIGHT	SIZE	CAGE CODE
3 PLC ±		CUSTOMER DRAWING	A3	00779
4 PLC ±			DRAWING NO	C-1827236
ANGLES ±		SCALE	RESTRICTED TO	
FINISH		2:1	SHEET 2 OF 4	
MATERIAL		REV		R2

THIS DRAWING IS UNPUBLISHED. RELEASED FOR PUBLICATION NOV. 2004. ALL RIGHTS RESERVED.

LOC	DIST	REVISIONS					
J		P	LTR	DESCRIPTION	DATE	DWN	APVD
		-		SEE SHEET 1	-	-	-



- (APPLIED TO SHEET 3)
1. TOLERANCES ON ALL DIMENSIONS ±0.15 UNLESS OTHERWISE SPECIFIED.
 2. P.C.BOARD THICKNESS APPLIES ACROSS TABS AND INCLUDES PLATING AND/OR METALIZATION.
 3. FINISH OF PAD : GOLD PLATING 0.00076 MIN. OVER Ni PLATING 0.002MIN.
- ▲ NO BURR
 △ CHAMFER 0.25 MAX x 45° IF EXIST.



RECOMMENDED MATING P.C.B CONFIGURATION

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN
DIMENSIONS: MM		CHK
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD
0 PLC	±	PRODUCT SPEC
1 PLC	±	APPLICATION SPEC
2 PLC	±	WEIGHT
3 PLC	±	CUSTOMER DRAWING
4 PLC	±	
ANGLES	±	
FINISH		

31.75	1.8V	17.55	2.4	2.7
25.4	2.5V	18.45	1.5	1.8
H	POWER SUPPLY	D5	D4	D3

STE TE Connectivity

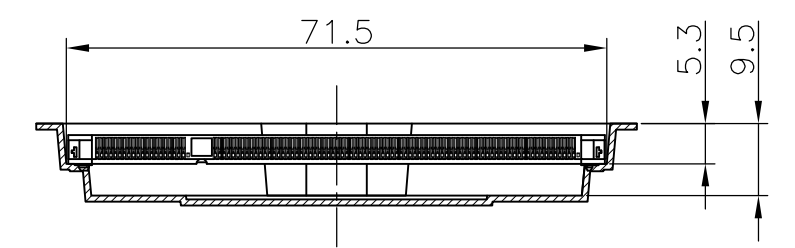
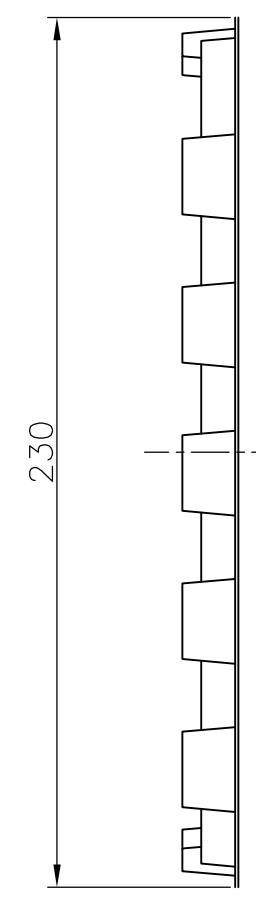
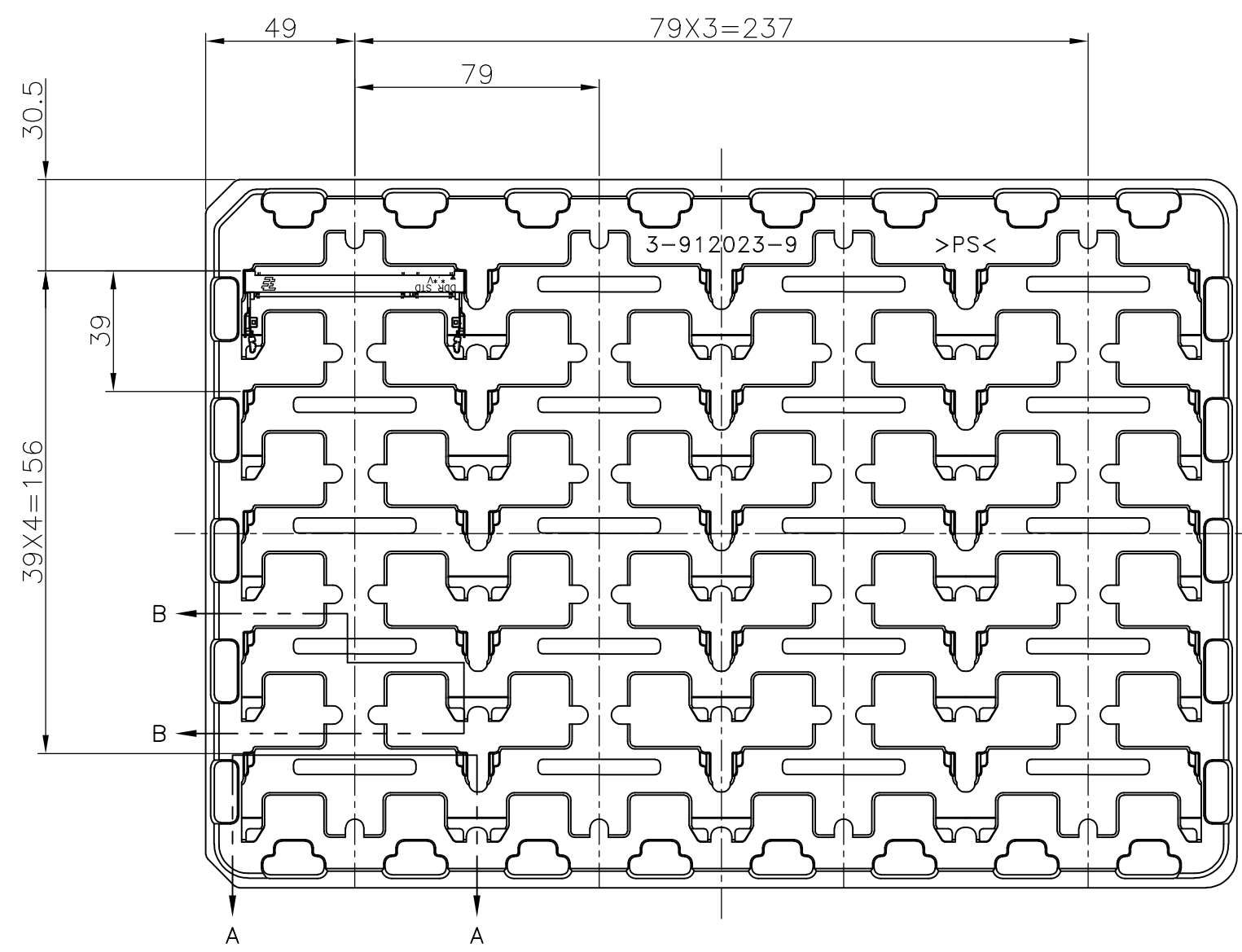
NAME: DDR1 & DDR2 SODIMM SOCKET 0.6mm PITCH 200POS LOW PROFILE STANDARD TYPE (LATCH DIRECT SOLDERING TYPE)

SIZE: A3 CAGE CODE: 00779 DRAWING NO: C-1827236 RESTRICTED TO: []

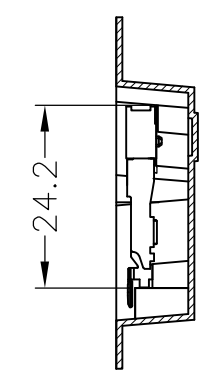
SCALE: 2:1 SHEET: 3 OF 4 REV: R2

THIS DRAWING IS UNPUBLISHED. RELEASED FOR PUBLICATION NOV. 2004.
 © COPYRIGHT 2004 By - ALL RIGHTS RESERVED.

LOC	DIST	REVISIONS			
P	LTR	DESCRIPTION	DATE	DWN	APVD
J		SEE SHEET 1	-	-	-



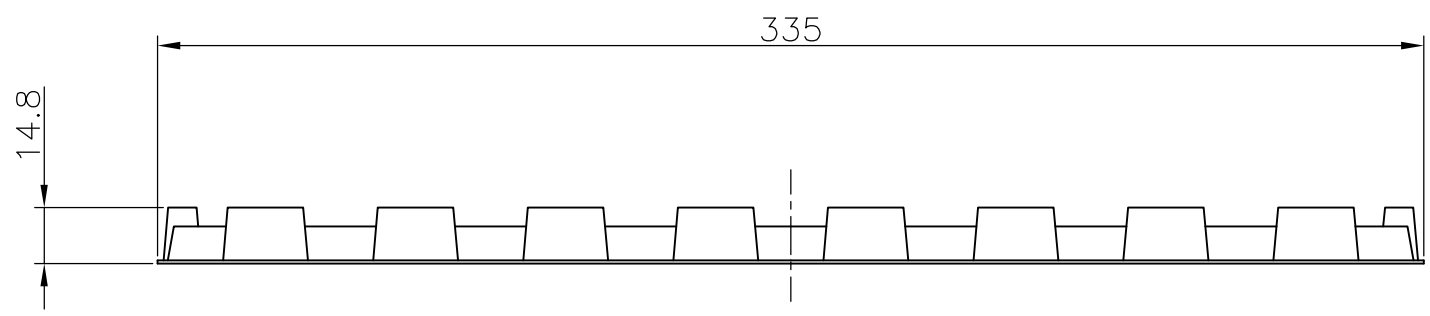
A-A
SCALE 1:1
(180DEG. ROTATE)



B-B
SCALE 1:1

1. THE DIRECTION OF EACH CONNECTOR ON A TRAY IS REFERRED TO THIS DRAWING.
2. GENERAL TOLERANCE : ±0.5

AVAILABLE	20EA/TRAY	1827236-6
AVAILABLE	20EA/TRAY	3-1827236-4
AVAILABLE	20EA/TRAY	2-1827236-4
AVAILABLE	20EA/TRAY	1-1827236-4
AVAILABLE	20EA/TRAY	1827236-4
NOT AVAILABLE	20EA/TRAY	-
TOOLING STATUS	QTY	P/N



THIS DRAWING IS A CONTROLLED DOCUMENT.

DIMENSIONS: MM	TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ± 1 PLC ± 2 PLC ± 3 PLC ± 4 PLC ± ANGLES ± FINISH	DWN CHK APVD PRODUCT SPEC APPLICATION SPEC WEIGHT	TE Connectivity NAME SEMI-HARD TRAY ASSY DDR1 & DDR2 SODIMM SOCKET 200P LOW PROFILE STANDARD TYPE (LATCH DIRECT SOLDERING TYPE)		
MATERIAL			SIZE A3	CAGE CODE 00779	DRAWING NO C-1827236
CUSTOMER DRAWING			SCALE 1:2	SHEET 4 OF 4	REV R2

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for [DIMM Connectors](#) category:

Click to view products by [TE Connectivity](#) manufacturer:

Other Similar products are found below :

[1-1932649-2](#) [1-1761024-0](#) [2309411-5](#) [10116658-252ASLF](#) [5390471-1](#) [2309413-3](#) [78507-0061](#) [KIT-SODIMM-1X16GB/01](#) [10129206-0010013LF](#) [10086891-052TSLF](#) [10079192-11102LF](#) [10116658-152FSLF](#) [10129206-0011113LF](#) [1-2308107-0](#) [2013310-1](#) [2013289-1](#) [390111-1](#) [390112-1](#) [2309413-1](#) [2309407-1](#) [2309414-1](#) [10081530-12208LF](#) [151080-0001](#) [78726-1004](#) [10005639-11107LF](#) [10005639-11109LF](#) [10005639-12207LF](#) [10023061-10207LF](#) [10033853-252FSLF](#) [10033853-052ASLF](#) [10033853-052FSLF](#) [10033853-152FSLF](#) [10033854-052FSLF](#) [10034542-10002LF](#) [10037402-12207LF](#) [10081530-11107LF](#) [10081530-12207LF](#) [10081530-12218LF](#) [10081530-12228LF](#) [10124632-0030001LF](#) [10124806-01006LF](#) [10145226-0241N13LF](#) [59354-052FSLF](#) [59355-052FSLF](#) [DDR4288S0211TF](#) [DDR4288S0511H](#) [DDR4288S0511TF](#) [DDR4288V0123TF](#) [DDR4288V0213TF](#) [DDR502112001K](#)