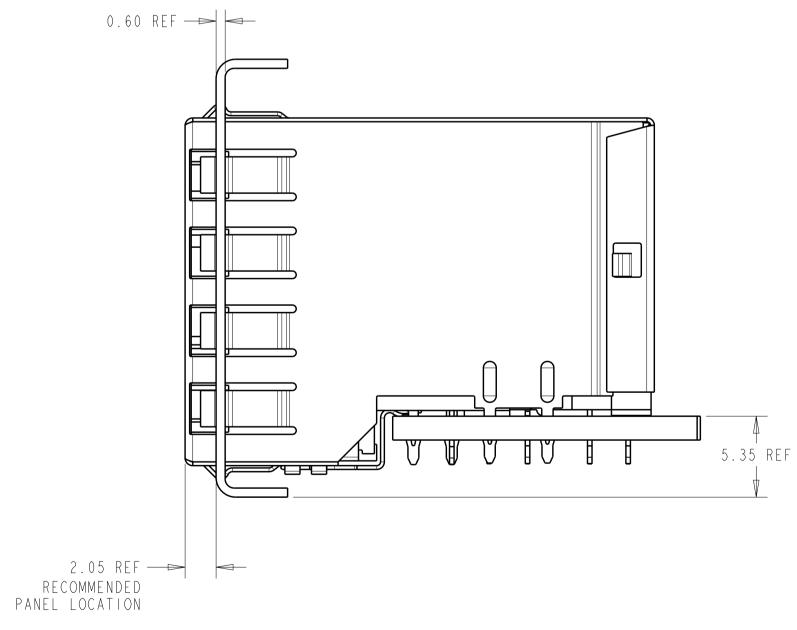


4805 (3/11)



— |3.|8 ——



AMC PANEL AND PRINTED CIRCUIT BOARD ASSEMBLY LAYOUT

	5		4	3	
	GROUND CLIP QUANTITY: 1	-5 , -6 & -7 ONLY			I. MATERIAL: HOUSING PBT POLY TERMINALS 0.33 THI HARD GOL MATTE TI OVER I.2 <u>SHIELD</u> 0.25 THI 0.25 THI 2.03um M <u>LIGHT EMIT</u> DIFFUSED PREPLATE OVER I.0 COPPER UN <u>LED ACTION</u> 0.3 THIC 1.27µm M 2. JACK CAVITY SUBPART F. MAXIMUM PIN <u>4</u> FINISHED P <u>5</u> DATUMS AND <u>6</u> DATE CODE <u>7</u> CONTACTS A <u>8</u> SUGGESTED <u>9</u> PCB THROUG <u>7</u> THE PN I-I
CU	l and It board .Ayout	4 MISSING	CONTACTS	4 MISSING CONTACT	S

\downarrow	15.88	NONE	GREEN/YELLOW	GREEN/YELLOW	NON
	15.88	NONE	GREEN	YELLOW	NON
	15.88	YELLOW	GREEN	GREEN	GREI
	15.88	NONE	GREEN	GREEN	NON
	15.88	NONE	YELLOW	GREEN	NON
	15.88	YELLOW	YELLOW	GREEN	GREI
	15.88	GREEN	GREEN	YELLOW	YELL
	15.88	GREEN	GREEN	GREEN	GREI
	6.13	GREEN	GREEN	GREEN	GREI
	M DIM	LED #4 (LOWER-RIGHT)	LED #3 (UPPER-RIGHT)	LED #2 (UPPER-LEFT)	LED (LOWER-
			1	THIS D	RAWING IS A CONTR

1888653-5,-6 (REPRESENTATION OF -5,-6 SHOWN IN THIS VIEW ONLY)

> DIMENSIONS: mm

aterial SEE NOTE I

	2			1			
LOC	DIST	REVISIONS					
АА	00	Р	LTR	DESCRIPTION	DATE	DWN	APVD
			G	ECO-15-006810	22JAN2016	LL	SH
			Н	ECO-16-010145	JUL20 6	LL	SH
			J	ECO-16-011047	25JUL2016	LL	SH
			K	ECO-17-011372	04AUG2017	LL	SH

LYESTER, BLACK, UL 94V-O.

THICK PHOSPHOR BRONZE PLATED WITH I.27um MINIMUM THICK GOLD IN LOCALIZED AREA AND 2.0um MINIMUM THICK TIN IN ACTION PIN COMPLIANT PIN TERMINAL AREA I.27um MINIMUM THICK NICKEL UNDERPLATE. THICK COPPER ALLOY PLATED WITH 2.03um MINIMUM TIN

I.27 um MINIMUM NICKEL N<u>G CLIP:</u>

HICK COPPER ALLOY PRE-PLATED WITH MINIMUM MATTE TIN OVER 1.27um MINIMUM NICKEL

<u>MITTING DIODE (LED)</u> GED EPOXY LENS, 12.95 CARBON STEEL WIREFRAME LEADS ATED WITH 8.89um THK. TIN/COPPER OVER 2.03um THK. SILVER .02um COPPER OVER 3.56um THK. NICKEL OVER 1.02um THK. UNDERPLATE

<u>ON PIN - COMPLIANT PIN CONTACTS</u> ICK BRASS PLATED WITH 2µm MINIMUM MATTE TIN OVER MINIMUM NICKEL UNDERPLATE.

TY CONFORMS TO FCC RULES AND REGULATIONS, PART 68

IN LENGTH 3.40 FROM TOP SURFACE OF PC BOARD.

PLATED THRU HOLE DIAMETER -ANNULAR RING DIAMETER I.3 TO I.5

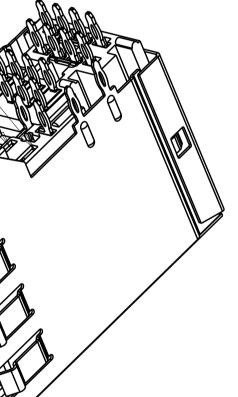
AND BASIC DIMENSIONS ESTABLISHED BY CUSTOMER

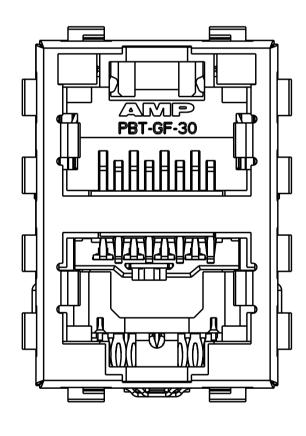
E LOCATED ON REAR OF PART APPROXIMATELY AS SHOWN

ARE NOT INTENDED TO BE ALIGNED WITH EACH OTHER

ED OPERATING CURRENT FOR LEDS IS 20 mA

DUGH HOLES FOR LED #I AND LED #4 ARE NOT REQUIRED FOR 1888653-5,6,1-1888653-1.





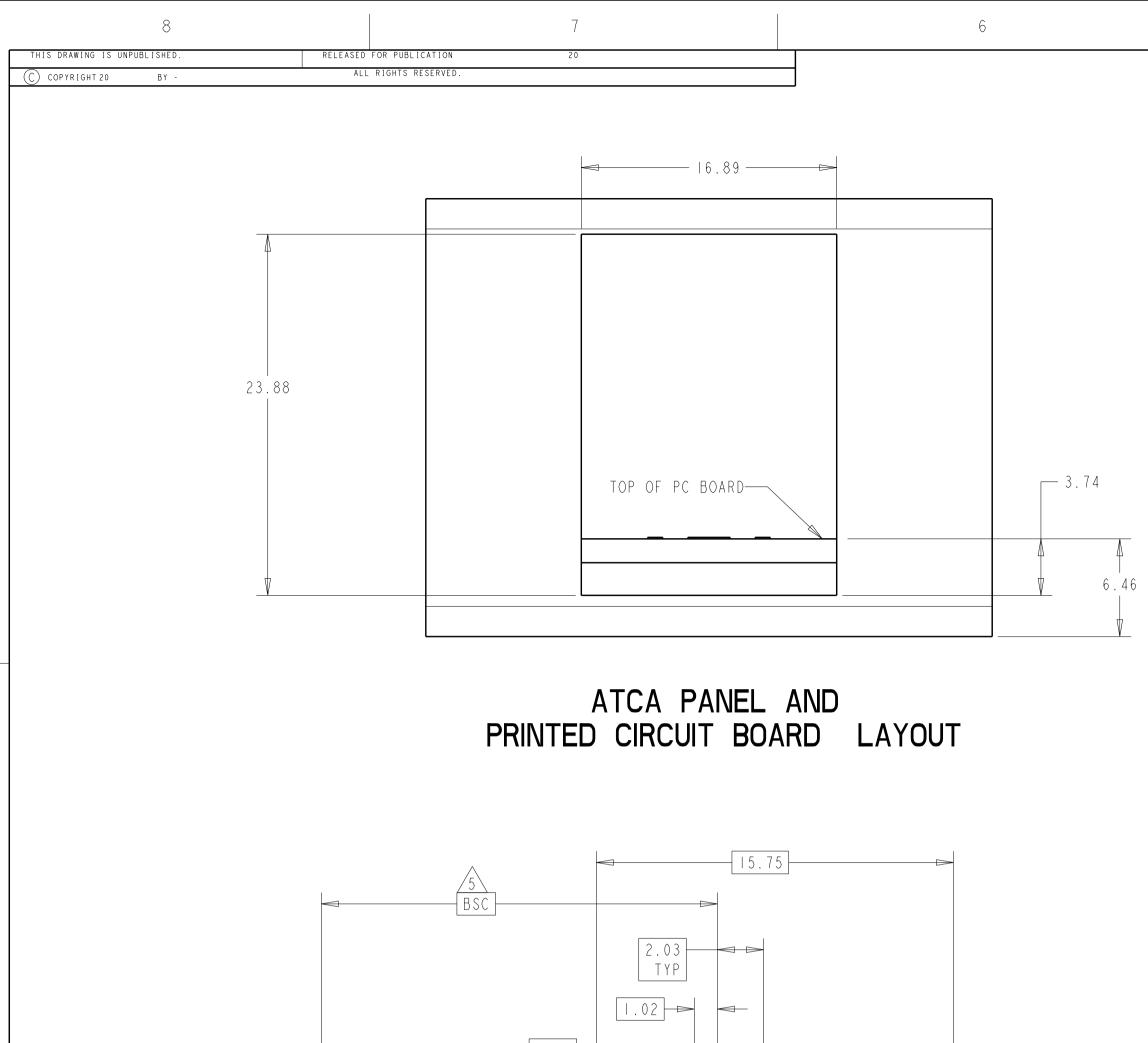
1-1888653-1 SCALE 4:1

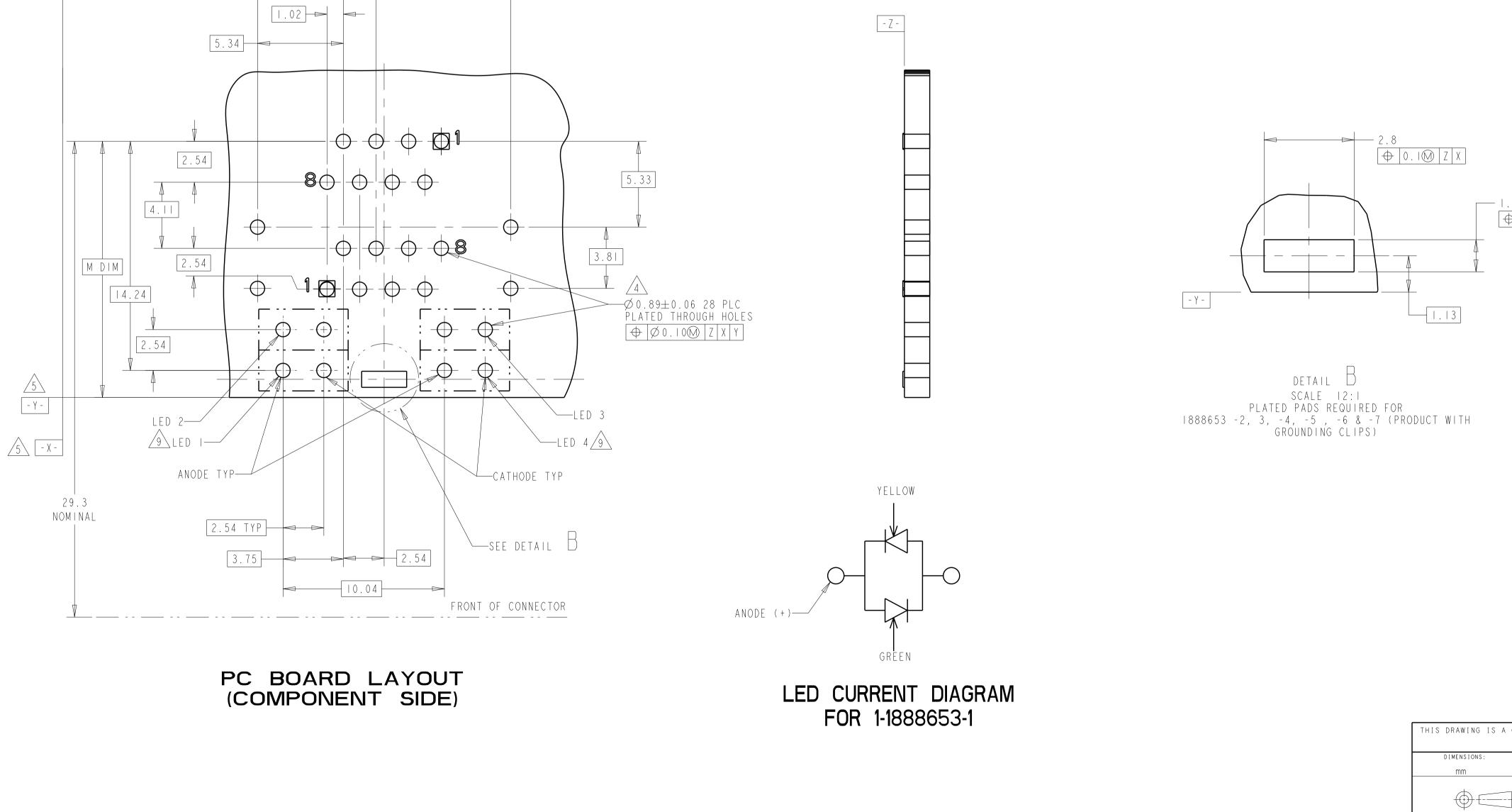
1-1888653-1 SCALE 3: I

> NONE WITH GROUND CLIP |-|888653-| 🏠) N E WITH GROUND CLIP | 888653-9 EEN WITH GROUND CLIP | 888653-7 WITH GROUND CLIP | 888653-6 NE WITH GROUND CLIP 1888653-5 NE ΞΕΝ WITH GROUND CLIP | 888653-4 LOW WITH GROUND CLIP 1888653-3 (SHOWN) WITH GROUND CLIP EEN 1888653-2 WITHOUT GROUND CLIP | 888653 - | EEN PART # | DESCRIPTION -LEFT) NUMBER NTROLLED DOCUMENT. N 190CT2006 ...A.MAYER K 190CT2006 **S**TE TE Connectivity .WESTMAN APVD 190CT2006 S.FLICKINGER 'RODUCT SPEC TOLERANCES UNLESS OTHERWISE SPECIFIED: STACKED MOD JACK ASSEMBLY, 2 X I 8 POSN, RJ45, SHIELDED, PANEL GROUND, OFFSET, CAT 5e 0 PLC 1 PLC 2 PLC 3 PLC 4 PLC ±-±-±0.|3 ±-PPLICATION SPEC IZE CAGE CODE DRAWING NO RESTRICTED ||4-2|60 1 00779 C-1888653 SCALE 4:1 SHEET OF REV K JSTOMER DRAWING

D

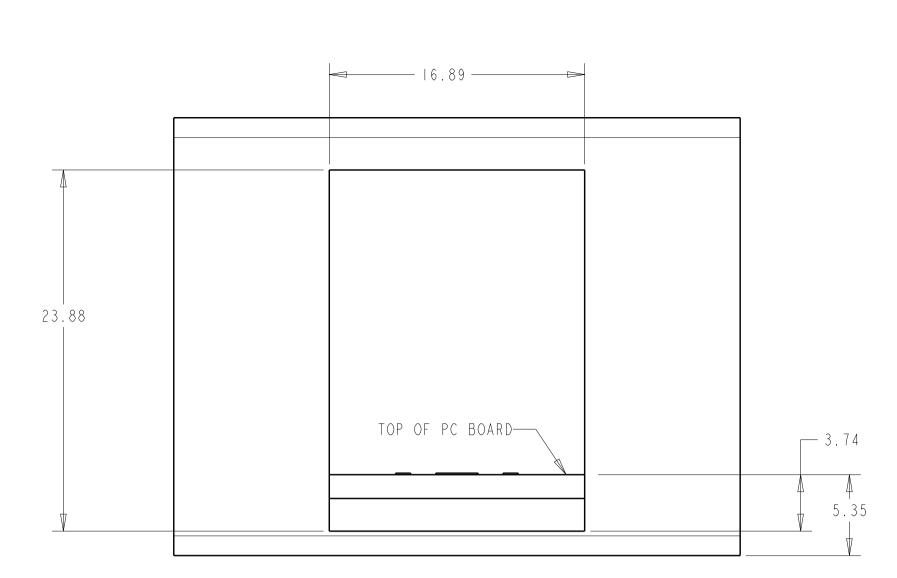
В





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AMC PANEL AND PRINTED CIRCUIT BOARD LAYOUT

SEE NOTE I

		2					1			
LC	C	DIST				REVISIONS				
ļ	ĄА	00	Р	LTR		DESCRIPTION		DATE	DWN	APVD
				-	SEE SHEET I			-	-	-
			-							

── I.0 ⊕ 0.1⊚ Z Y

3

С	ONTROLLED DOCUMENT.	DWN 190СТ2006 L.A.MAYER снк 190СТ2006 J.WESTMAN	TE Connectivity
	TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ±- 1 PLC ±- 2 PLC ±0.13 3 PLC ±-	APVD 190CT2006 S.FLICKINGER PRODUCT SPEC 108-1854 APPLICATION SPEC	NAME STACKED MOD JACK ASSEMBLY, 2 X I, 8 POSN, RJ45, SHIELDED, PANEL GROUND, OFFSET, CAT 5e
	4 PLC ±- ANGLES ±1° FINISH	4 - 2 6 0 WEIGHT _	SIZE CAGE CODE DRAWING NO A 1 00779 C= 1888653 -
	-	CUSTOMER DRAWING	SCALE 6:1 SHEET 2 OF 2 REV K

D

В

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