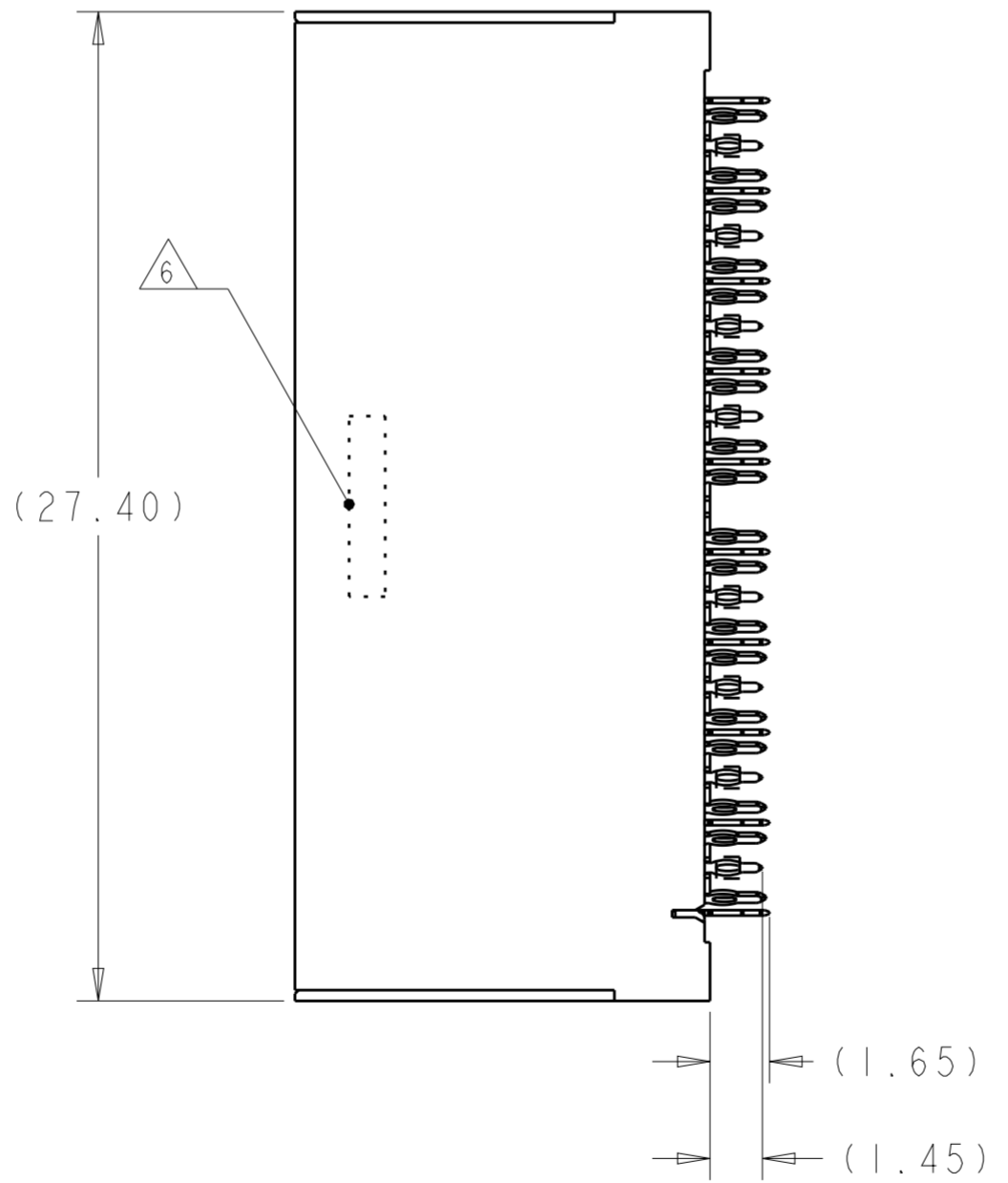
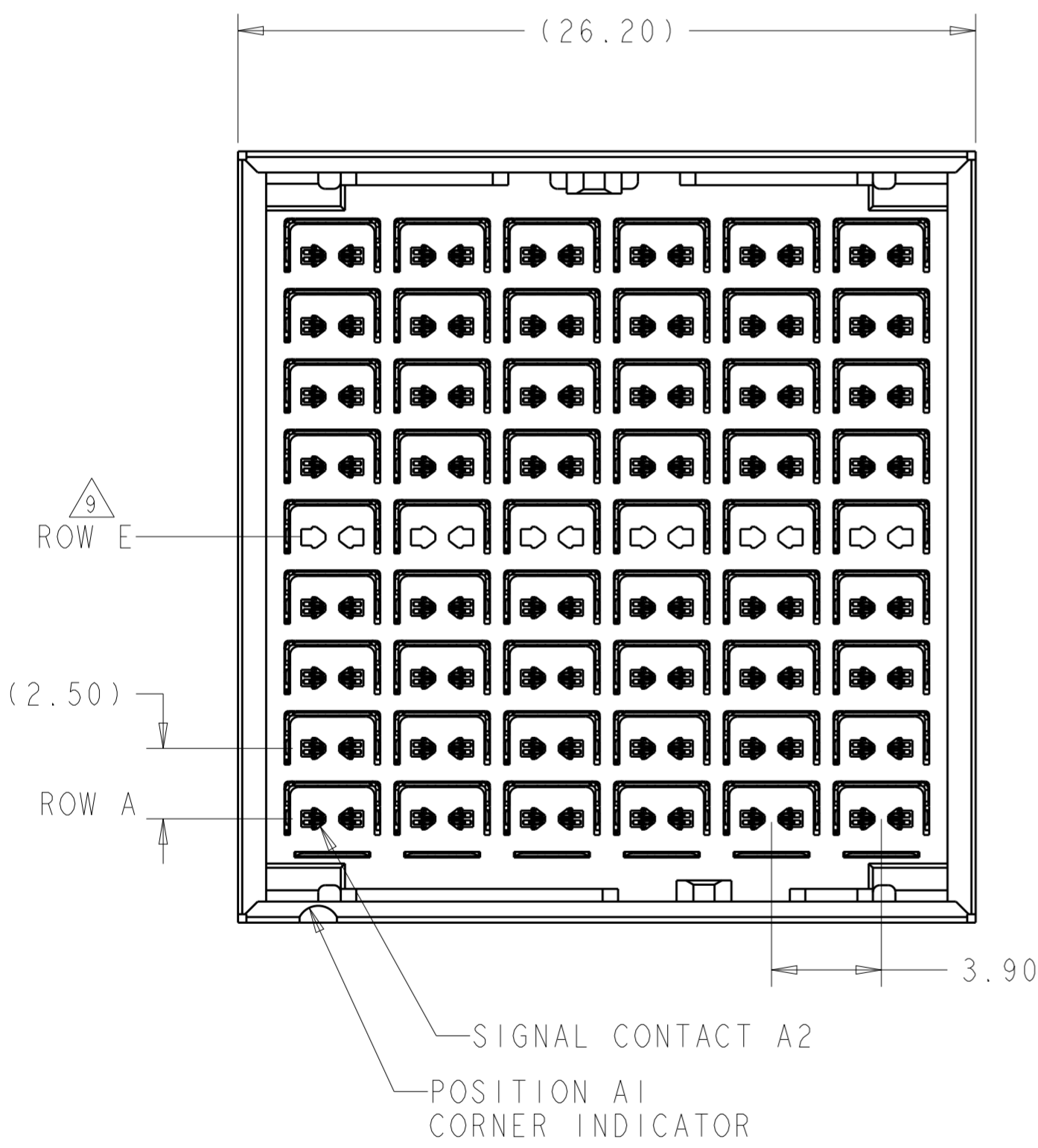
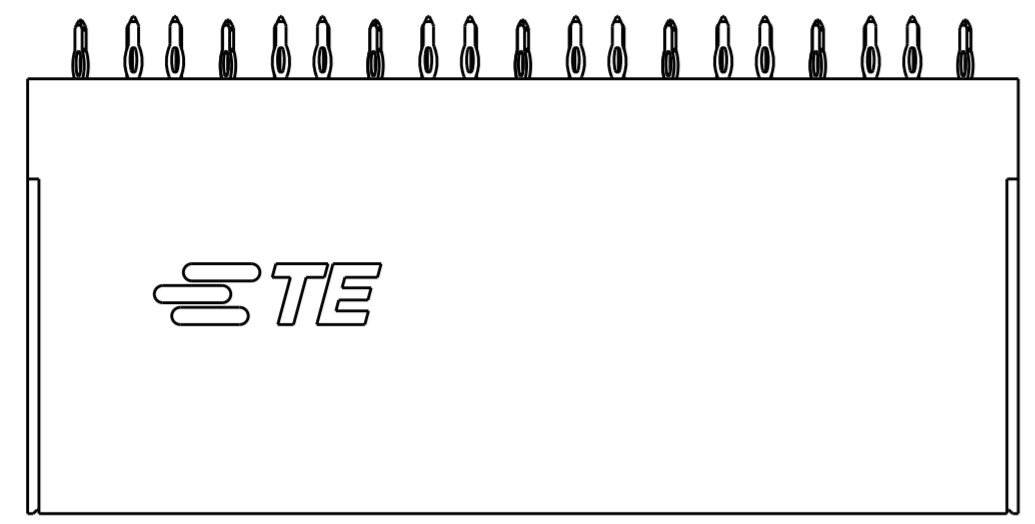


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LOC	DIST	REVISIONS					
GP	00	P	LTR	DESCRIPTION	DATE	DWN	APVD
		A		REVISED PER ECO-16-000464	02FEB2016	AP	RP
		B		REVISED PER ECO-17-003153	14MAR2017	AP	RP



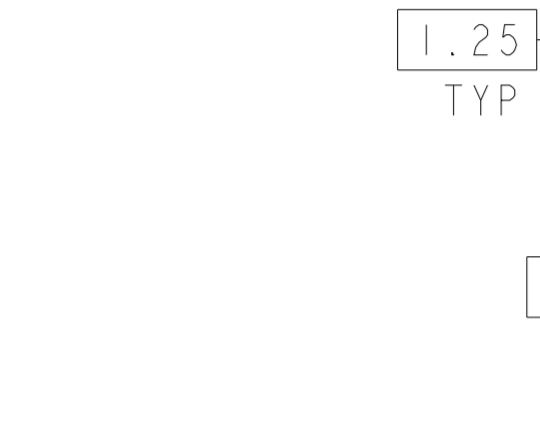
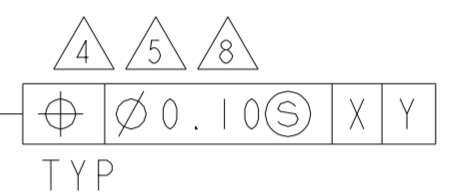
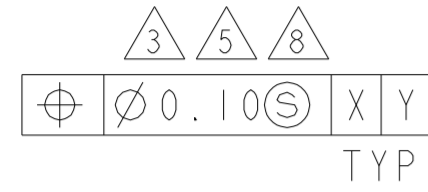
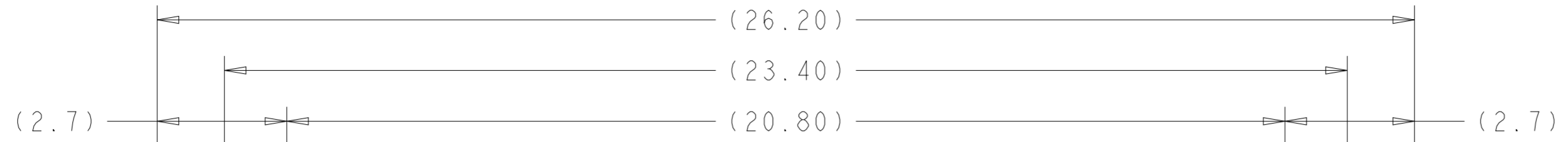
- 1 MATERIAL:
HOUSING: THERMOPLASTIC, UL 94V-0, BLACK.
CONTACTS AND SHIELDS: COPPER ALLOY
- 2 FINISH:
- CONFORMS TO THE REQUIREMENTS OF TE PRODUCT SPECIFICATION 108-32021 BASED ON TELCORDIA GR-1217-CORE FOR SYSTEM QUALITY LEVEL III, APPLICATIONS IN CONTROLLED ENVIRONMENTS (CENTRAL OFFICE).
- COMPLIANT PIN AREA: 0.5µm MIN MATTE TIN.
- 3 SIGNAL PLATED THRU HOLE REQUIREMENTS:
FINISHED HOLE = $\varnothing 0.34 \pm 0.05$
HOLE SIZE PRIOR TO PLATING = $\varnothing 0.420 \pm 0.025$
COPPER THICKNESS = 0.025MIN
IMMERSION TIN IF APPLICABLE = 0.00095 REF
- 4 GROUND AND SI VIA PLATED THRU HOLE REQUIREMENTS:
FINISHED HOLE = $\varnothing 0.37 \pm 0.05$
HOLE SIZE PRIOR TO PLATING = $\varnothing 0.450 \pm 0.025$
COPPER THICKNESS = 0.025 MIN
IMMERSION TIN IF APPLICABLE = 0.00095 REF
- 5 PTH DIMENSIONS APPLY TO THE TOP 1.00mm OF THE PCB THICKNESS FROM THE CONNECTOR MOUNTING SIDE.
- 6 CONNECTOR MARKED WITH PART NUMBER, DATE CODE, AND "100 OHM" IN APPROXIMATE AREA SHOWN.
- 7 DATUMS AND BASIC DIMENSIONS ESTABLISHED BY CUSTOMER.
- 8 APPLIES TO FINISHED HOLE DIAMETER.
- 9 NO SIGNAL CONTACTS IN ROW E.

2187461-1
PART NO

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN W SOURBEER 11SEP2012	TE Connectivity	
		CHK N EVANS 11SEP2012		
DIMENSIONS: mm		APVD -	NAME HEADER ASSEMBLY, 9 PAIR, 6 COLUMN, ROW E SKIPPED, BACKPLANE, 100 OHM, PAIR-IN-ROW, STRADA WHISPER	
TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ± 1 PLC ± 2 PLC ± 3 PLC ± 4 PLC ± ANGLES ± FINISH		PRODUCT SPEC 108-32021	SIZE A2	CAGE CODE 00779
MATERIAL 1 -		APPLICATION SPEC 114-32029	DRAWING NO C-2187461	RESTRICTED TO -
		WEIGHT -	SCALE 5:1 SHEET 1 OF 2 REV B	
		CUSTOMER DRAWING		

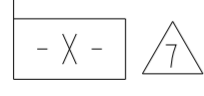
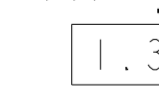
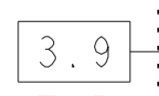
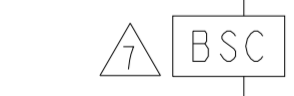
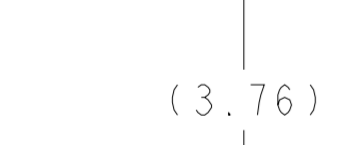
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LOC		DIST		REVISIONS			
P	LTR	DESCRIPTION	DATE	DWN	APVD		
-		SEE SHEET 1					



CONNECTOR OUTLINE

TOP SURFACE OF DAUGHTERCARD



SIGNAL CONTACT A2



RECOMMENDED PCB LAYOUT,
 SINGLE CONNECTOR
 COMPONENT SIDE SHOWN
 SCALE 10:1

- = SIGNAL
- ⊗ = GROUND
- ⊙ = SIGNAL INTEGRITY VIA TO GROUND

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN W. SOURBEER 11SEP2012	TE Connectivity	
		CHK N. EVANS 11SEP2012		
DIMENSIONS: mm		APVD	NAME HEADER ASSEMBLY, 9 PAIR, 6 COLUMN, ROW E SKIPPED, BACKPLANE, 100 OHM, PAIR-IN-ROW, STRADA WHISPER	
TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ± 1 PLC ± 2 PLC ± 3 PLC ± 4 PLC ± ANGLES ± FINISH		PRODUCT SPEC 108-32021	SIZE A2	
MATERIAL material_2		APPLICATION SPEC 114-32029	CAGE CODE 00779	
		WEIGHT	DRAWING NO C-2187461	
		Customer Drawing	RESTRICTED TO	
		SCALE 2:1	SHEET 2 OF 2	
			REV B	

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