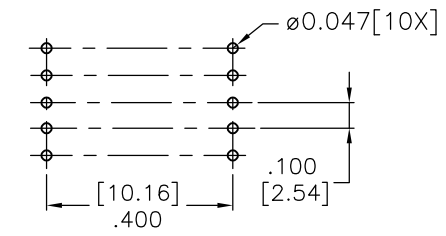
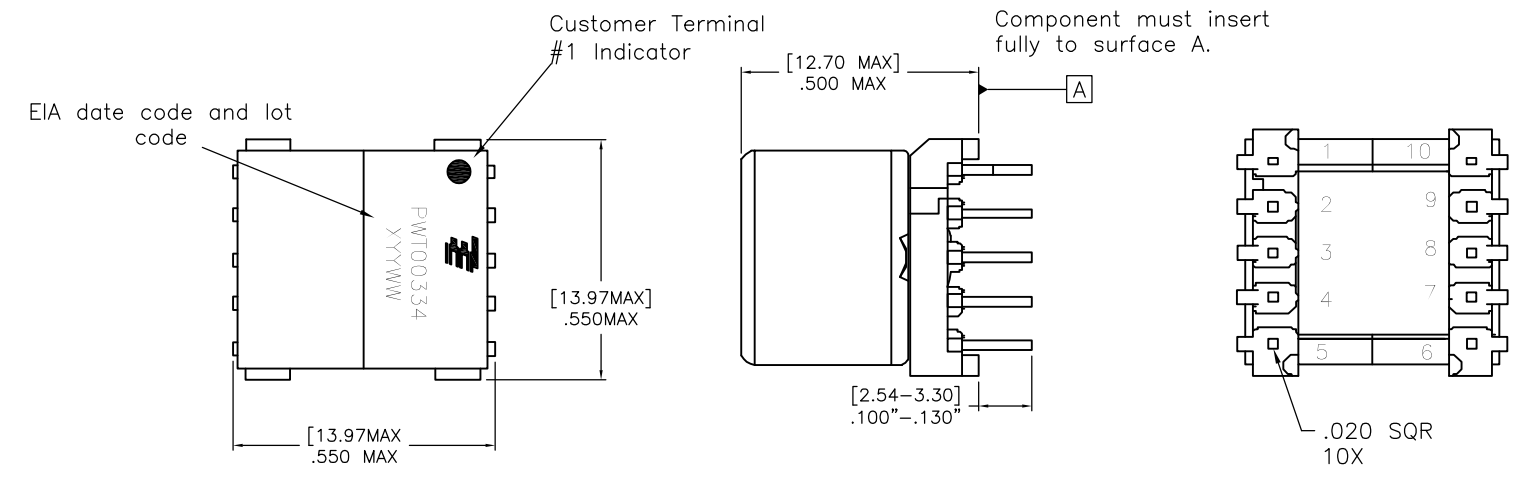


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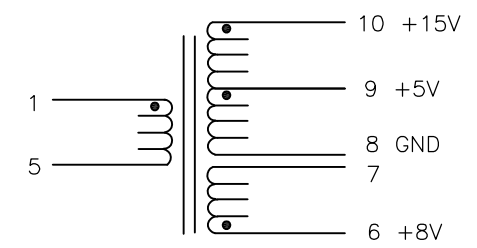
LOC	DIST	REVISIONS					
		P	LTR	DESCRIPTION	DATE	DWN	APVD
E	B		BA	NEW DRAWING	15MAR2011	PSK	JC

Electrical Specifications at +20°C unless noted otherwise

Test Description	Limit	Units	Tol	Notes
Inductance	51.00	μH	±5%	1-5, 100kHz, 100mVAC, Ls.
DC Resistance	0.41	Ω	Max	1-5
DC Resistance	0.18	Ω	Max.	10-9
DC Resistance	0.04	Ω	Max.	9-8
DC Resistance	0.19	Ω	Max.	6-7
Turns Ratio	2.64 : 1.00	--	±1%	(1-5) : (10-9), 100kHz, 100mVAC.
Turns Ratio	4.83 : 1.00	--	±1%	(1-5) : (9-8), 100kHz, 100mVAC.
Turns Ratio	3.22 : 1.00	--	±1%	(1-5) : (6-7), 100kHz, 100mVAC.
Dielectric	1500	VAC	500μA	1-10, 1 second test, 500μA maximum leakage current.
Dielectric	1500	VAC	500μA	1-6, 1 second test, 500μA maximum leakage current.
Dielectric	2500	VAC	500μA	6-core, 1 second test, 500μA maximum leakage current.



Terminal Dimensions
 Customer to determine PCB pad dimensions.



Schematic

NOTES:

1. Operating Temperature Range: -40°C to +85°C
2. Terminal Plating is Hot-dipped SnAgCu over Ni
3. 265°C Maximum wave solder temperature per J-STD-020
4. All dimensions are in inches [millimeters].

RoHS Compliant

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN PRASAD K 15MAR2011	TE Connectivity			
DIMENSIONS: INCHES [MM]		CHK ALLAN P 15MAR2011				
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD J CATCHPOLE 15MAR2011	NAME MGPWT-00334-P			
0 PLC ± 0.5 1 PLC ± 0.2 2 PLC ± 0.1 3 PLC ± - 4 PLC ± - ANGLES ± 5		PRODUCT SPEC -	SIZE A3			
MATERIAL -		FINISH -	APPLICATION SPEC -	CAGE CODE 00779	DRAWING NO C-2-1879391-5	RESTRICTED TO -
CUSTOMER DRAWING			WEIGHT -	SCALE NTS	SHEET 1 OF 1	REV BA

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