

Multi-Channel

Silicon ESD Protector Overvoltage Protection Device

PRODUCT: SESD1004Q4UG-0020-090

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Specification Status: Preliminary

BENEFITS

- Industry-leading lowest capacitance; provides lowest insertion loss for high speed data signals
- Industry's smallest footprint and lowest profile multi-channel ESD array helps to optimize board space
- Flow-through and single connection design helps routing PCB matched impedance high speed data lines
- Helps protect electronic circuits against damage from Electrostatic Discharge (ESD), surge and cable discharge events
- Assists equipment to pass IEC61000-4-2, level 4 testing

FEATURES

- Low capacitance: 0.20 pF (typ)
- Low leakage current: 50nA @ 5V (max)
 Low clamping voltage: +9.18 / -0.8V (typ)
- @ (tp=8x20µs, lpp=2A)
- ESD maximum rating per IEC61000-4-2 standard:
 - ± 20 kV contact discharge
 - ± 20kV air discharge
- Surge: 2A (max) @ (tp=8x20µs) per IEC61000-4-2-5
- Small size and low profile: XDFN array packages 0.31mm height

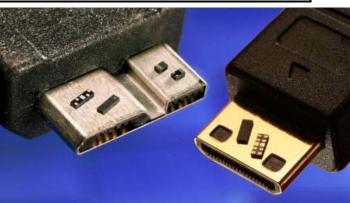
APPLICATIONS

- Consumer, mobile and portable electronics
- Tablet PC and external storage with high speed interfaces
- Ultra-high speed data lines
- USB 3.0/2.0, HDMI 1.3/1.4, DisplayPort, Thunderbolt (Light Peak), V-by-One HS, and LVDS interfaces
- Applications requiring high ESD performance in small DFN packages

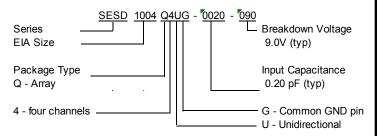
MATERIALS INFORMATION



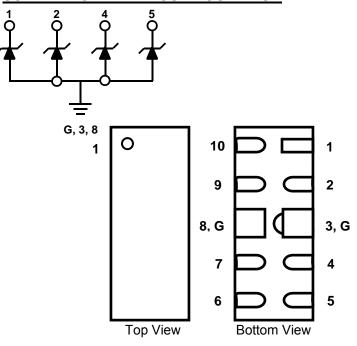
* Halogen Free refers to: Br≤900ppm, Cl≤900ppm, Br+Cl≤1500ppm SESD devices meet MSL-1 Requirements DFN case epoxy meets UL 94 V-0



PART NUMBERING



SCHEMATIC AND PIN CONFIGURATION





DEVICE MAXIMUM RATING

Multi-Channe	
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	hstand ⁽¹⁾ 4-2, level 4)	Temperature		Peak Current (tp=8x20μs)
Contact (kV)	Air (kV)	Operating (°C)	Storage (°C)	lpp (A)
± 20	± 20	-55 to +125	-55 to +150	2.0

⁽¹⁾ 20kV @ ± 1 pulse; 10kV @ ± 50 pulses; 8kV @ 1,000 pulses (under IEC6100-4-2)

• Device maximum rating @ T = 25°C, unless otherwise specified

• Caution: Stress exceeding Device Maximum Ratings may damage the device

Prolonged exposure to stresses above the Recommended Operating Conditions may affect device reliability

DEVICE ELECTRICAL CHARACTERISTICS

	Input Capacitance @ V _R = 0V, f = 3GHz, I/O to GND (pF)		e Reverse Working Voltage (V)		Reverse Leakage Current I _L @ V _{WRV} =5.0V (nA)		Clamping Voltage V _{CL} @ lpp=2.0A (V)
Тур	Maximum	Тур	Min	Max	Тур	Max	Max
0.20	0.22	+9.00 / -0.80	0	+8.00	<5.0	50.0	+9.18 / -0.80

• All device electrical characteristics @ T = 25°C, unless otherwise specified

FIGURE 1. INSERTION LOSS DIAGRAM

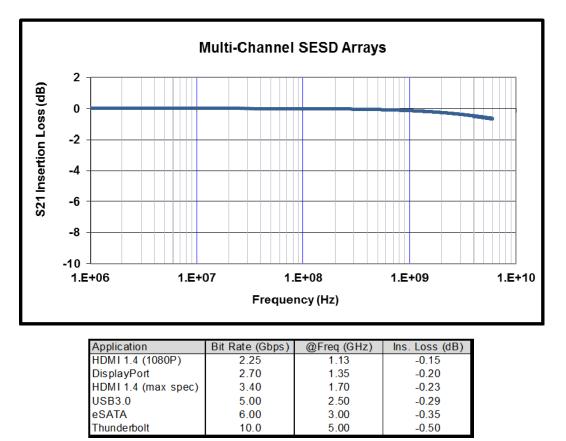




FIGURE 2. DEVICE IV CURVE



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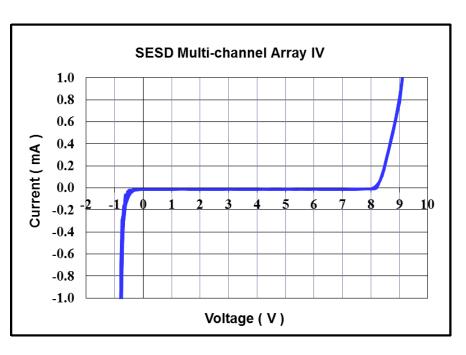
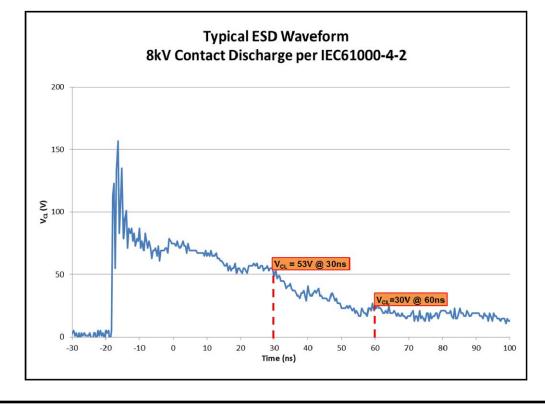


FIGURE 3. ESD WITHSTAND



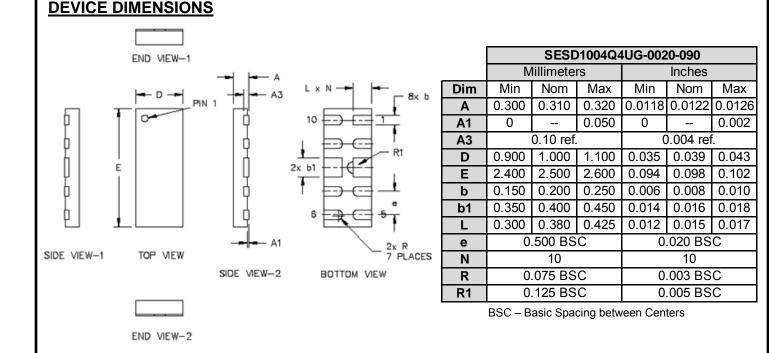


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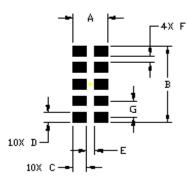
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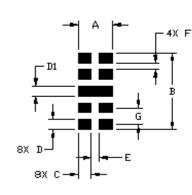
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RECOMMENDED LANDING PATTERN:





SESD Landing Pad Layout							
10 Pin 4-ch Standard FT Array							
Symbol	Millimeters	Inches					
Α	1.00	0.039					
В	2.30	0.091					
С	0.38	0.015					
D	0.30	0.012					
D1	0.30	0.012					
E	0.24	0.009					
F	0.20	0.008					
G	0.50 BSC	0.020 BSC					

BSC - Basic Spacing between Centers

PACKAGING

Packaging	Tape & Reel	Standard Box
SESD1004Q4UG-0020-090	5,000	25,000

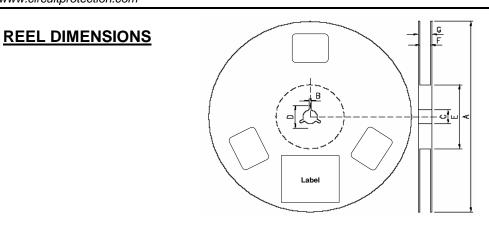


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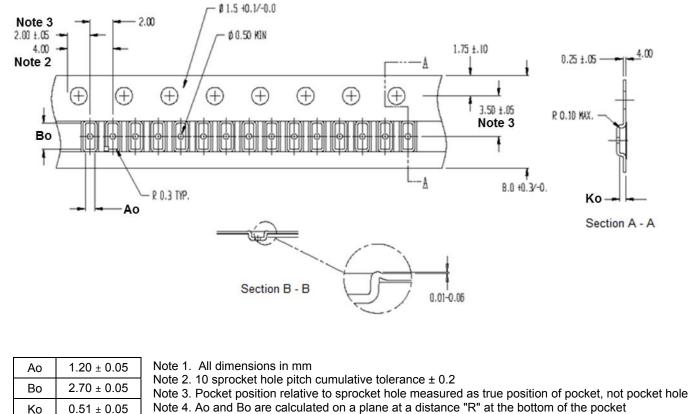
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Dimensions	Α	В	С	D	Е	F	G
(mm)	180.0 ± 1.5	2.3. 0 ± 0.2	13.0 + 0.5 / -0.2	17.3 ± 0.2	60.5 ± 1.5	8.4 +1.5/-0.0	14.4 (max)

CARRIER TAPE DIMENSIONS



Note 5. Tolerances unless noted 1PL \pm 0.20, 2PL \pm 0.10



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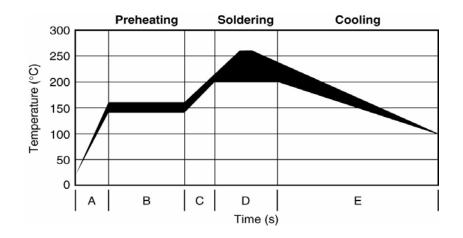
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SOLDER REFLOW RECOMMENDATION

А	Temperature ramp up 1	From ambient to Preheating temperature	30s to 60s
В	Preheating	140°C - 160°C	60s to 120s
С	Temperature ramp up 2	From Preheating to Main heating temperature	20s to 40s
D	Main heating	at 200°C at 220°C at 240°C at 260°C	60s ~ 70s 50s ~ 60s 30s ~ 40s 5s ~ 10s
E	Cooling	From main heating temperature to 100°C	4°C/s (max)

FIGURE 4. REFLOW PROFILE



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