

## 5.5V, 1.5MHz, 1A Synchronous Buck Converter with Power Saving Mode in Light Load

### FEATURES

- High Efficiency: Up to 95%(@3.3V)
- 1.5MHz Constant Frequency Operation
- 1.0A Output Current
- Power Saving Mode in Light Load Condition
- No Schottky Diode Required
- 2.5V to 5.5V Input Voltage Range
- Output Voltage as Low as 0.6V
- 100% Duty Cycle in Dropout
- Low Quiescent Current: 50μA
- Slope Compensated Current Mode Control for Excellent Line and Load Transient Response
- Short Circuit Protection
- Thermal Fault Protection
- Inrush Current Limit and Soft Start
- Input over voltage protection (OVP)
- <1μA Shutdown Current
- DFN2X2-6 Package

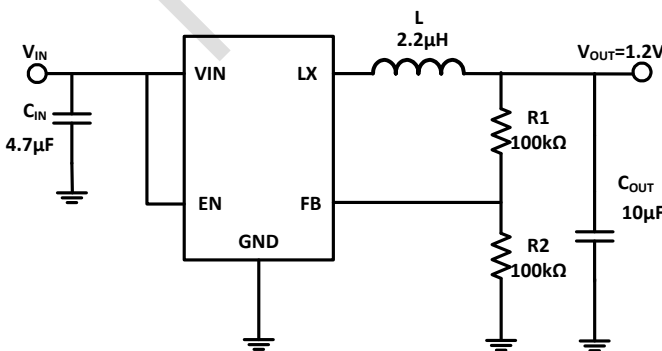
### GENERAL DESCRIPTION

The TM108JD is a constant frequency, current mode PWM step-down converter. The device integrates a main switch and a synchronous rectifier for high efficiency without an external Schottky diode. It is ideal for powering portable equipment that runs from a single cell Lithium-Ion (Li+) battery. The output voltage can be regulated as low as 0.6V. The TM108JD can also run at 100% duty cycle for low dropout operation, extending battery life in portable system. This device offers two operation modes, PWM control and PFM Mode switching control, which allows a high efficiency over the wider range of the load.

### APPLICATIONS

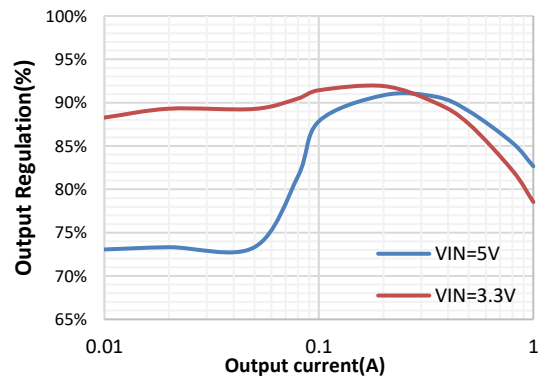
- Cellular and Smart Phones
- Wireless and DSL Modems
- PDA/MID/PAD
- Digital Still and Video Cameras

### TYPICAL APPLICATION



### Efficiency

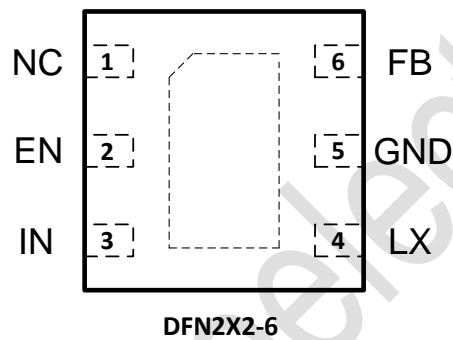
$V_{OUT}=1.2V, I_{OUT}=0.01A \text{ to } 1A, L_{OUT}=2.2\mu H, T_A=25^\circ C$



**ABSOLUTE MAXIMUM RATINGS** (Note 1)

Parameter	Value	Unit
Input Supply Voltages	-0.3~6.5	V
LX Voltages	-0.3~6.5	V
EN, FB Voltage	-0.3~6.5	V
Storage Temperature Range	-65~150	°C
Junction Temperature (Note 2)	-40~150	°C
Power Dissipation	1000	mW
Lead Temperature Soldering,10Sec	260	°C

**PIN CONFIGURATION**



**Top Mark: T80XXX (T80: Device Code, XXX: Inside Code)**

Part Number	Package	Top mark	Quantity/ Reel
TM108JD	DFN2X2-6	T80XXX	3000

TM108JD devices are Pb-free and RoHS compliant.

**PIN FUNCTIONS**

Pin	Name	Function
1	NC	No Connection
2	EN	Chip Enable Pin. Drive EN above 1.5V to turn on the part. Drive EN below 0.3V to turn it off. Do not leave EN floating.
3	VIN	Power Supply Input. Must be closely decoupled to GND with a 4.7 $\mu$ F or greater ceramic capacitor.
4	LX	Power Switch Output. It is the switch node connection to Inductor.
5	GND	Ground Pin.
6	FB	Output Voltage Feedback Pin.

**ESD RATING**

Items	Description	Value	Unit
$V_{ESD\_HBM}$	Human Body Model for all pins	$\pm 2000$	V
$V_{ESD\_CDM}$	Charger Device Model for all pins	$\pm 1000$	V

JEDEC specification JS-001

**RECOMMENDED OPERATING CONDITIONS**

Items	Description	Min	Max	Unit
Voltage Range	IN	2.5	5.5	V
$T_J$	Operating Junction Temperature Range	-40	125	$^{\circ}$ C

**THERMAL RESISTANCE** (Note 3)

Items	Description	Value	Unit
$\theta_{JA}$	Junction-to-ambient thermal resistance	156	$^{\circ}$ C/W

## ELECTRICAL CHARACTERISTICS

(V<sub>IN</sub>=V<sub>EN</sub>=3.6V, V<sub>OUT</sub>=1.8V, T<sub>A</sub> = 25°C, unless otherwise noted.)

Parameter	Test Conditions	Min	Typ	Max	Unit
Input Voltage Range		2.5		5.5	V
OVP Threshold		5.8	6.0	6.3	V
UVLO Threshold			2.1		V
Quiescent Current	V <sub>EN</sub> =2.0V, I <sub>OUT</sub> =0, V <sub>FB</sub> =V <sub>REF</sub> *105%		50	65	μA
Shutdown Current	V <sub>EN</sub> =0V		0.1	10	μA
Regulated Feedback Voltage	T <sub>A</sub> = 25°C	0.588	0.600	0.612	V
Reference Voltage Line Regulation	V <sub>IN</sub> = 2.5V to 6.0V		0.04	0.40	%/V
Output Voltage Line Regulation	V <sub>IN</sub> = 2.5V to 6.0V		0.04	0.4	%
Output Voltage Load Regulation			0.5		%
Oscillation Frequency	V <sub>OUT</sub> =100%		1.5		MHz
	V <sub>OUT</sub> =0V		400		kHz
On Resistance of PMOS	I <sub>LX</sub> =100mA		0.32		Ω
On Resistance of NMOS	I <sub>LX</sub> =-100mA		0.21		Ω
Peak Current Limit	V <sub>IN</sub> =5V, V <sub>OUT</sub> =1.2V, L=4.7μH/2A	1.5		2.2	A
EN Input Low Level				0.3	V
EN Input High Level		1.5			V
EN Leakage Current			±0.01	±1.0	μA
LX Leakage Current	V <sub>EN</sub> =0V, V <sub>IN</sub> =V <sub>LX</sub> =5V		±0.01	±1.0	μA
Thermal Shutdown Threshold (Note 4)			150		°C
Thermal Shutdown Hysteresis (Note 4)			25		°C

**Note 1:** Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

**Note 2:** T<sub>J</sub> is calculated from the ambient temperature T<sub>A</sub> and power dissipation P<sub>D</sub> according to the following formula: T<sub>J</sub> = T<sub>A</sub> + (P<sub>D</sub>) × θ<sub>JA</sub>.

**Note 3:** Measured on JESD51-7, 4-layer PCB.

**Note 4:** Guaranteed by design.

**FUNCTION DESCRIPTION**

The TM108JD is a high performance 1.0A, 1.5MHz monolithic step-down converter. The TM108JD requires only three external power components ( $C_{in}$ ,  $C_{out}$  and L). The adjustable version can be programmed with external feedback to any voltage, ranging from 0.6V to the input voltage.

At dropout, the converter duty cycle increases to 100% and the output voltage tracks the input voltage minus the  $R_{DS(ON)}$  drop of the high-side MOSFET.

The internal error amplifier and compensation provides excellent transient response, load, and line regulation. Soft start function prevents input inrush current and output overshoot during start up.

**FUNCTIONAL BLOCK DIAGRAM**

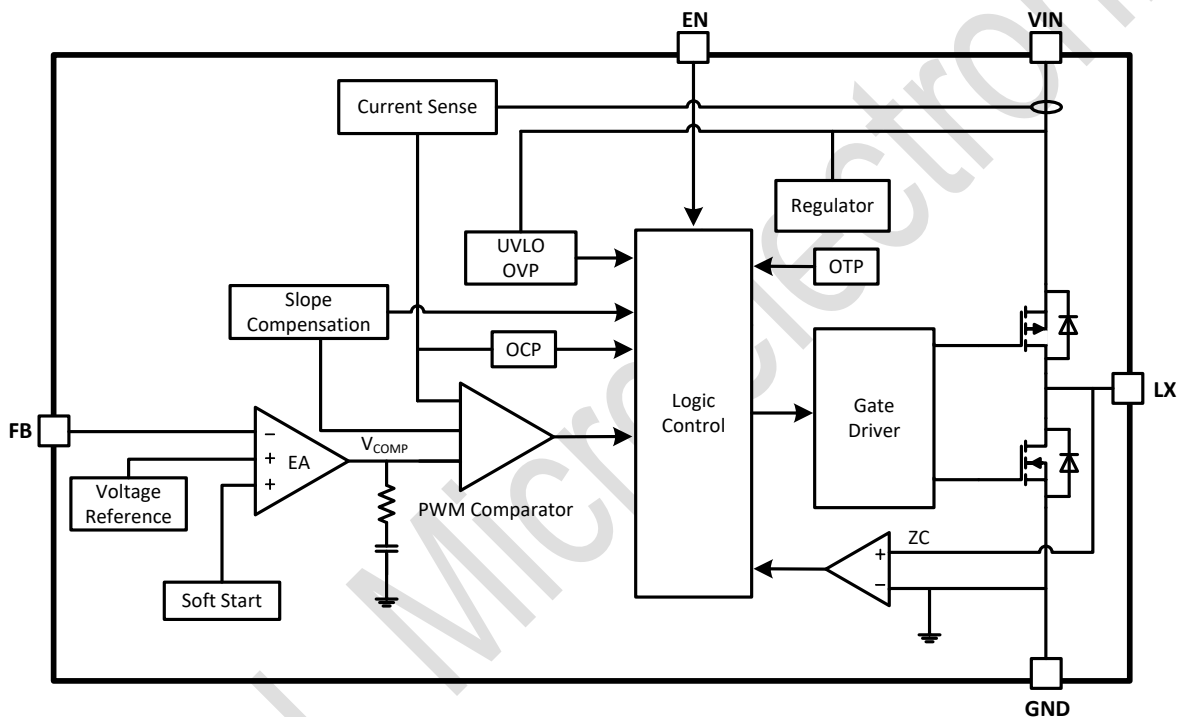


Figure 2. TM108JD Block Diagram

## APPLICATION INFORMATION

### Setting the Output Voltage

Figure 1 shows the basic application circuit for the TM108JD. The TM108JD can be externally programmed. Resistors R1 and R2 in Figure 1 program the output to regulate at a voltage higher than 0.6V. The external resistor sets the output voltage according to the following equation:

$$V_{OUT} = 0.6 \times \left(1 + \frac{R_1}{R_2}\right)$$

$$R_1 = (V_{OUT} / 0.6 - 1) \times R_2$$

### Inductor Selection

For most designs, 2.2μH inductance can satisfy most application conditions. Inductance value is related to inductor ripple current value, input voltage, output voltage setting and switching frequency. The inductor value can be derived from the following equation:

$$L = \frac{V_{OUT} \times (V_{IN} - V_{OUT})}{V_{IN} \times \Delta I_L \times f_{OSC}}$$

Where  $\Delta I_L$  is inductor ripple current. Large value inductors result in lower ripple current and small value inductors result in high ripple current, so inductor value has effect on output voltage ripple value. DC resistance of inductor which has impact on efficiency of DC/DC converter should be taken into account when selecting the inductor.

### Input Capacitor Selection

The input capacitor reduces the surge current drawn from the input and switching noise from the device. The input capacitor impedance at the switching frequency should be less than input source impedance to prevent high frequency switching current passing to the input.

A low ESR input capacitor sized for maximum RMS current must be used. Ceramic capacitors with X5R or X7R dielectrics are highly recommended because of their low ESR and small temperature coefficients.

A 4.7μF ceramic capacitor for most applications is sufficient. A large value may be used for improved input voltage filtering.

### Output Capacitor Selection

The output capacitor is required to keep the output voltage ripple small and to ensure regulation loop stability. The output capacitor must have low impedance at the switching frequency. Ceramic capacitors with X5R or X7R dielectrics are recommended due to their low ESR and high ripple current ratings. The output ripple  $V_{OUT}$  is determined by:

$$\Delta V_{OUT} \leq \frac{V_{OUT} \times (V_{IN} - V_{OUT})}{V_{IN} \times f_{OSC} \times L} \times \left( ESR + \frac{1}{8 \times f_{osc} \times C3} \right)$$

A 10μF ceramic can satisfy most applications.

### Layout Consideration

When laying out the printed circuit board, the Following checking should be used to ensure proper operation of the TM108JD. Check the following in your layout:

1. The power traces, consisting of the GND trace, the LX trace and the VIN trace should be kept short, direct and wide.
2. Does the (+) plates of Cin connect to Vin as closely as possible. This capacitor provides the AC current to the internal power MOSFETs.
3. Keep the switching node, LX, away from the sensitive VOUT node.
4. Keep the (-) plates of Cin and Cout as close as possible

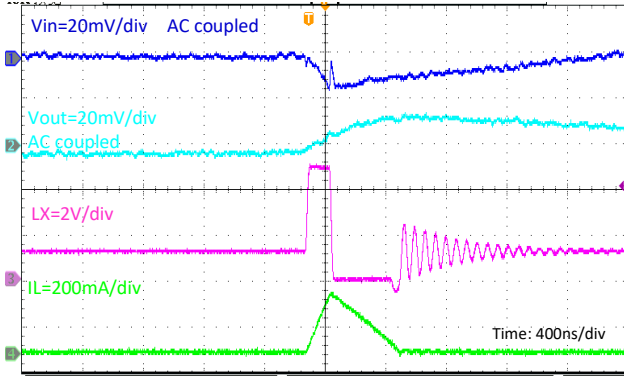
TOLL Microelectronic

**TYPICAL PERFORMANCE CHARACTERISTICS**

Test condition:  $V_{IN}=5V$ ,  $V_{OUT}=1.2V$ ,  $L=2.2\mu H$ ,  $T_A=25^\circ C$ , unless other noted.

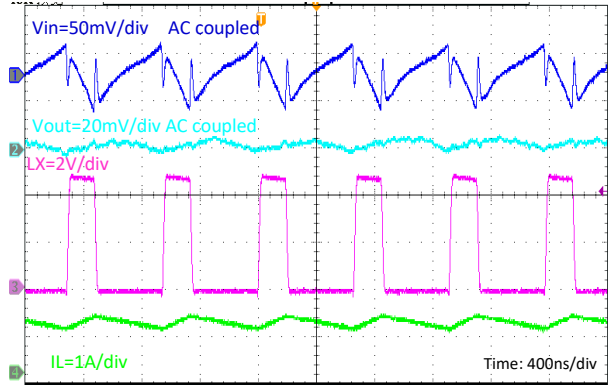
**Steady State Operation**

$V_{IN}=5V$ ,  $V_{OUT}=1.2V$ , No Load



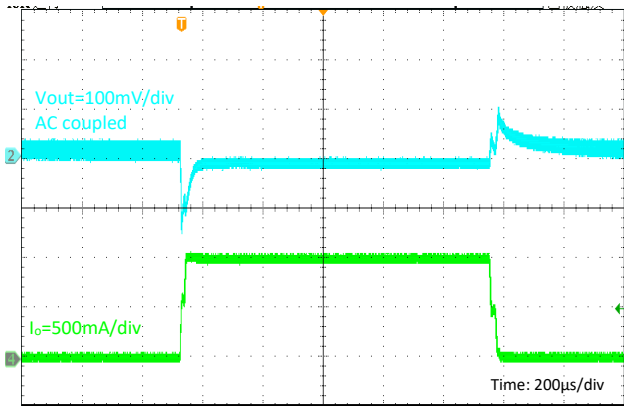
**Steady State Operation**

$V_{IN}=5V$ ,  $V_{OUT}=1.2V$ ,  $I_o=1A$



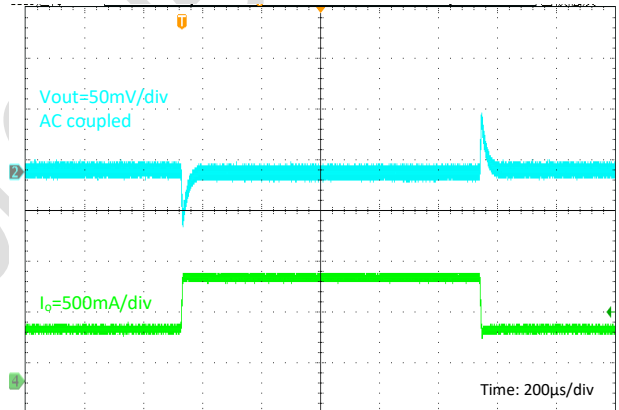
**Load Transient**

$V_{IN}=5V$ ,  $V_{OUT}=1.2V$ ,  $I_o=0A$  to  $1A$



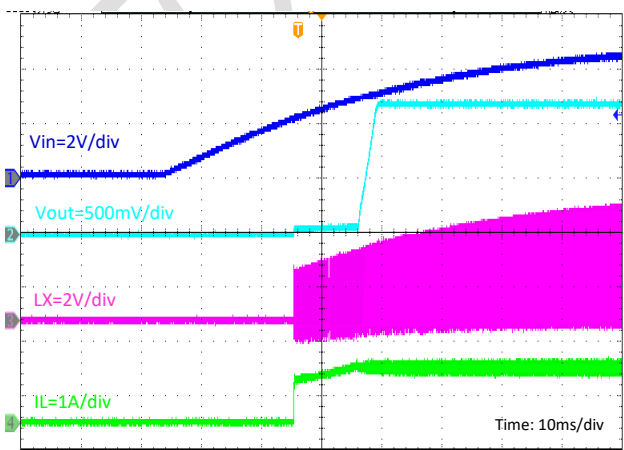
**Load Transient**

$V_{IN}=5V$ ,  $V_{OUT}=1.2V$ ,  $I_o=0.5A$  to  $1A$



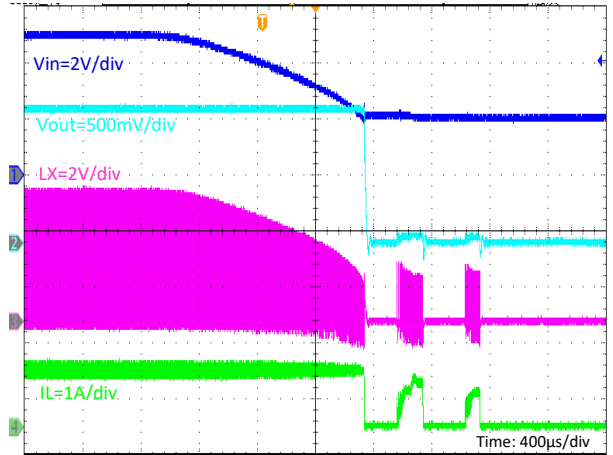
**Input Power On**

$V_{IN}=5V$ ,  $V_{OUT}=1.2V$ ,  $I_o=1A$



**Input Power Down**

$V_{IN}=5V$ ,  $V_{OUT}=1.2V$ ,  $I_o=1A$

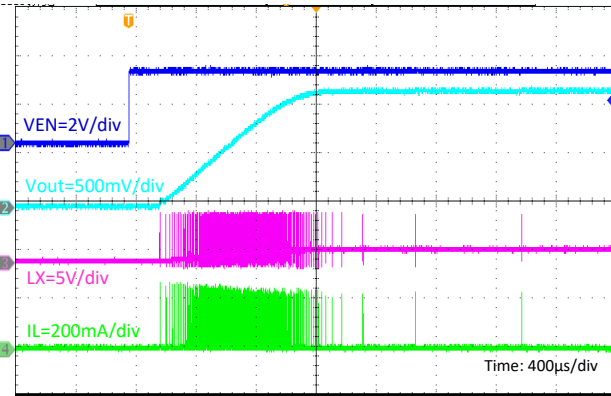




TYPICAL PERFORMANCE CHARACTERISTICS (continued)

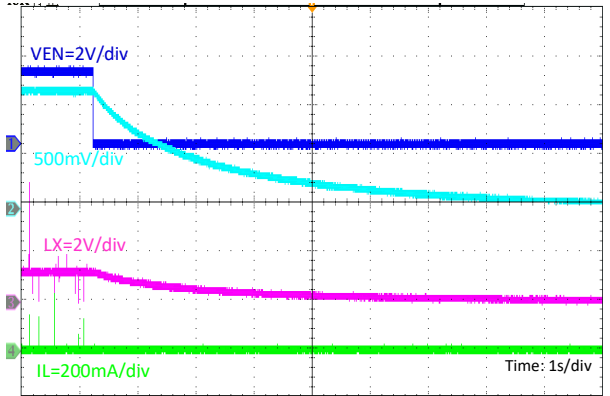
EN Enable Power On

$V_{IN}=3.6V, V_{OUT}=1.2V, \text{No Load}$



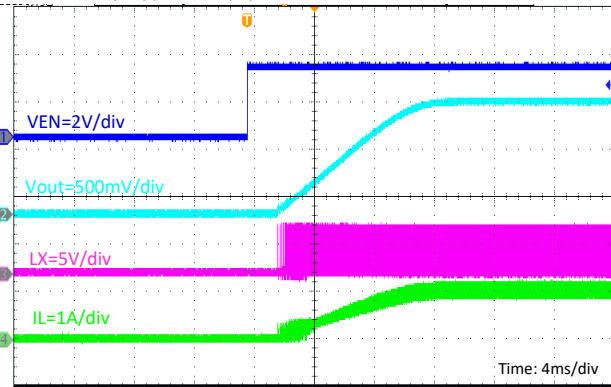
EN Disable Power down

$V_{IN}=3.6V, V_{OUT}=1.2V, \text{No Load}$



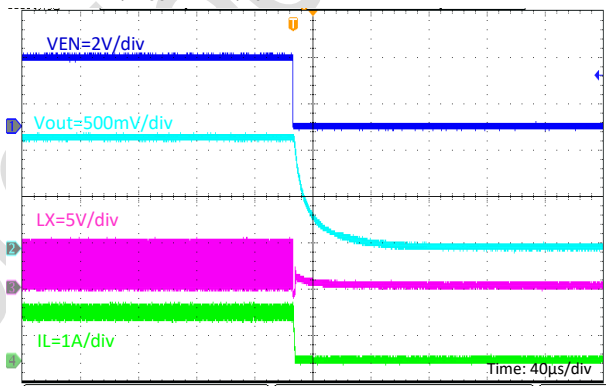
EN Enable Power On

$V_{IN}=3.6V, V_{OUT}=1.2V, I_o=1A$



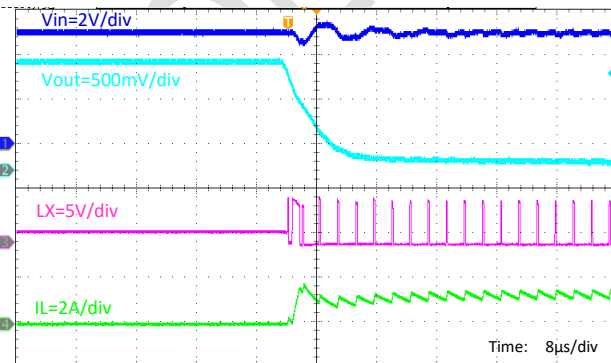
EN Enable Power On

$V_{IN}=3.6V, V_{OUT}=1.2V, I_o=1A$



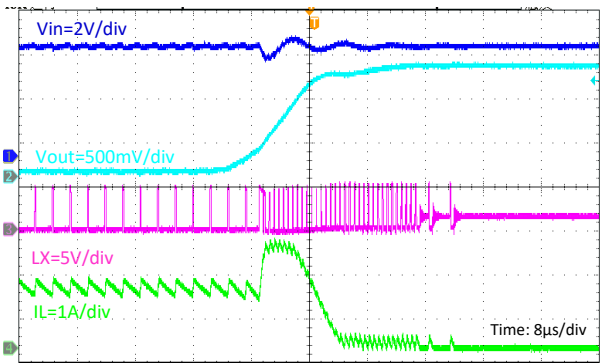
Output Short Entry

$V_{IN}=5V, V_{OUT}=1.2V, \text{No Load}$

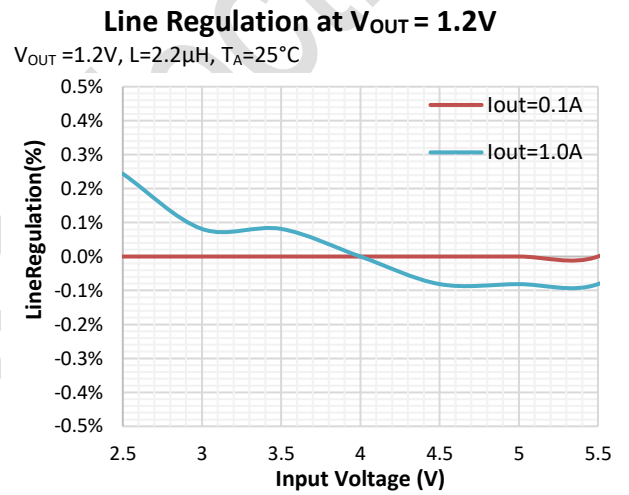
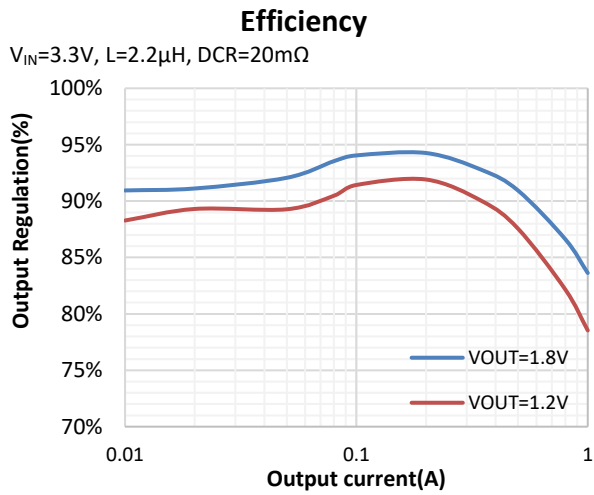
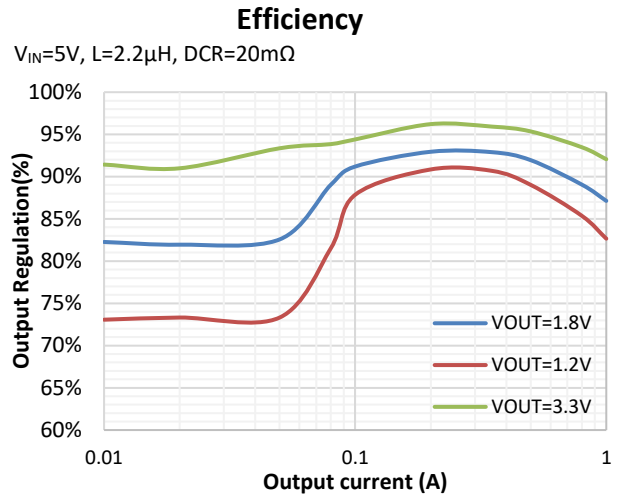
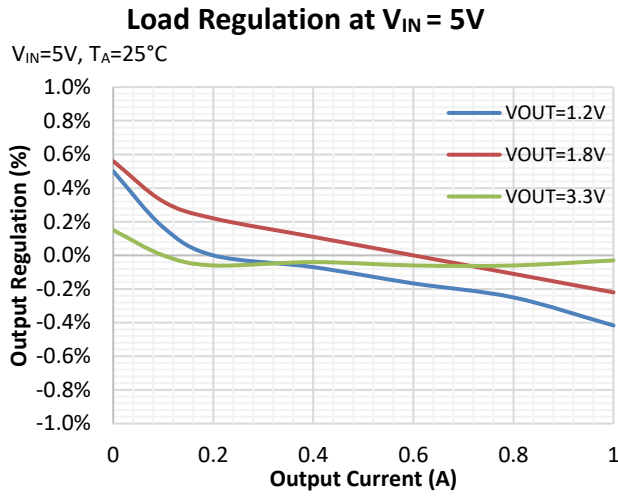


Output Short Recovery

$V_{IN}=5V, V_{OUT}=1.2V, \text{No Load}$

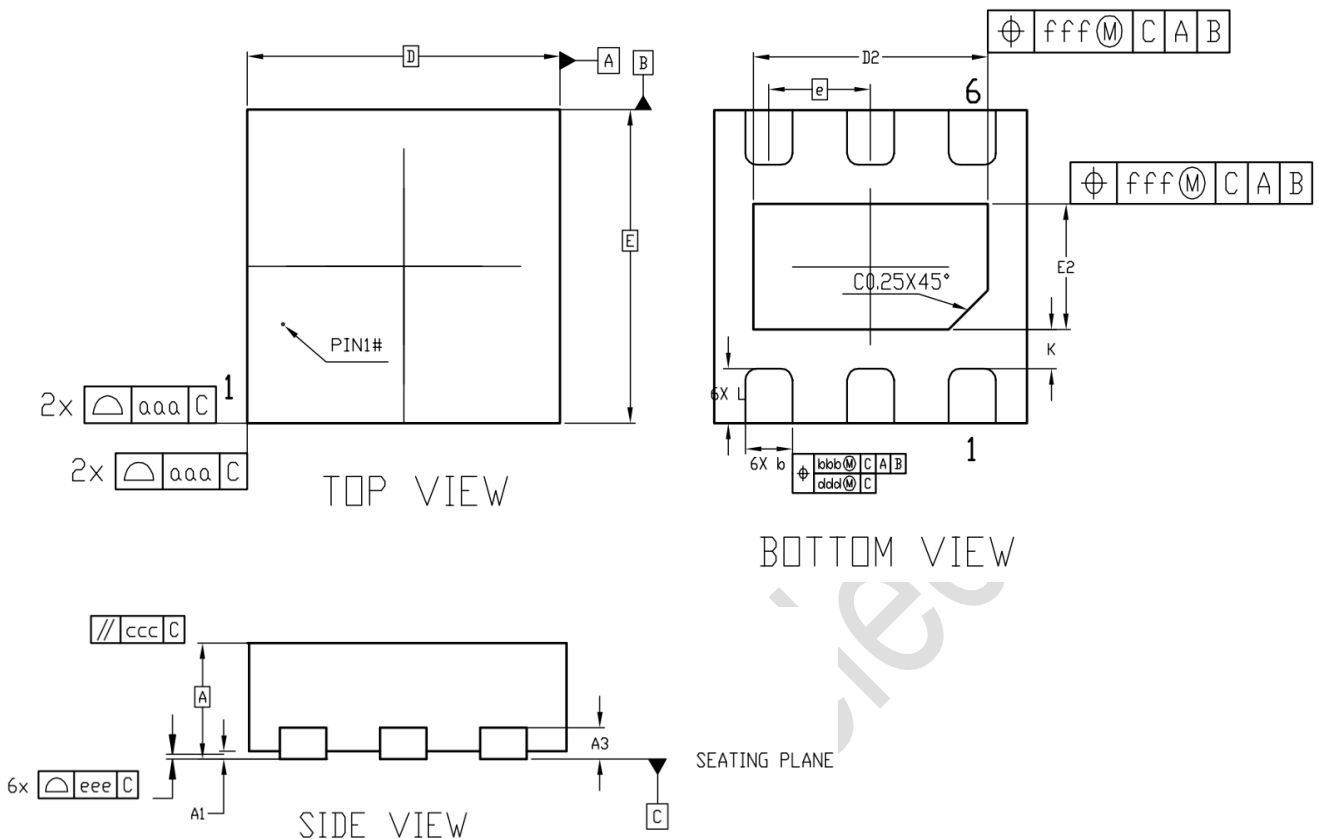


TYPICAL PERFORMANCE CHARACTERISTICS (continued)



## PACKAGE INFORMATION

### DFN2x2-6



Unit: mm

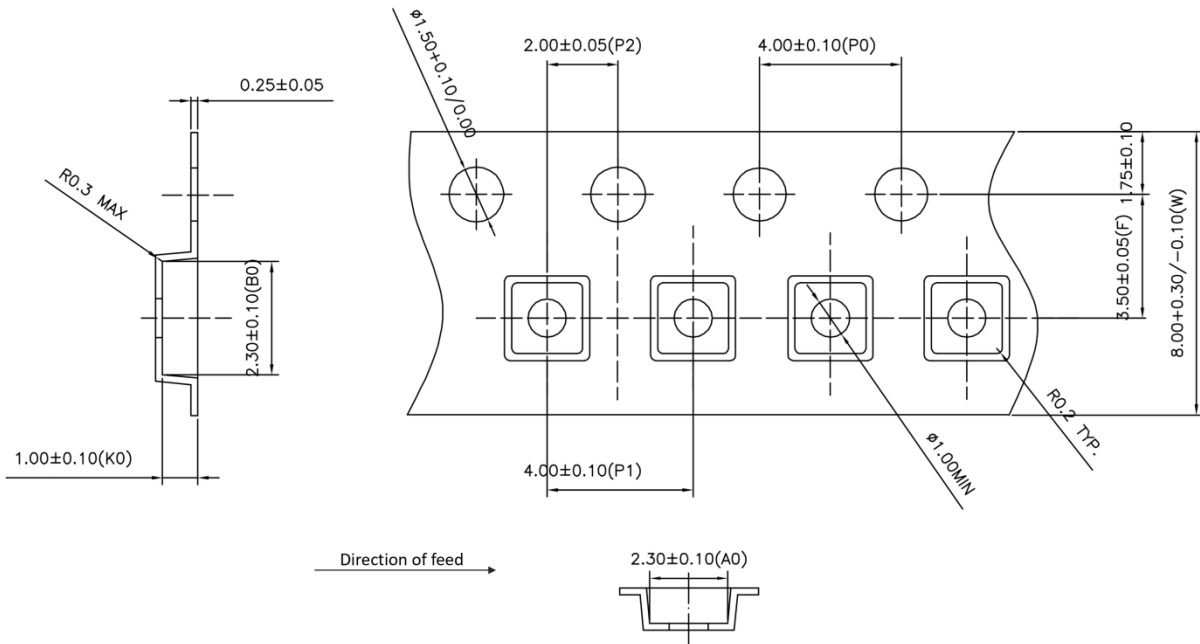
Symbol	Dimensions In Millimeters			Symbol	Dimensions In Millimeters		
	Min	Nom	Max		Min	Nom	Max
A	0.70	0.75	0.80	e	0.65 BSC		
	0.80	0.85	0.90	L	0.30	0.35	0.40
A1	0	0.02	0.05	K	0.25	-	-
A3	-	0.20 REF	-	aaa	0.15		
b	0.25	0.30	0.35	bbb	0.10		
D	2.00 BSC			ccc	0.10		
E	2.00 BSC			ddd	0.05		
D2	1.40	1.50	1.60	eee	0.08		
E2	0.70	0.80	0.90	fff	0.10		

#### Note:

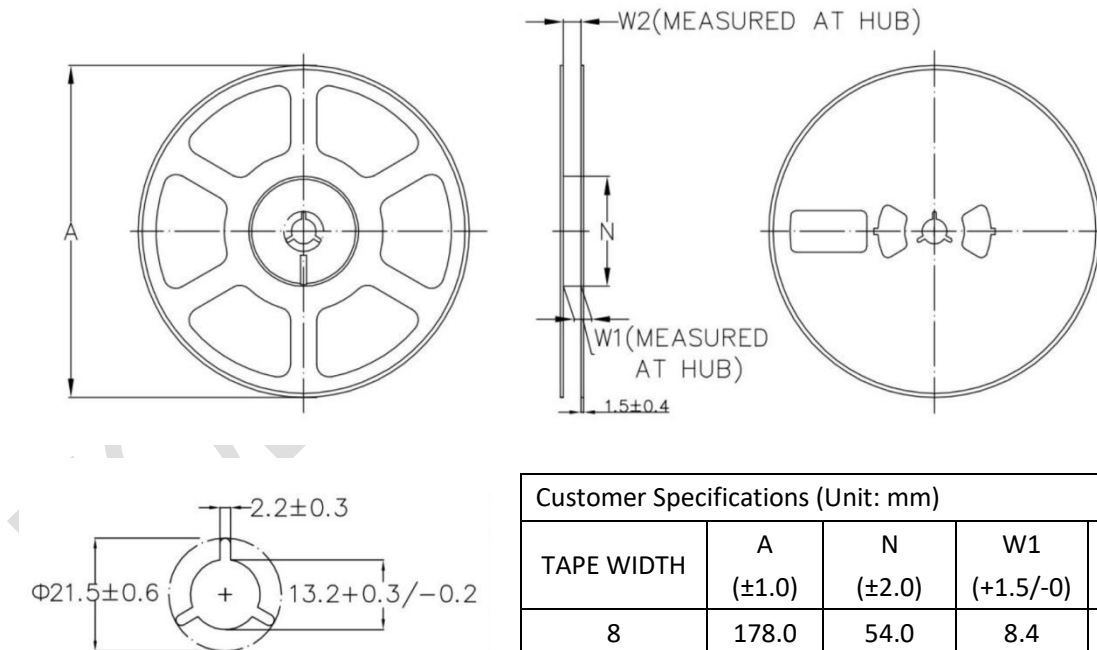
- 1) All dimensions are in millimeters. Angles are in degree.
- 2) Dimensioning and tolerancing confirm to ASME Y14.5M-1994.
- 3) Unilateral coplanarity zone applies to the exposed heat sink slug as well as the thermals.
- 4) Dimension b applies to metallized terminal and is measured between 0.150mm to 0.30mm from the thermal tip. Dimension b should not be measured in radius area.
- 5) All specs take JEDEC MO-229 for reference.

TAPE AND REEL INFORMATION

TAPE DIMENSIONS: DFN2x2-6



REEL DIMENSIONS: DFN2x2-6



Customer Specifications (Unit: mm)				
TAPE WIDTH	A ( $\pm 1.0$ )	N ( $\pm 2.0$ )	W1 ( $+1.5/-0$ )	W2 (Max)
8	178.0	54.0	8.4	14.4

Note:

- 1) All Dimensions are in Millimeter
- 2) Quantity of Units per Reel is 3000
- 3) MSL level is level 3.

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[NCP1336BDR2G](#) [NCP1361BABAYSNT1G](#) [NCP1230P100G](#) [NX2124CSTR](#) [SG2845M](#) [NCP1366BABAYDR2G](#) [NCP81101MNTXG](#)  
[TEA19362T/1J](#) [NCP81174NMNTXG](#) [NCP4308DMTTWG](#) [NCP4308DMNTWG](#) [NCP4308AMTTWG](#) [NCP1366AABAYDR2G](#)  
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