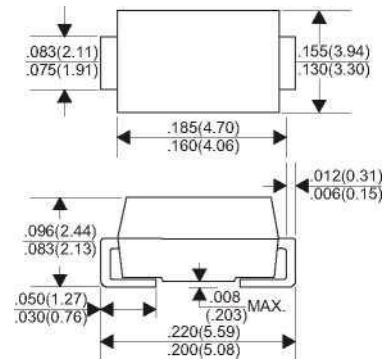


SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

Features

- ◆ The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- ◆ For surface mounted applications
- ◆ Metal silicon junction, majority carrier conduction
- ◆ Low power loss, high efficiency
- ◆ Built-in strain relief, ideal for automated placement
- ◆ High forward surge current capability
- ◆ High temperature soldering guaranteed:
250 °C/10 seconds at terminals

DO-214AA(SMB)

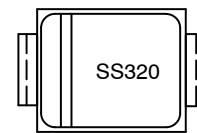


Dimensions in inches and (millimeters)

Mechanical Data

Case : JEDEC DO-214AC/SMA molded plastic body
 Terminals : Solderable per MIL-STD-750, Method 2026
 Polarity : Color band denotes cathode end Mounting
 Position : Any
 Weight : 0.003 ounce, 0.093 grams

MARKING DIAGRAM



ORDERING INFORMATION

Device	Package	Shipping
SS320	SMB (Pb-Free)	3000 / Tape & Reel

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	200	V
Average Rectified Forward Current ($T_L = 150^\circ\text{C}$)	$I_{F(AV)}$	3.0	A
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I_{FSM}	100	A
Operating Junction Temperature	T_J	-65 to +175	$^\circ\text{C}$

THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction-to-Lead (Note 1)	$R_{\theta JL}$	13	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	62	$^\circ\text{C/W}$

ELECTRICAL CHARACTERISTICS

Maximum Instantaneous Forward Voltage (Note 3) ($I_F = 3.0\text{ A}$, $T_J = 25^\circ\text{C}$) ($I_F = 4.0\text{ A}$, $T_J = 25^\circ\text{C}$) ($I_F = 3.0\text{ A}$, $T_J = 150^\circ\text{C}$)	V_F	0.84 0.86 0.59	V
Maximum Instantaneous Reverse Current (Note 3) (Rated dc Voltage, $T_J = 25^\circ\text{C}$) (Rated dc Voltage, $T_J = 150^\circ\text{C}$)	I_R	1.0 5.0	mA mA

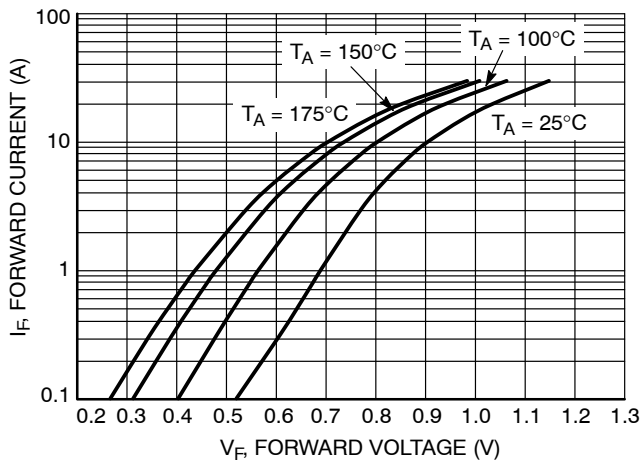


Figure 1. Typical Forward Voltage

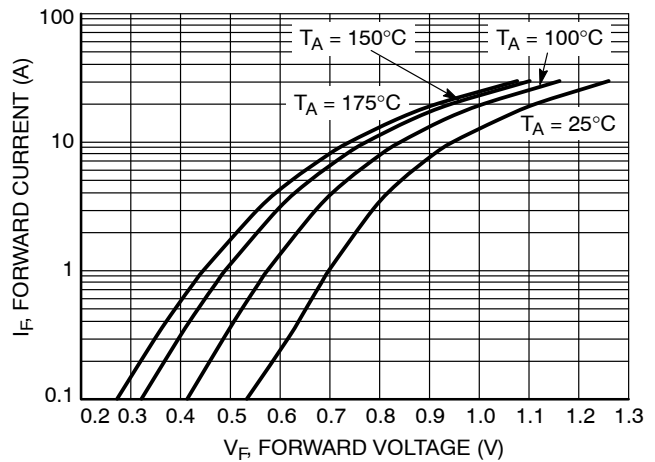


Figure 2. Maximum Forward Voltage

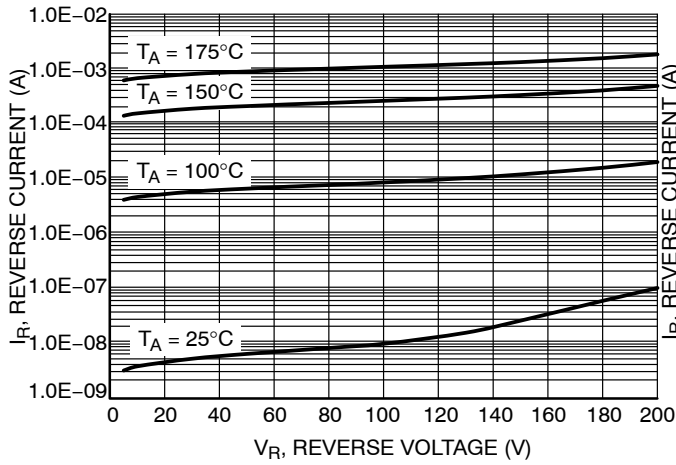


Figure 3. Typical Reverse Current

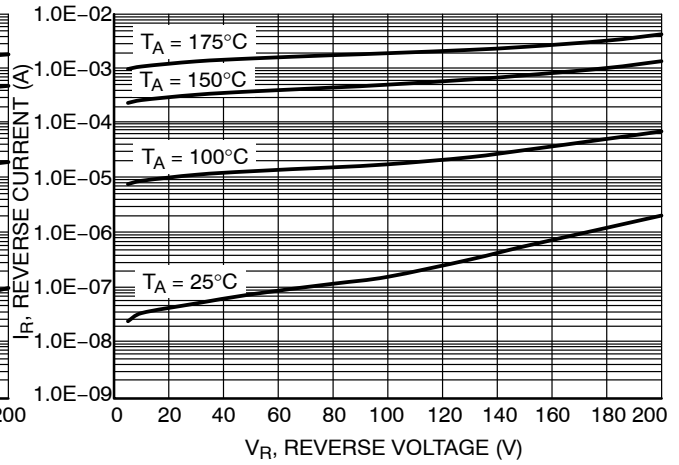


Figure 4. Maximum Reverse Current

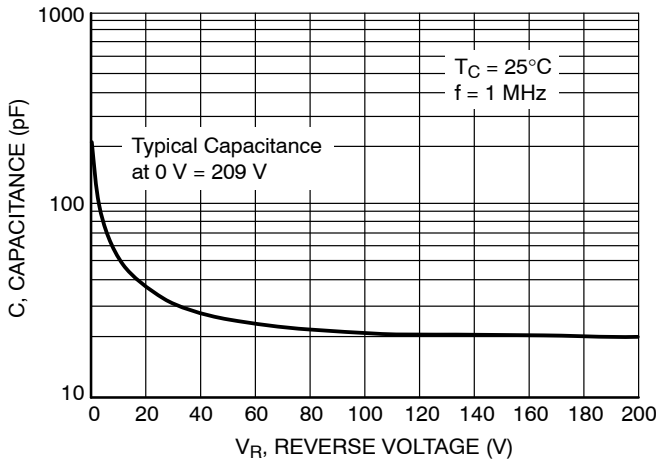


Figure 5. Typical Capacitance

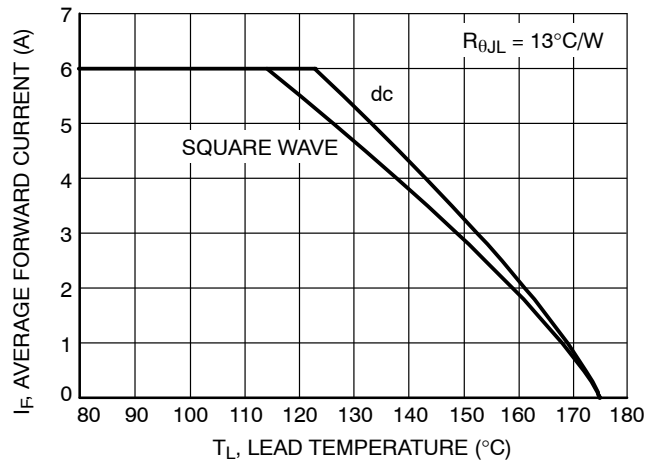


Figure 6. Current Derating - Lead

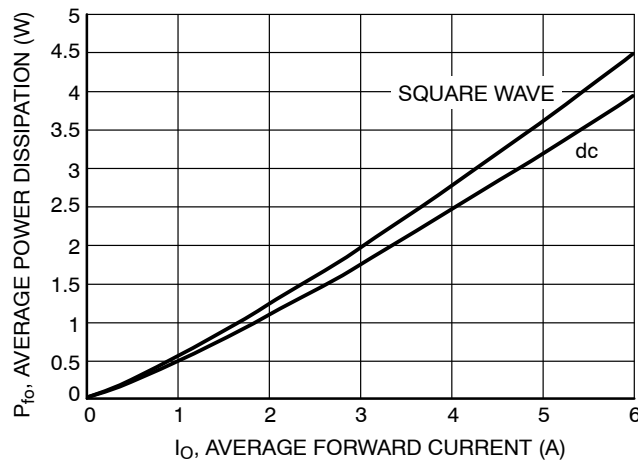


Figure 7. Forward Power Dissipation

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