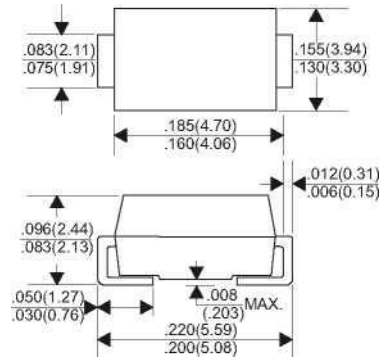


SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

Features

- ◆ The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- ◆ For surface mounted applications
- ◆ Metal silicon junction, majority carrier conduction
- ◆ Low power loss, high efficiency
- ◆ Built-in strain relief, ideal for automated placement
- ◆ High forward surge current capability
- ◆ High temperature soldering guaranteed:
250 °C/10 seconds at terminals

DO-214AA(SMB)

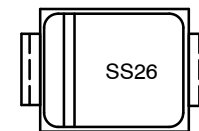


Dimensions in inches and (millimeters)

Mechanical Data

Case : JEDEC DO-214AC/SMA molded plastic body
 Terminals : Solderable per MIL-STD-750, Method 2026
 Polarity : Color band denotes cathode end Mounting
 Position : Any
 Weight : 0.003 ounce, 0.093 grams

MARKING DIAGRAM



MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V_{RRM}	60	V
Working Peak Reverse Voltage	V_{RWM}		
DC Blocking Voltage	V_R		
Average Rectified Forward Current (At Rated V_R , $T_L = 95^\circ\text{C}$)	I_O	2.0	A
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I_{FSM}	40	A
Storage/Operating Case Temperature	T_{stg}, T_C	-55 to +150	$^\circ\text{C}$
Operating Junction Temperature	T_J	-55 to +150	$^\circ\text{C}$
Voltage Rate of Change (Rated V_R , $T_J = 25^\circ\text{C}$)	dv/dt	10,000	$\text{V}/\mu\text{s}$

ORDERING INFORMATION

Device	Package	Shipping [†]
SS26	SMB (Pb-Free)	3000 / Tape & Reel

THERMAL CHARACTERISTICS

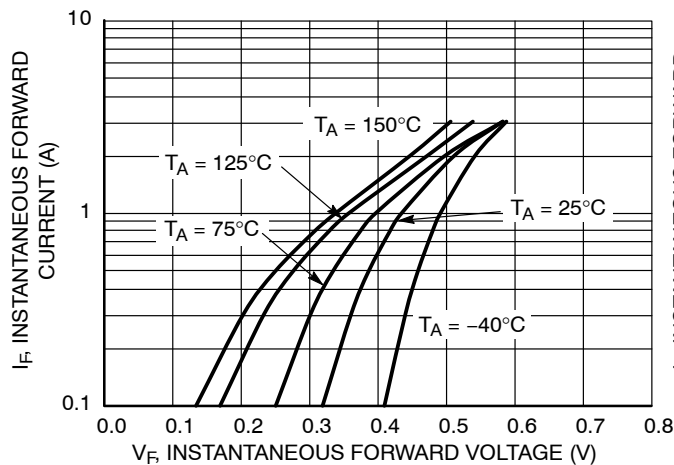
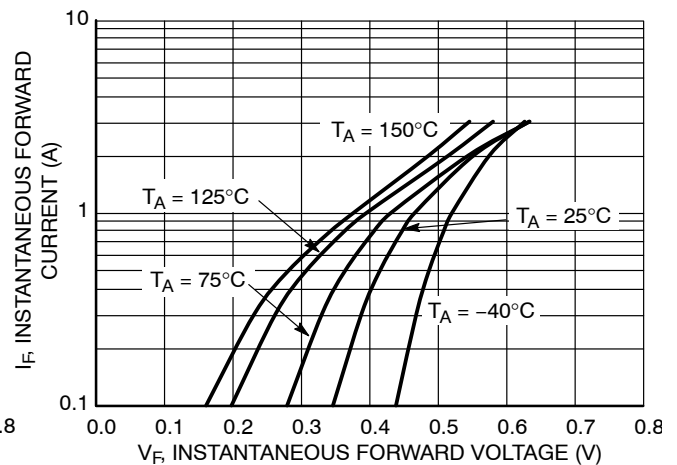
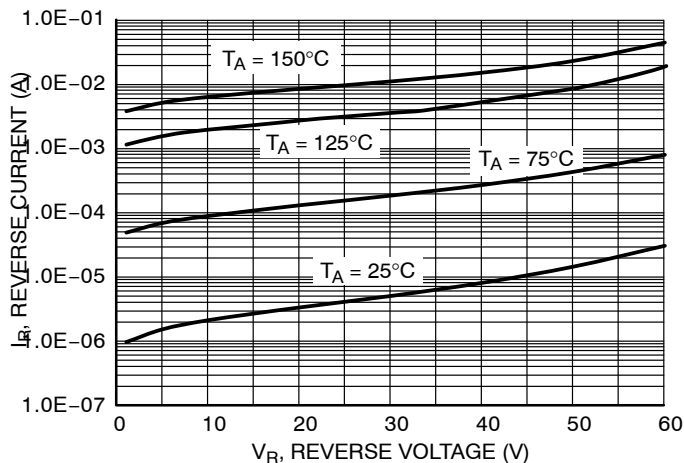
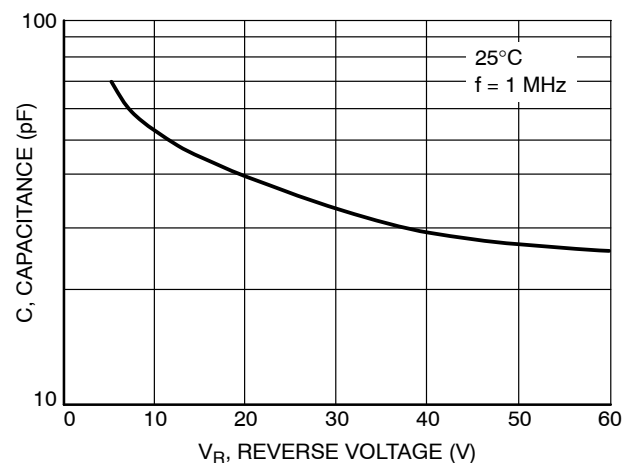
Characteristic	Symbol	Value	Unit
Thermal Resistance – Junction-to-Lead (Note 1)	$R_{\theta JL}$	24	$^{\circ}\text{C}/\text{W}$
Thermal Resistance – Junction-to-Ambient (Note 2)	$R_{\theta JA}$	80	$^{\circ}\text{C}/\text{W}$

- Mounted with minimum recommended pad size, PC Board FR4.
- 1 inch square pad size (1 x 0.5 inch for each lead) on FR4 board.

ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Value		Unit
		$T_J = 25^{\circ}\text{C}$	$T_J = 125^{\circ}\text{C}$	
Maximum Instantaneous Forward Voltage (Note 3) ($i_F = 1.0 \text{ A}$) ($i_F = 2.0 \text{ A}$)	v_F	0.51 0.63	0.475 0.55	V
Maximum Instantaneous Reverse Current (Note 3) ($V_R = 60 \text{ V}$)	I_R	0.2	20	mA

- Pulse Test: Pulse Width $\leq 250 \mu\text{s}$, Duty Cycle $\leq 2.0\%$.


Figure 1. Typical Forward Voltage

Figure 2. Maximum Forward Voltage

Figure 3. Typical Reverse Current

Figure 4. Typical Capacitance

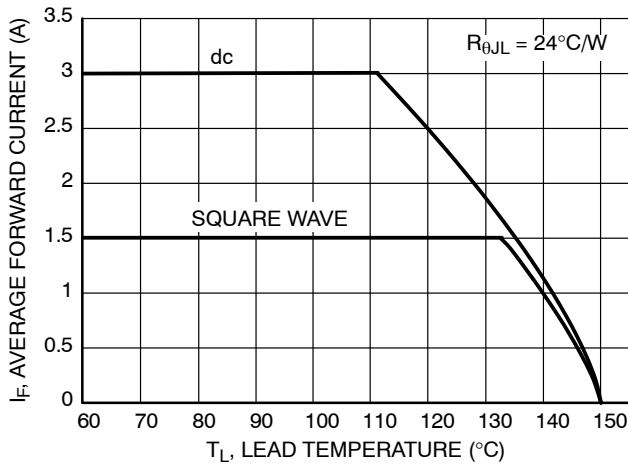


Figure 5. Current Derating – Junction to Lead

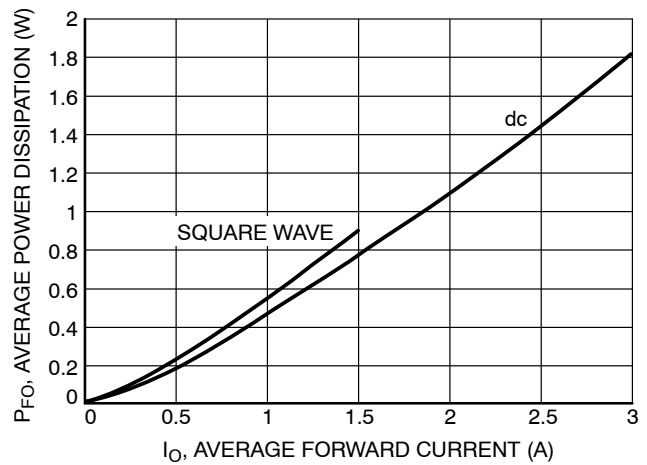


Figure 6. Forward Power Dissipation

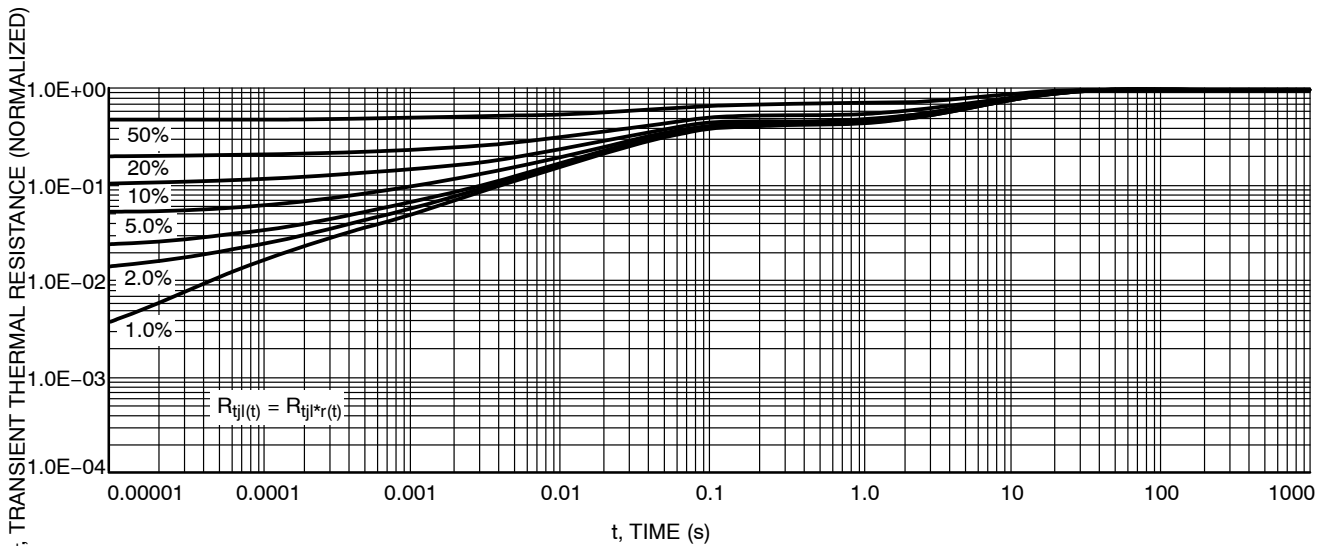


Figure 7. Thermal Response – Junction to Case

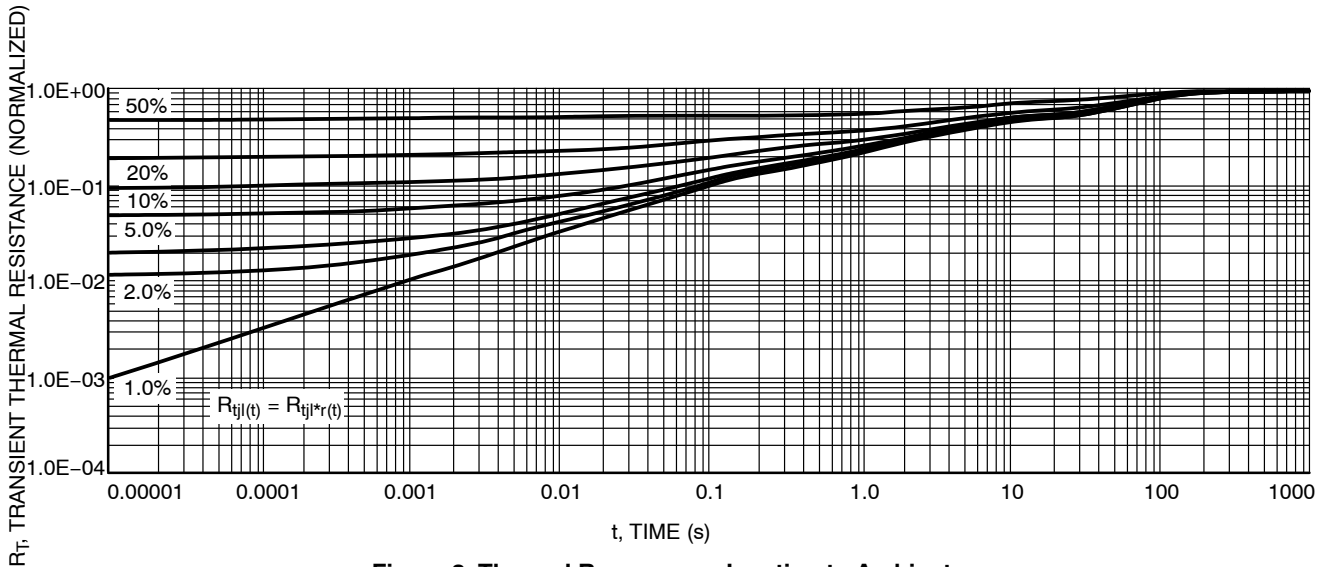


Figure 8. Thermal Response – Junction to Ambient

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