ETR0315\_006a

### 1A Low Dropout Positive Voltage Regulator

### **■**GENERAL DESCRIPTION

The XB1117 series is 1A Bi-polar transistor LDO voltage regulator. Output voltage of the XB1117P series is fixed to 1.8V, 2.5V, 3.3V, and 5.0V. The XB1117K series output voltage is adjustable by the external resistors. Please refer to the absolute maximum ratings for the difference between the rated input voltage of the XB1117P50 (Vout=5.0V) and XB1117P18, 25, 31, K12B. With the dropout voltage 1.2V (TYP.), output current can be generated up to 1A. The built-in overcurrent circuit and thermal protection circuit start to operate when either one of output put current reaches the current limit level or junction temperature reaches the temperature limit. The XB1117 series provide stable line and load regulation by using an input capacitor and an output capacitor (10  $\mu$  F, tantalum). Package is available in SOT-223.

#### ■APPLICATIONS

- Highly efficient linear regulators
- ●5V ~ 3.3V DC / DC converter
- Battery charger
- Local power supply inside equipment
- Battery powered equipment

### **■**FEATURES

Maximum Output Current : 1A

Output Voltages : 1.8V, 2.5V, 3.3V, 5.0V, ADJ

Output Voltage Accuracy : ±1%

Low Dropout Voltage : 1.2V @ IOUT=1A Line Regulation (TYP.) : 0.04% (ADJ) Load Regulation (TYP.) : 0.1% (ADJ)

Adjust Pin Current : Less than  $120 \mu$  A (ADJ) Protection Circuit : Over-current protection

Thermal protection

Package : SOT-223

Environmentally Friendly : EU RoHS Compliant, Pb Free

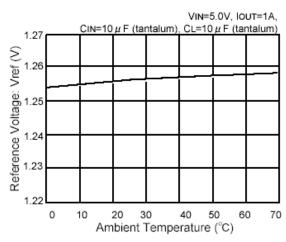
### ■TYPICAL APPLICATION CIRCUIT

### XB1117P Series VIN Vss CIN=10 μ F CI =10 # F (Tantalum (Tantalum) /// XB1117K Series Vin Vou - lou⊤ ADJ CL=10 μ F CIN=10 μ F Vout=VREF x (1 + R2/R1) +IADJ x R2

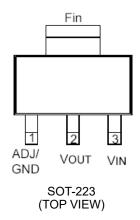
# ■ TYPICAL PERFORMANCE CHARACTERISTICS

● Reference Voltage vs. Ambient Temperature

XB1117K12B



### **■PIN CONFIGURATION**



### **■PIN ASSIGNMENT**

PIN NUMBER	PIN NAME	FUNCTIONS
1	ADJ/GND	ADJ/Ground
2	Vout	Output
3	Vin	Input

<sup>\*</sup> The electrical potential of the package fin is the same as the VOUT pin.

### **■PRODUCT CLASSIFICATION**

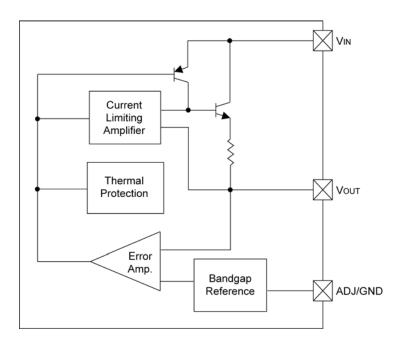
#### Ordering Information

XB1117(1)2(3)4(5)6-7

DESIGNATOR	ITEM	SYMBOL	DESCRIPTION
(1)	Type of Regulators	Р	Fixed Vout type
	Type of Regulators	K	Adjustable Vout type
		181	Fixed Vout 1.80V (±1%)
	Output Voltage & Accuracy	251	Fixed Vout 2.50V (±1%)
234		331	Fixed Vout 3.30V (±1%)
		501	Fixed Vout 5.00V (±1%)
		12B	Adjustable Vo∪⊤ 1.25V (±1%)
(F)(C) (7)(*1)	Package	ED C	SOT 222/4 000/Pagl\
56-7(*1)	(Order Unit)	FR-G	SOT-223(1,000/Reel)

 $<sup>^{(1)}</sup>$  The "-G" suffix denotes Halogen and Antimony free as well as being fully EU RoHS compliant.

# ■BLOCK DIAGRAM



### ■ ABSOLUTE MAXIMUM RATINGS

#### XB1117P501

PARAMETER	SYMBOL	RATINGS	UNITS	
Input Voltage	VIN	10.0	V	
Thermal Resistance	θJc	15		
(Junction to Case)	<i>9</i> 3C	10	°C/W	
Thermal Resistance	$\theta$ Ja	160		
(Junction to Ambient)	O JA	100		
Power Dissipation	Pn	625	mW	
(ΔT=100°C)	I D	023	11177	
Operating Ambient Temperature	Topr	0 ~+70		
Operating Junction Temperature Range	Tj	0~+125	က	
Storage Temperature Range	Tstg	- 65 ~ <del>+</del> 150		
Lead Temperature	Tlead	260		

<sup>\*</sup>Stress above the listed absolute maximum rating may cause permanent damage to the device.

### **■**ELECTRICAL CHARACTERISTICS

XB1117P501 Ta=25°C

PARAMETER	SYMBOL	CONDITIONS		MIN.	TYP.	MAX.	UNITS
Output Voltage	Vout	VIN=7.0V		4.950	5.000	5.050	V
Output Voltage	V001	Iout=0A	*Over Temp.	4.900	5.000	5.100	V
Line Regulation	∆Vout1	7.0V≦VIN≦9.0V IOUT=0A	*Over Temp.	-	1	6	\ <i>(</i>
Load Pogulation	A MOUTO	VIN=7.0V		-	5.0	15.2	mV
Load Regulation	∆Vout2	0A≦Ioυτ≦1.0A	*Over Temp.	-	10.1	20.2	
Dropout Voltage	Vdif	ΔVouτ=±1%		-	1.2	1.4	V
Dropout Voltage	Vali	0A≦Iouт≦1.0A	*Over Temp.	-	1.3	-	V
Current Limit	ILIM	7.0V≦VIN≦10.0V	*Over Temp.	1.0	1.5	ı	Α
Supply Current	Iss	VIN=7.0V 0A≦IouT≦1.0A	*Over Temp.	-	6	13	mA
Temperature Coefficient	Тс	7.0V≦VIN≦10.0V 0A≦IouT≦1.0A		_	50	ı	ppm/°C
Temperature Stability	Ts	VIN=7.0V IOUT=100mA	*Over Temp.	-	0.5	-	%

<sup>\*</sup>Over Temp. = Over Temperature  $(0 \sim +70^{\circ}C)$ 

<sup>\*\*</sup> The rated values of the XB1117P18 / 25 / 30 (VOUT=1.8V, 2.5V and 3.0V) and XB1117K type are different from that of the XB1117P50 (VOUT=5.0V).

### ■ ABSOLUTE MAXIMUM RATINGS

XB1117P181, P251, P331, K12B

PARAMETER	SYMBOL	RATINGS	UNITS
Input Voltage	VIN	7.0	V
Thermal Resistance (Junction to Case)	θ Jc	15	°C/W
Thermal Resistance (Junction to Ambient)	heta Ja	160	C/VV
Power Dissipation (ΔT=100°C)	PD	625	mW
Operating Ambient Temperature	Topr	0 ~ 70	
Operating Temperature Range	TJ	0 ~ 125	°C
Storage Temperature Range	Tstg	-65 ~ 150	
Lead Temperature	TLEAD	260	

<sup>\*</sup>Stress above the listed absolute maximum rating may cause permanent damage to the device.

### **■**ELECTRICAL CHARACTERISTICS

XB1117P181 T<sub>J</sub>=25°C

PARAMETER	SYMBOL	CONDITIONS		MIN.	TYP.	MAX.	UNITS
Output Voltage	Vouт	Vin=5.0V Iout=0A	*Over Temp.	1.782	1.800	1.818	V
Line Regulation	ΔVout1	4.75V≦VIN≦7.0V IOUT = 0A		-	1.0	5.5	mV
Load Degulation	Δ Vout2	VIN=5.0V		-	1.80	18.2	mV
Load Regulation	Δ VOU12	0A≦Iouт≦1.0A	*Over Temp.	-	3.70	22.0	IIIV
Drangut Voltage	Vdif	ΔVouτ=±1%		-	1.2	1.4	V
Dropout Voltage	Vuli	0A≦Iouт≦1.0A	*Over Temp.	-	1.3	-	V
Current Limit	I <sub>LIM</sub>	4.75V≦VIN≦7.0V	*Over Temp.	1.0	1.5	-	Α
Supply Current	Iss	Vin=5.0V 0A≦IouT≦1.0A	*Over Temp.	-	6	13	mA
Temperature Coefficient	Tc	4.75V≦VIN≦7.0V 0A≦Iout≦1.0A			50	-	ppm/ °C
Temperature Stability	Ts	VIN=5.0V IOUT=100mA	*Over Temp.	-	0.5	-	%

<sup>\*</sup>Over Temp. = Over Temperature (0~+70°C)

XB1117P251  $T_{J} = 25^{\circ}C$ 

PARAMETER	SYMBOL	CONDITIONS		STAN	IDARD V	ALUE	UNITS
PARAIVIETER	STIVIDOL	CONDITIONS	CONDITIONS		TYP.	MAX.	UNITS
Output Voltage	Vouт	VIN=5.0V		2.475	2.500	2.525	V
Output voltage	<b>V</b> 001	IOUT=0A	*Over Temp.	2.450	2.500	2.550	V
Line Regulation	ΔVOUT1	4.75V≦VIN≦7.0V IOUT=0A		-	1.0	6.8	mV
Load Pagulation	Δ Vout2	VIN=5.0V		-	2.5	25.3	mV
Load Regulation	Δ ۷0012	0A≦Iouт≦1.0A	*Over Temp.	-	5.1	30.3	IIIV
Dropout Voltage	Vdif	ΔVout=±1%		-	1.2	1.4	V
Dropout voltage	vuii	0A≦Iouт≦1.0A	*Over Temp.	-	1.3	ı	V
Current Limit	I <sub>LIM</sub>	4.75V≦VIN≦7.0V	*Over Temp.	1.0	1.5	ı	Α
Supply Current	Iss	Vin=5.0V 0A≦IouT≦1.0A	*Over Temp.	-	6	13	mA
Temperature Coefficient	Tc	4.75V≦VIN≦7.0V 0A≦IouT≦1.0A		-	50	-	ppm/
Temperature Stability	Ts	VIN=5.0V IOUT=100mA	*Over Temp.	-	0.5	ı	%

<sup>\*</sup>Over Temp. = Over Temperature  $(0 \sim +70^{\circ}C)$ 

# ■ELECTRICAL CHARACTERISTICS (Continued)

XB1117P331 T<sub>J</sub> =25°C

PARAMETER	SYMBOL	CONDITIONS		MIN.	TYP.	MAX.	UNITS
Output Voltage	Vouт	VIN=5.0V		3.267	3.300	3.333	V
Output Voltage	V001	Iout=0A	*Over Temp.	3.234	3.300	3.366	V
Line Regulation	ΔVout1	4.75V≦VIN≦7.0V IOUT=0A		-	1.0	4.5	\
Load Degulation	Δ Vout2	VIN=5.0V		-	3.4	10.0	mV
Load Regulation	Δ VOU12	0A≦Iouт≦1.0V	*Over Temp.	-	6.7	13.4	
Dropout Voltage	Vdif	ΔVout=±1%		-	1.2	1.4	V
Dropout Voltage	vuii	0A≦Iouт≦1.0A	*Over Temp.	-	1.3	1	V
Current Limit	I <sub>LIM</sub>	4.75V≦VIN≦7.0V	*Over Temp.	1.0	1.5	ı	Α
Supply Current	Iss	VIN=5.0V 0A≦IOUT≦1.0A	*Over Temp.	-	6	13	mA
Temperature Coefficient	Tc	4.75V≦VIN≦7.0V 0A≦Iout≦1.0A		-	50	-	ppm/°C
Temperature Stability	Ts	VIN=5.0V IOUT=100mA	*Over Temp.	-	0.5	-	%

<sup>\*</sup>Over Temp. = Over Temperature  $(0 \sim +70^{\circ}C)$ 

XB1117K12B TJ=25°C

PARAMETER	SYMBOL	CONDITIONS		MIN.	TYP.	MAX.	UNITS
Poforonce voltage	Vref	VIN=5.0V		1.238	1.250	1.262	V
Reference voltage	viei	IOUT=10mA	*Over Temp.	1.225	1.250	1.275	V
Line Regulation	Δ Vout1	4.75V≦VIN≦7.0V			0.04	0.20	%
Line Regulation	Δ ۷0011	IOUT=0A		_	0.04	0.20	70
Load Population	Δ Vout2	VIN=5.0V		-	0.1	0.3	%
Load Regulation	Δ ۷0012	10mA≦Iouт≦1.0A	*Over Temp.	-	0.2	0.4	90
Dropout Voltage	Vdif	ΔVouτ=±1%		-	1.2	1.4	V
Dropout Voltage	vuii	10mA≦Iouт≦1.0A	*Over Temp.	-	1.3	-	V
Current Limit	I <sub>LIM</sub>	2.75A≦VIN≦7.0V	*Over Temp.	1.0	1.5	-	Α
Temperature	Tc	2.75V≦VIN≦7.0V			50		ppm/
Coefficient	10	10mA≦Iouт≦1.0A		_	30	-	°C
Adjust Pin Current	ladj	2.75V≦VIN≦7.0V			55	-	μΑ
Adjust Fill Cullett	IADJ	10mA≦IouT≦1.0A	*Over Temp.	-	-	120	μΑ
Adjust Pin Current	ΔIADJ	2.75V≦VIN≦7.0V	*Over Temp.	_	0.2	5.0	μΑ
Change	ΔIADJ	10mA≦IouT≦1.0A	Over remp.	-	0.2	3.0	μΑ
Temperature Stability	Ts	VIN=5.0V	*Over Temp.	_	0.5		%
Temperature Stability	13	IOUT=100mA	Over lemp.	_	0.5		/0
Minimum Load Current	Іоит	Vоuт=5.0V		-	-	10	mA

<sup>\*</sup>Over Temp. = Over Temperature  $(0 \sim +70^{\circ}\text{C})$ 

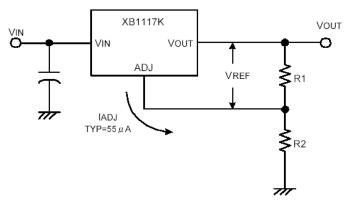
#### ■ OPERATIONAL EXPLANATION

#### 1. Output voltage adjustment

The XB1117 series provide a stable output by comparing the output voltage to an internal reference voltage. With the adjustable XB1117K type, a 1.25V reference voltage (VREF) is fixed between the Vout pin and the ADJ pin and the external resistors R1 and R2 are used to set the output voltage. The resistance values of R1 and R2 should be set so as to provide a minimum load current of 10mA. The output voltage is given by the following equation.

VOUT=VREF(1+R2/R1)+IADJ x R2

The output voltage of the XB1117P type is internally fixed to 1.8V, 2.5V, 3.3V, and 5.0V so external resistors are not necessary.



#### 2. Stability and load regulation

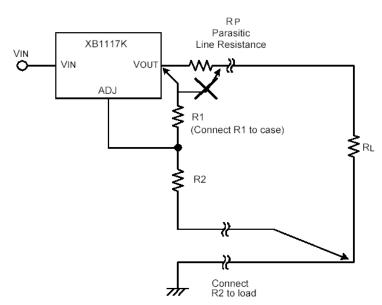
The XB1117 series requires a load capacitor between the Vout pin and the GND pin to provide phase compensation thereby ensuring stability of the output voltage. Either a tantalum capacitor of more than  $10 \,\mu$  F (TYP.) or an aluminum electrolytic capacitor of more than  $50 \,\mu$  F (TYP.) should be connected.

(Note: The capacitor's ESR value should not exceed  $0.5\Omega$ .)

The output capacitor does not have a theoretical upper limit so increasing its value will increase stability. CL= $100 \mu$  F or more is typical for high current regulator design.

In order to avoid any reductions in output voltage accuracy with the XB1117K type, we recommend not to place a parasitic resistor (Rp) between the Vout pin and the divider resistor R1. The parasitic resistor (Rp) does not influence the output however if the divider resistor R1 is directly connected to the Vout pin.

With the XB1117P type, although external resistor (R1) is internally connected to the Vout pin, stability can be maintained by not wiring a parasitic resistor to the GND pin.



### ■ OPERATIONAL EXPLANATION (Continued)

#### 3. Thermal protection

XB1117 series has thermal protection which limits junction temperature to 150°C. However, device functionally is only guaranteed to a maximum junction temperature of + 125°C. The power dissipation and junction temperature for the XB1117 series are given by;

PD=(VIN-VOUT) x IOUT

 $TJ=TA+(PD \times \theta JA)$ 

NOTE: TJ must not exceed 125°C.

#### 4. Current limit protection

XB1117 series is protected against overload conditions. Current protection is triggered at 1.5A (TYP.).

#### 5. Thermal consideration

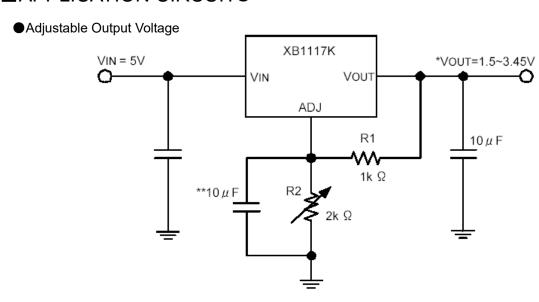
The XB1117 series contain thermal limiting circuitry designed to protect itself from over-temperature conditions. Even for normal load conditions, maximum junction temperature ratings must not be exceeded. As mentioned in thermal protection section, we need to consider all sources of thermal resistance between junction and ambient. It includes junction-to-case, case-to-heat-sink interface and heat sink thermal resistance itself.

Junction-to-case thermal resistance is specified from the IC junction to the bottom of the case directly below the die. Proper mounting is required to ensure the best possible thermal flow from this area of the package to the heat sink. The case of all devices in this product series is electrically connected to the output. Therefore, if the case of the device is not electrically isolated, a thermally conductive spacer is recommended.

### ■NOTES ON USE

- For the phenomenon of temporal and transitional voltage decrease or voltage increase, the IC may be damaged or deteriorated if IC is used beyond the absolute MAX. specifications.
- Torex places an importance on improving our products and their reliability.We request that users incorporate fail-safe designs and post-aging protection treatment when using Torex products in their systems.

#### ■APPLICATION CIRCUITS

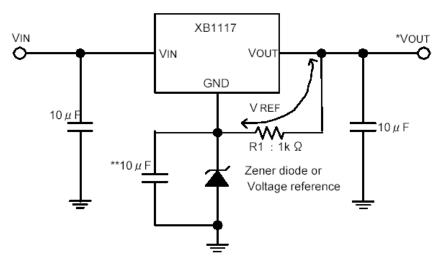


NOTE: \*VOUT = VREF  $(1 + \frac{R2}{R1}) + IADJ \times R2$ 

\*\* Optional for improved ripple rejection.

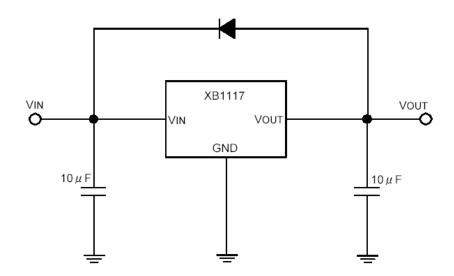
# ■APPLICATION CIRCUITS (Continued)

#### Regulator with reference voltage



NOTE: \*Vout = VREF + VZ (VZ: Zener diode)

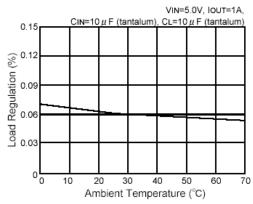
#### Regulator with reverse diode protection



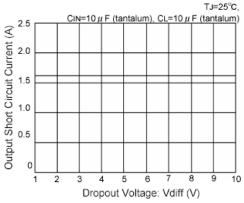
<sup>\*\*</sup> Optional for improved ripple rejection.

### ■TYPICAL PERFORMANCE CHARACTERISTICS

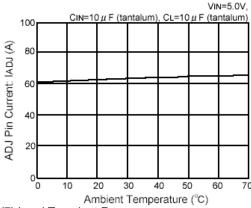
#### (1) Load Regulation vs. Ambient Temperature XB1117K12B



#### (3) Output Short Circuit Current vs. Dropout Voltage XB1117P331

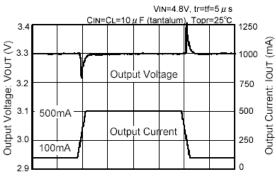


(5) Adjust Pin Current vs. Ambient Temperature XB1117K12B



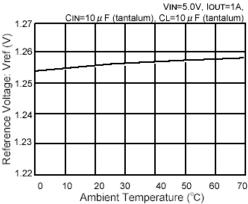
(7) Load Transient Response



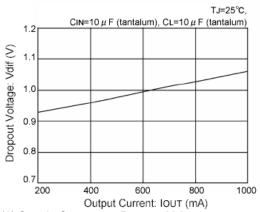


Time (20  $\mu$  s/div.)

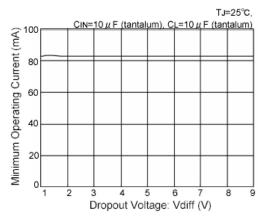
#### (2) Reference Voltage vs. Ambient Temperature XB1117K12B



(4) Dropout Voltage vs. Output Current XB1117P331

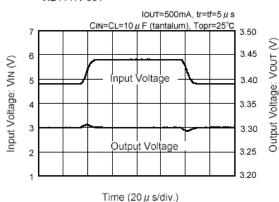


(6) Supply Current vs. Dropout Voltage XB1117P331



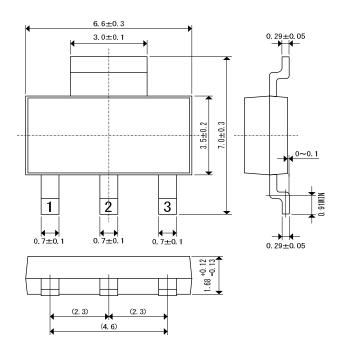
#### (8) Line Transient Response

XB1117P331



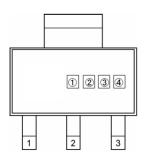
### ■ PACKAGING INFORMATION

#### ●SOT-223



### ■ MARKING RULE

#### ●SOT-223



SOT-223 (TOP VIEW)

#### ① represents product series

MARK	PRODUCT SERIES		
В	XB1117xxxxFx		

#### 2 represents fixed or adjustable output voltage

MARK	PRODUCT SERIES		
K	XB1117KxxxFx		
Р	XB1117PxxxFx		

#### 3 represents output voltage

MARK	OUTPUT VOLTAGE (V)	PRODUCT SERIES
В	ADJ	XB1117K12BFx
K	1.8	XB1117P181Fx
Т	2.5	XB1117P251Fx
2	3.3	XB1117P331Fx
M	5.0	XB1117P501Fx

### 4 represents production lot number

0 to 9, A to Z repeated. (G, I, J, O, Q, W are excepted.)

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