XBP14E5UFN-G



ETR29021-001

Low Capacitance TVS Diode Array

■FEATURES

Terminal Capacitance : 0.8pF (Line-to-GND)

ESD Protection : 8kV Contact (IEC61000-4-2)

Environmentally Friendly : EU RoHS Compliant, Pb Free

■APPLICATIONS

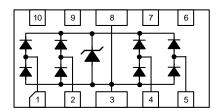
- ●USB 3.0
- DVI
- Set Top Box

■PRODUCT NAME

PRODUCT NAME	PACKAGE	ORDER UNIT
XBP14E5UFN-G *	DFN2510-10A	5,000pcs/Reel

^{*} The "-G" suffix denotes Halogen and Antimony free as well as being fully EU RoHS compliant.

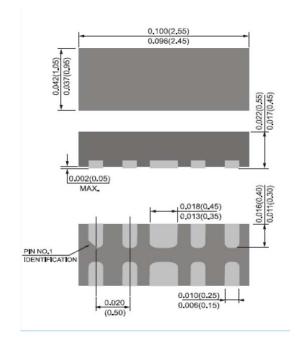
■PIN CONFIGURATION



- 1. I/O 1
- 2. I/O 2
- 3. GND
- 4. I/O 3
- 5. I/O 4
- NC
 NC
- 8. GND
- 9. NC
- 10. NC

■ PACKAGING INFORMATION

●DFN2510-10A Unit: inch (mm)



■ ABSOLUTE MAXIMUM RATINGS

Ta=25°C

PARAMETER	SYMBOL	RATINGS	UNIT
Junction Temperature	Tj	125	°C
Storage Temperature	Tstg	-55 to +150	°C
IEC61000-4-2 (ESD) Air	V_{ESD_A}	±15	kV
IEC61000-4-2 (ESD) Contact	V_{ESD_C}	±8	kV

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■ELECTRICAL CHARACTERISTICS

Ta=25°C

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			LINUT
			MIN.	TYP.	MAX.	UNIT
Stand-Off Voltage	V_{RWM}		-	-	5	V
Breakdown Voltage	V_{BR}	I _R =1mA, I/O pin to Pin3	6	-	9	V
Leakage Current	I _R	V _R =5V, I/O pin to Pin3	-	-	1	μA
Clamping Voltage (8/20 µs)	Vc	I _{PP} =2.5A, I/O pin to Pin3	-	11	13	V
Tanahad Osaa a'aa aa	Ct	V _R =0V, f=1MHz Between I/O pin to Pin3	-	0.6	0.8	pF
Terminal Capacitance	Ct	V _R =0V, f=1MHz Between I/O pins	-	0.35	0.4	pF

■NOTES ON USE

1. Please use this IC within the absolute maximum ratings.

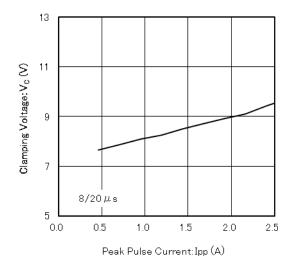
Even within the ratings, in case of high load use continuously such as high temperature, high voltage, high current and thermal stress may cause reliability degradation of the IC.

2. Torex places an importance on improving our products and their reliability.

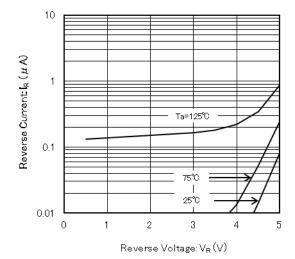
We request that users incorporate fail-safe designs and post-aging protection treatment when using Torex products in their systems.

■TYPICAL PERFORMANCE CHARACTERISTICS

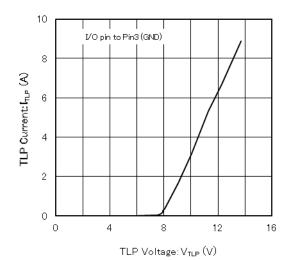
(1) Clamping Voltage vs. Peak Pulse Current



(2) Reverse Current vs. Reverse Voltage



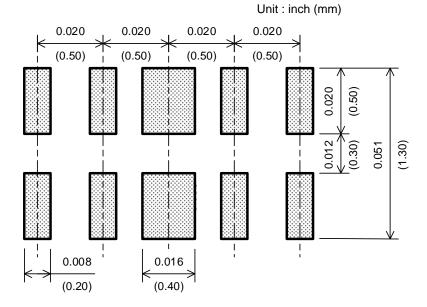
(3) Transmission Line Pulse (TLP) Measurement



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■ REFERENCE PATTERN LAYOUT

●DFN2510-10A



■MARKING

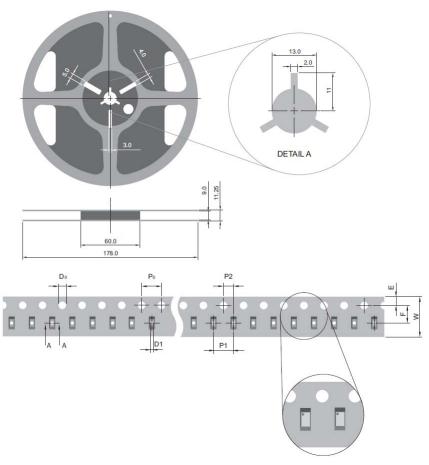


12 : Control Number

■TAPING SPECIFICATIONS

●DFN2510-10A

Unit : mm



SYMBOL	mm
D_0	1.55 ± 0.05
D1	0.50 ± 0.05
Е	1.75 ± 0.10
F	3.50 ± 0.05
P_0	4.00 ± 0.10
P1	4.00 ± 0.10
P2	2.00 ± 0.05
W	8.00 + 0.30 - 0.15

- 1. The product and product specifications contained herein are subject to change without notice to improve performance characteristics. Consult us, or our representatives before use, to confirm that the information in this datasheet is up to date.
- 2. The information in this datasheet is intended to illustrate the operation and characteristics of our products. We neither make warranties or representations with respect to the accuracy or completeness of the information contained in this datasheet nor grant any license to any intellectual property rights of ours or any third party concerning with the information in this datasheet.
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- 5. Although we make continuous efforts to improve the quality and reliability of our products; nevertheless Semiconductors are likely to fail with a certain probability. So in order to prevent personal injury and/or property damage resulting from such failure, customers are required to incorporate adequate safety measures in their designs, such as system fail safes, redundancy and fire prevention features.
- 6. Our products are not designed to be Radiation-resistant.
- 7. Please use the product listed in this datasheet within the specified ranges.
- 8. We assume no responsibility for damage or loss due to abnormal use.
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ESD119B1W01005E6327XTSA1 ESD5V0L1B02VH6327XTSA1 ESD7451N2T5G 19180-510 CPDT-5V0USP-HF 3.0SMCJ33CA-F
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