

## Low Consumption Current Regulators

## ■ GENERAL DESCRIPTION

XC6206J series are highly precise, low noise, positive voltage LDO regulators manufactured using CMOS processes. The series achieves very low supply current, 1.0  $\mu\text{A}$  (TYP.) and consists of a reference voltage source, an error amplifier, current limit circuit, and a phase compensation circuit plus a driver transistor.

The series is also compatible with low ESR ceramic capacitors ( $C_L$ ), which give added output stability.

The current limiter's fold-back circuit also operates as a short protect for the output current limiter and the output pin.

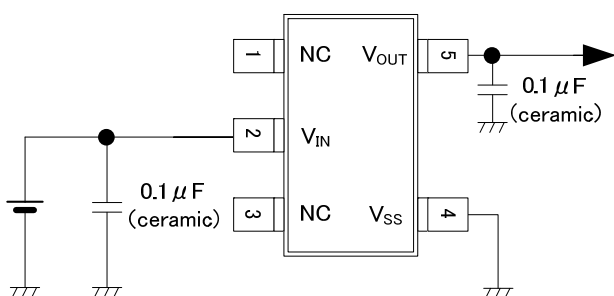
## ■ APPLICATIONS

- Mobile devices / terminals
- Communication equipment  
(Bluetooth, Wi-Fi, GPS etc)
- Module (Communication, Camera, etc)
- Cell phones
- Smartphones

## ■ FEATURES

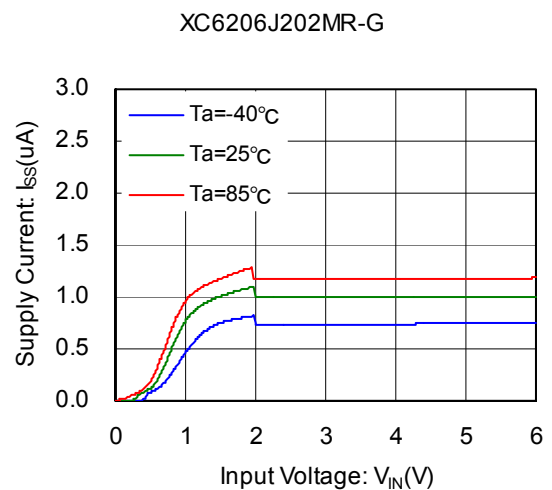
<b>Maximum Output Current</b>	: 200mA
<b>Dropout Voltage</b>	: 200mV@ $I_{OUT}=100\text{mA}$ ( $V_{OUT}=3.0\text{V}$ )
<b>Operating Voltage Range</b>	: 1.5V ~ 6.0V
<b>Output Voltage</b>	: 0.9 ~ 4.0V (0.1V increments)
<b>Low Consumption Current</b>	: 1.0 $\mu\text{A}$ (TYP.)
<b>Protection Circuit</b>	: Current Limit 300mA (TYP.) Short Circuit Protection 50mA (TYP.)
<b>External Capacitor</b>	: 0.1 $\mu\text{F}$ ~ 1.0 $\mu\text{F}$
<b>Operating Ambient Temperature</b>	: -40°C ~ +85°C
<b>Package</b>	: SOT-25-02
<b>Environmentally Friendly</b>	: EU RoHS Compliant, Pb Free

## ■ TYPICAL APPLICATION CIRCUIT



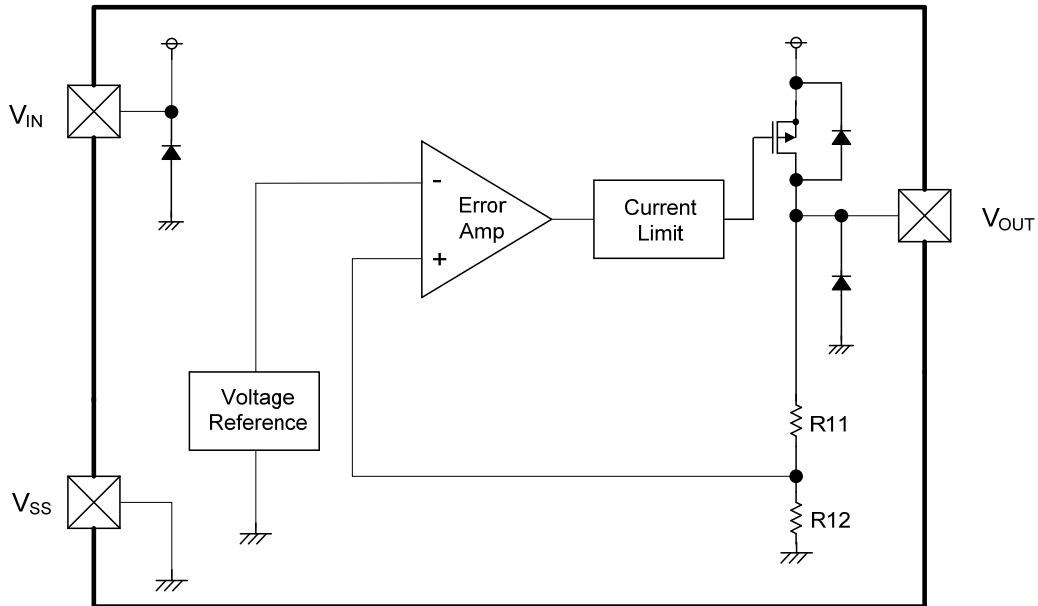
## ■ TYPICAL PERFORMANCE CHARACTERISTICS

- Supply Current vs. Input Voltage



# XC6206J Series

## ■ BLOCK DIAGRAM



\* Diodes inside the circuits are ESD protection diodes and parasitic diodes.

## ■ PRODUCT CLASSIFICATION

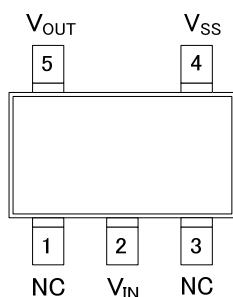
### ● Ordering Information

XC6206J①②③④⑤-⑥

DESIGNATOR	ITEM	SYMBOL	DESCRIPTION
①②	Output Voltage	09~40	Output voltage ex.) 3.0V ⇒ ①= 3, ②= 0
③	Output Voltage Accuracy	2	$\pm 2\%$ ( $V_{OUT(T)} \geq 1.5V$ ), $\pm 30mV$ ( $V_{OUT(T)} < 1.5V$ )
④⑤-⑥ (*1)	Package (Order Unit)	MR-G	SOT-25-02 (3,000/Reel)

(\*1) The "-G" suffix denotes Halogen and Antimony free as well as being fully EU RoHS compliant.

## ■ PIN CONFIGURATION



SOT-25-02  
(TOP VIEW)

## ■ PIN ASSIGNMENT

PIN NUMBER	PIN NAME	FUNCTION
SOT-25-02		
1	NC	No Connection
2	V <sub>IN</sub>	Power Supply Input
3	NC	No Connection
4	V <sub>SS</sub>	Ground
5	V <sub>OUT</sub>	Output

## ■ ABSOLUTE MAXIMUM RATINGS

T<sub>a</sub>=25°C

PARAMETER	SYMBOL	RATINGS	UNITS	
Input Voltage	V <sub>IN</sub>	- 0.3 ~ + 7.0	V	
Output Current	I <sub>OUT</sub>	500 <sup>(*)1</sup>	mA	
Output Voltage	V <sub>OUT</sub>	- 0.3 ~ V <sub>IN</sub> + 0.3 or +7.0 <sup>(*)2</sup>	V	
Power Dissipation <sup>(*)1</sup>	SOT-25-02	P <sub>d</sub>	250	mW
Operating Ambient Temperature	T <sub>opr</sub>	-40~+85	°C	
Storage Temperature	T <sub>stg</sub>	-55~+125	°C	

All voltages are described based on V<sub>SS</sub>.

<sup>(\*)1</sup> I<sub>OUT</sub> ≤ Pd / (V<sub>IN</sub> - V<sub>OUT</sub>)

<sup>(\*)2</sup> The maximum value should be either V<sub>IN</sub>+0.3 or +7.0 in the lowest.

## ELECTRICAL CHARACTERISTICS

Ta=25°C

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS	CIRCUIT
Output Voltage	V <sub>OUT(E)</sub> <sup>(*2)</sup>	V <sub>IN</sub> =V <sub>OUT(T)</sub> <sup>(*1)</sup> +1.0V、I <sub>OUT</sub> =1mA	E-1 <sup>(*4)</sup>	V <sub>OUT(T)</sub>	E-1 <sup>(*4)</sup>	V	①
Load Regulation	ΔV <sub>OUT</sub>	0.9V ≤ V <sub>OUT(T)</sub> < 1.3V V <sub>IN</sub> =V <sub>OUT(T)</sub> +1.5V 1mA ≤ I <sub>OUT</sub> ≤ 100mA	-	15	70	mV	①
		V <sub>OUT(T)</sub> ≥ 1.3V V <sub>IN</sub> =V <sub>OUT(T)</sub> +1.0V 1mA ≤ I <sub>OUT</sub> ≤ 100mA					
Dropout Voltage1	V <sub>dif1</sub> <sup>(*3)</sup>	I <sub>OUT</sub> =30mA	-	E-2 <sup>(*5)</sup>		mV	①
Dropout Voltage2	V <sub>dif2</sub> <sup>(*3)</sup>	I <sub>OUT</sub> =100mA	-	E-3 <sup>(*5)</sup>		mV	①
Supply Current	I <sub>SS</sub>	V <sub>IN</sub> =V <sub>OUT(T)</sub> +1.0V	-	1.0	5.0	μA	②
Line Regulation	ΔV <sub>OUT</sub> / (ΔV <sub>IN</sub> · V <sub>OUT</sub> )	V <sub>OUT(T)</sub> =0.9V 1.5V ≤ V <sub>IN</sub> ≤ 6.0V I <sub>OUT</sub> =1mA	-	0.05	0.15	% / V	①
		V <sub>OUT(T)</sub> ≥ 1.0V V <sub>OUT(T)</sub> +0.5V ≤ V <sub>IN</sub> ≤ 6.0V I <sub>OUT</sub> =1mA					
Input Voltage	V <sub>IN</sub>	-	1.5	-	6	V	-
Output Voltage Temperature Characteristics	ΔV <sub>OUT</sub> / (ΔT <sub>opr</sub> · V <sub>OUT</sub> )	V <sub>IN</sub> =V <sub>OUT(T)</sub> +1.0V I <sub>OUT</sub> =1mA -40°C ≤ T <sub>opr</sub> ≤ 85°C	-	±100	-	ppm/°C	①
Current Limit	I <sub>LIM</sub>	V <sub>OUT</sub> =V <sub>OUT(E)</sub> ×0.95 0.9V ≤ V <sub>OUT(T)</sub> ≤ 1.5V V <sub>IN</sub> =2.6V	200	300	-	mA	①
		V <sub>OUT</sub> =V <sub>OUT(E)</sub> ×0.95 V <sub>OUT(T)</sub> ≥ 1.6V V <sub>IN</sub> =V <sub>OUT(T)</sub> +1.0V	250	300	-		
Short Current	I <sub>SHORT</sub>	V <sub>IN</sub> =V <sub>OUT(T)</sub> +1.0V、V <sub>OUT</sub> =0V	-	50	-	mA	①

Notes:

<sup>(\*1)</sup> V<sub>OUT(T)</sub> is Nominal output voltage

<sup>(\*2)</sup> V<sub>OUT(E)</sub> is Effective output voltage

(i.e. the output voltage when "(V<sub>OUT(T)</sub> + 1.0V)" is provided at the V<sub>IN</sub> pin while maintaining a certain I<sub>OUT</sub> value.

<sup>(\*3)</sup> V<sub>dif</sub>={V<sub>IN1</sub>-V<sub>OUT1</sub>}

V<sub>IN1</sub> is the input voltage when V<sub>OUT1</sub> appears at the V<sub>OUT</sub> pin while input voltage is gradually decreased.

V<sub>OUT1</sub> is the voltage equal to 98% of the normal output voltage when amply stabilized V<sub>OUT(T)</sub>+1.0V is input at the V<sub>IN</sub> pin.

<sup>(\*4)</sup> Refer to the VOLTAGE CHART.

<sup>(\*5)</sup> V<sub>OUT(T)</sub> ≥ 1.5V → Accuracy ±2.0%

V<sub>OUT(T)</sub> < 1.5V → MIN : V<sub>OUT(T)</sub> - 30mV, MAX : V<sub>OUT(T)</sub> + 30mV.

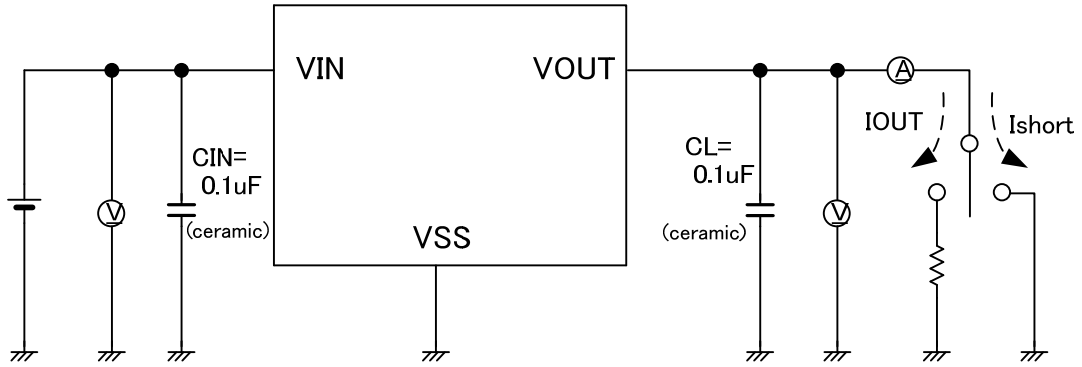
■ **VOLTAGE CHART**

Ta=25°C

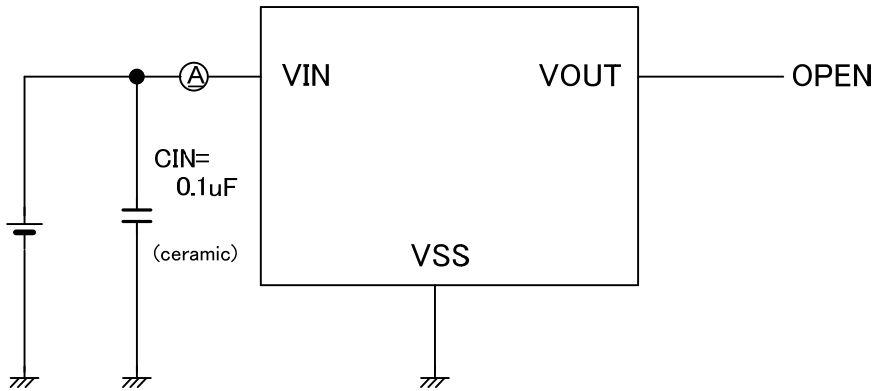
NOMINAL OUTPUT VOLTAGE	E-1		E-2		E-3	
	OUTPUT VOLTAGE (V)		DROPOUT VOLTAGE1 (mV)		DROPOUT VOLTAGE2 (mV)	
	V <sub>OUT(E)</sub>		V <sub>dif1</sub>		V <sub>dif2</sub>	
V <sub>OUT(T)</sub>	MIN.	MAX.	TYP.	MAX.	TYP.	MAX.
0.9	0.870	0.930	460	900	850	1450
1.0	0.970	1.030	370	760	750	1300
1.1	1.070	1.130	300	650	680	1190
1.2	1.170	1.230	240	540	600	1070
1.3	1.270	1.330	190	420	530	950
1.4	1.370	1.430	160	340	470	840
1.5	1.470	1.530				
1.6	1.568	1.632				
1.7	1.666	1.734	110	200	350	610
1.8	1.764	1.836				
1.9	1.862	1.938				
2.0	1.960	2.040	90	150	290	480
2.1	2.058	2.142				
2.2	2.156	2.244				
2.3	2.254	2.346				
2.4	2.352	2.448				
2.5	2.450	2.550	70	120	240	370
2.6	2.548	2.652				
2.7	2.646	2.754				
2.8	2.744	2.856				
2.9	2.842	2.958				
3.0	2.940	3.060	60	100	200	320
3.1	3.038	3.162				
3.2	3.136	3.264				
3.3	3.234	3.366				
3.4	3.332	3.468				
3.5	3.430	3.570				
3.6	3.528	3.672				
3.7	3.626	3.774				
3.8	3.724	3.876				
3.9	3.822	3.978				
4.0	3.920	4.080				

## TEST CIRCUITS

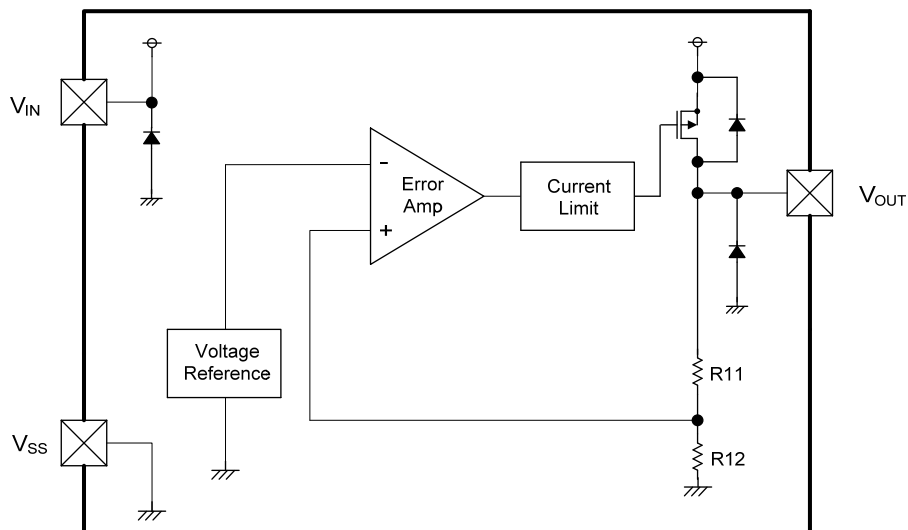
● Circuit ①



● Circuit ②



## OPERATIONAL EXPLANATION



The XC6206J series consists of a reference voltage source, an error amplifier, current limit circuit, and a phase compensation circuit plus a driver transistor. The voltage, divided by resistors R11 & R12, which are connected to the  $V_{OUT}$  pin is compared with the internal reference voltage by the error amplifier. The P-channel MOSFET, which is connected to the  $V_{OUT}$  pin, is then driven by the subsequent output signal. The output voltage at the  $V_{OUT}$  pin is controlled & stabilized by negative feedback. The current limit circuit and short circuit protection operate in relation to the level of output current.

### <Current Limit, Short-Circuit Protection>

The XC6206J series includes a current limit circuit, which aid the operations of the current limiter and short-circuit protection. When the load current reaches the current limit level (300mA, TYP.), the current limiter circuit operates and output voltage drops. The circuit operates to decrease the current limit as the load impedance decreases further and the output voltage drops. When the output pin is shorted, a current of about 50mA flows.

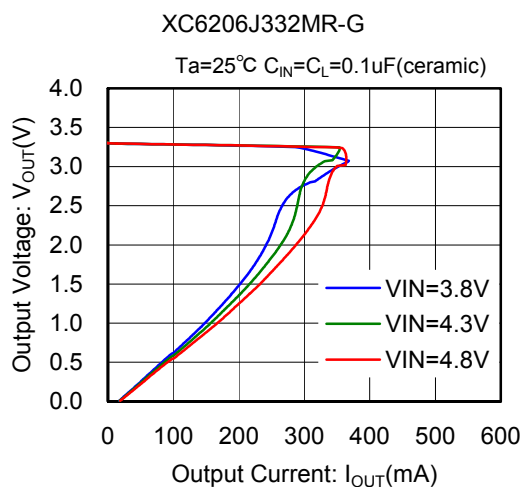
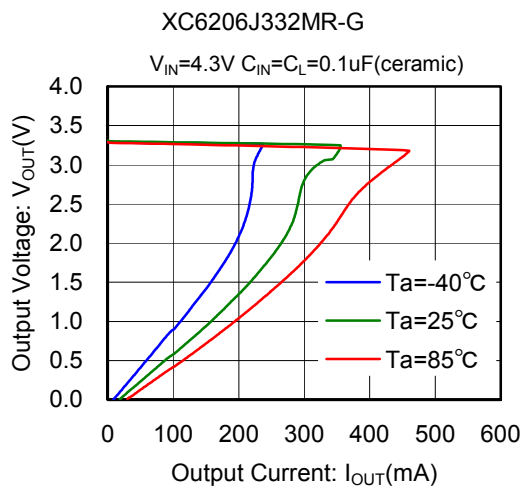
## ■ NOTES ON USE

1. For temporary, transitional voltage drop or voltage rising phenomenon, the IC is liable to malfunction should the ratings be exceeded.
2. Where wiring impedance is high, operations may become unstable due to noise and/or phase lag depending on output current.
3. The XC6206J series oscillates normally even without an input capacitor,  $C_{IN}$ , or an output capacitor,  $C_L$ , because the series compensates by the phase compensation circuit. However, when an input wiring is long, about  $0.1\ \mu F$  to  $1.0\ \mu F$  of the input capacitor,  $C_{IN}$ , is required for stabilizing input. When an under-shoot or over-shoot is large at transient response, about  $0.1\ \mu F$  to  $1.0\ \mu F$  of output capacitor, which prevents output fluctuation occurred by load fluctuation, is also recommended. Please wire the input capacitor ( $C_{IN}$ ) and the output capacitor ( $C_L$ ) as close to the IC as possible. Please wire the input capacitor ( $C_{IN}$ ) and the output capacitor ( $C_L$ ) as close to the IC as possible.
4. When the input voltage starts from 0V, over-shoot may occur because of the slope of the input rising. In order to avoid the over-shoot, please use the IC by setting the slope of the input rising within 0.1V/ms.
5. Torex places an importance on improving our products and their reliability.  
We request that users incorporate fail-safe designs and post-aging prevention treatment when using Torex products in their systems.

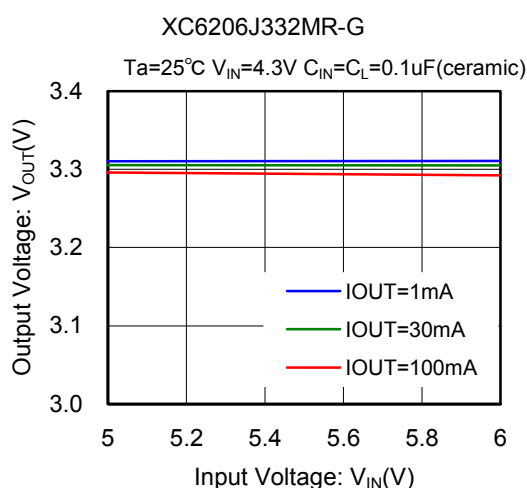
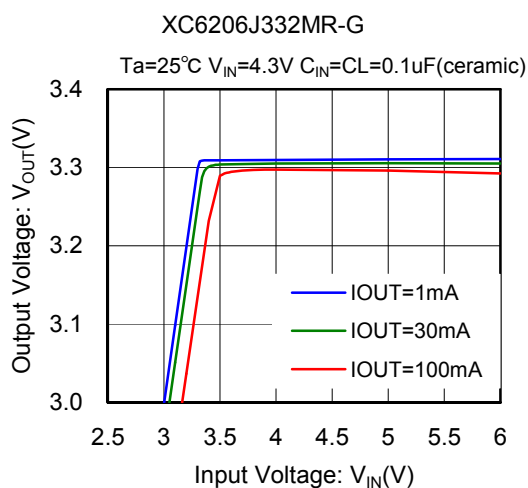


## ■ TYPICAL PERFORMANCE CHARACTERISTICS

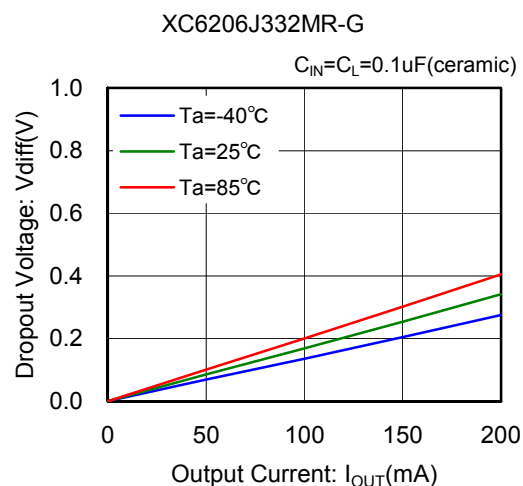
(1) Output Voltage vs. Output Current



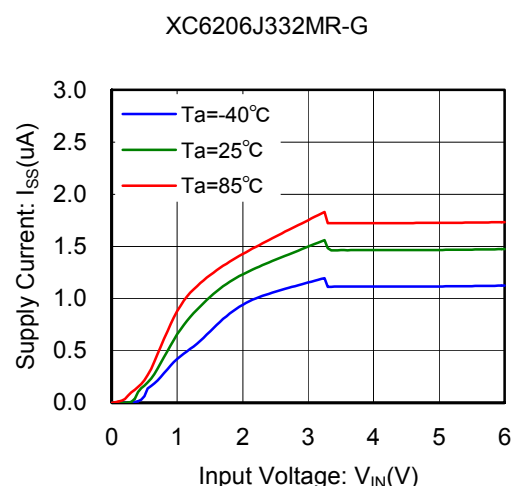
(2) Output Voltage vs. Input Voltage



(3) Dropout Voltage vs. Output Current



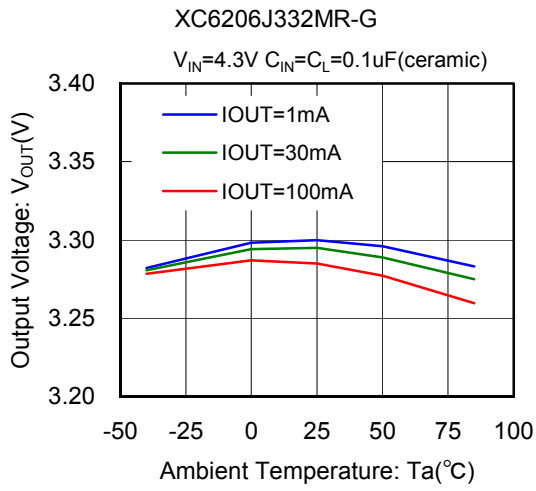
(4) Supply Current vs. Input Voltage



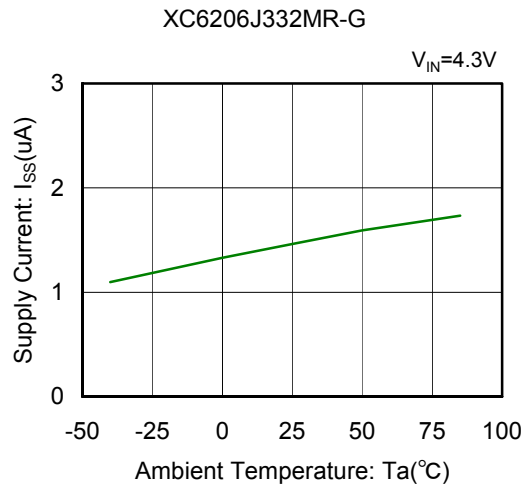
# XC6206J Series

## TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(5) Output Voltage vs. Ambient Temperature



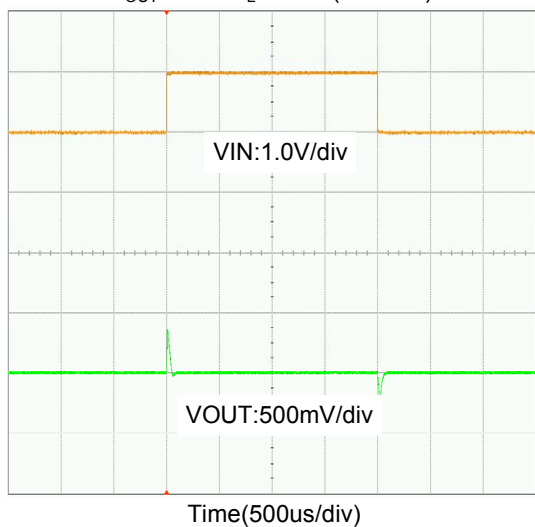
(6) Supply Current vs. Ambient Temperature



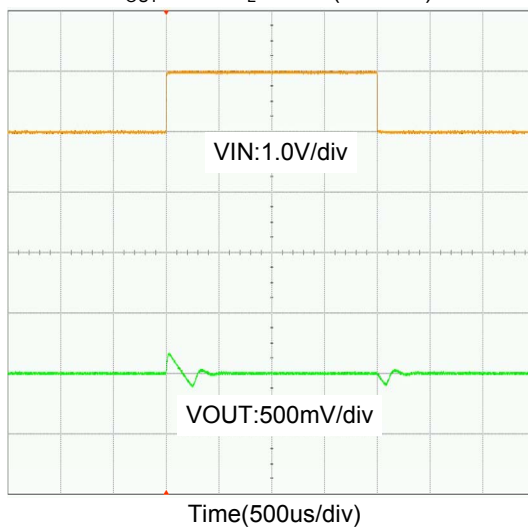
■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(7) Input Transient Response

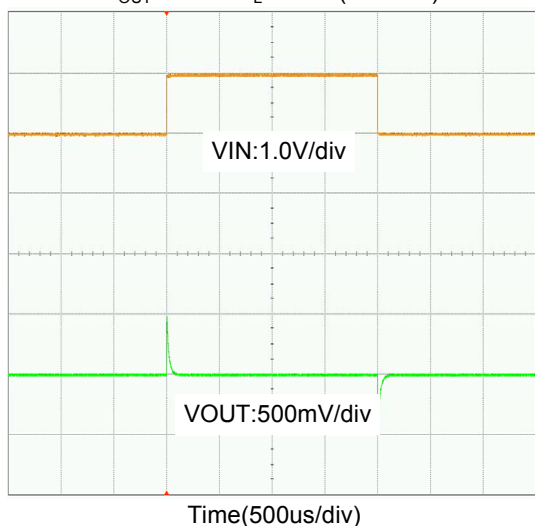
XC6206J332MR-G  
tr=tf=5us I<sub>OUT</sub>=1mA C<sub>L</sub>=0.1uF(ceramic) Ta=25°C



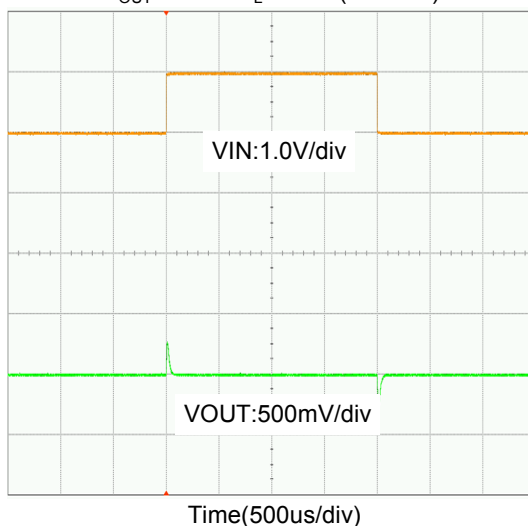
XC6206J332MR-G  
tr=tf=5us I<sub>OUT</sub>=1mA C<sub>L</sub>=1.0uF(ceramic) Ta=25°C



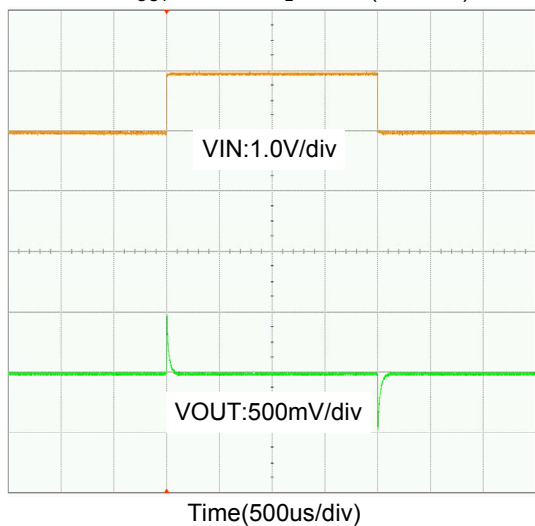
XC6206J332MR-G  
tr=tf=5us I<sub>OUT</sub>=50mA C<sub>L</sub>=0.1uF(ceramic) Ta=25°C



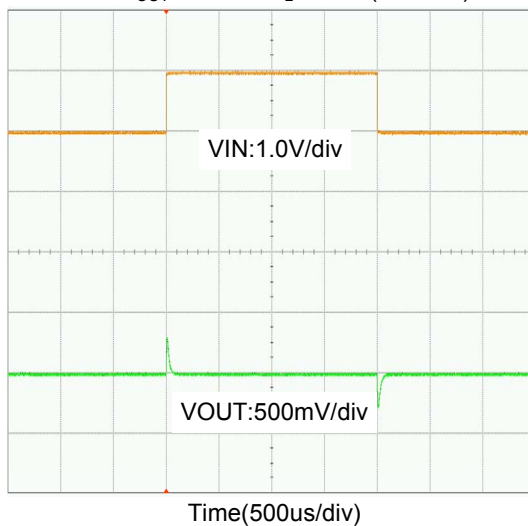
XC6206J332MR-G  
tr=tf=5us I<sub>OUT</sub>=50mA C<sub>L</sub>=1.0uF(ceramic) Ta=25°C



XC6206J332MR-G  
tr=tf=5us I<sub>OUT</sub>=100mA C<sub>L</sub>=0.1uF(ceramic) Ta=25°C

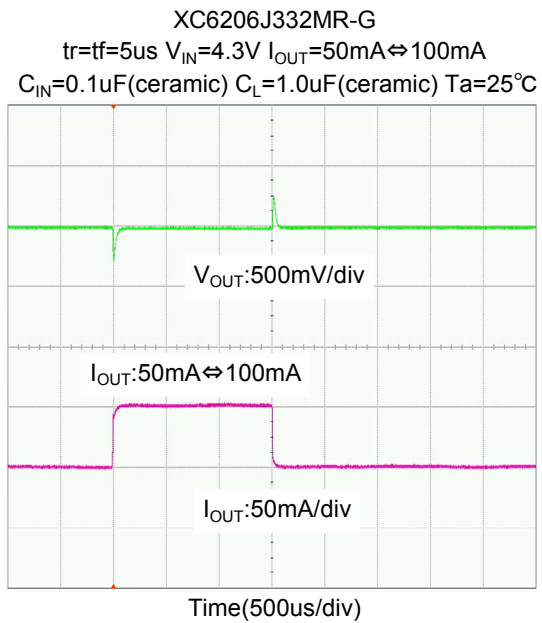
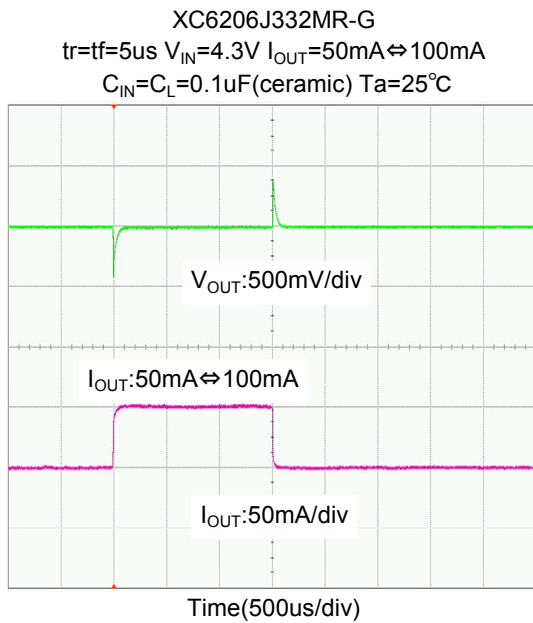
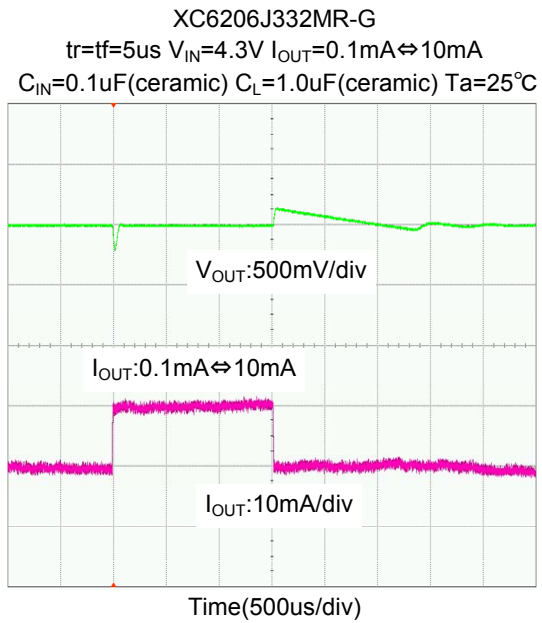
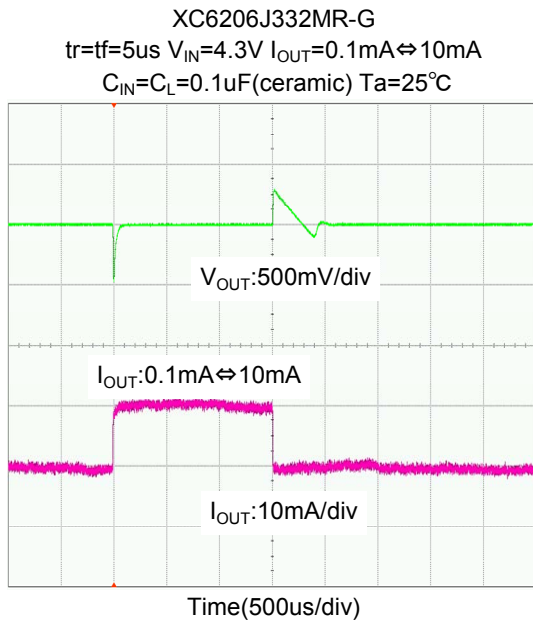


XC6206J332MR-G  
tr=tf=5us I<sub>OUT</sub>=100mA C<sub>L</sub>=1.0uF(ceramic) Ta=25°C



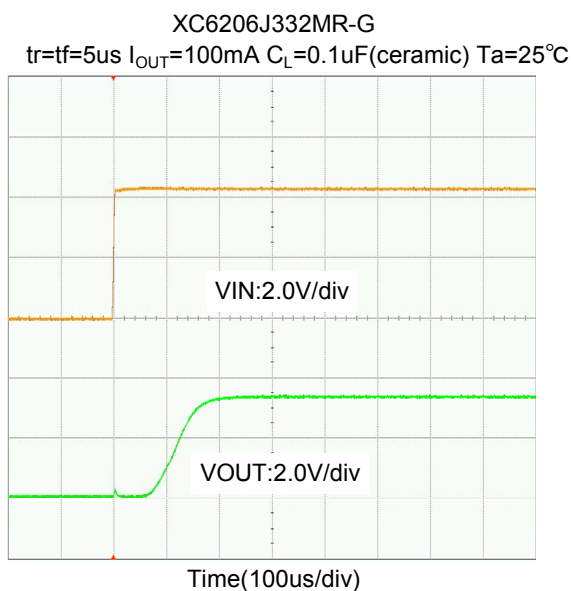
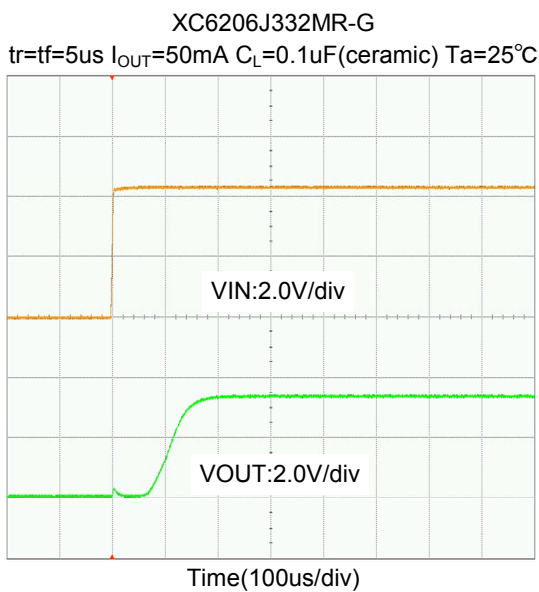
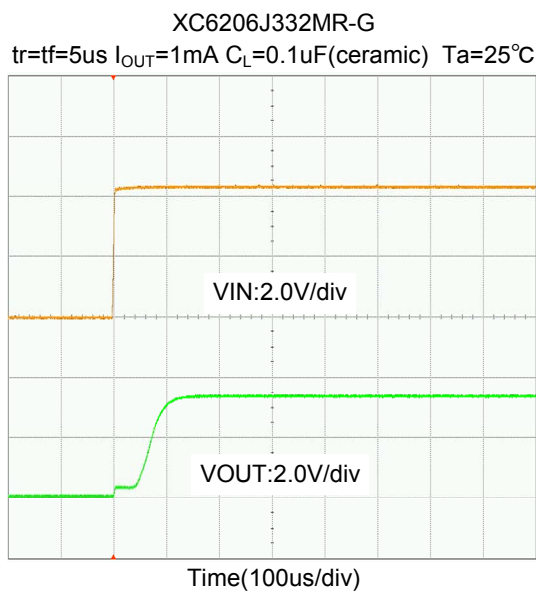
## TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

### (8) Load Transient Response

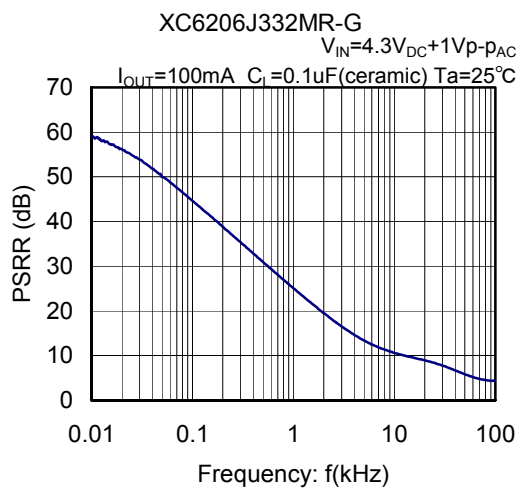


## ■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

### (9) Input Turn-On Transient Response



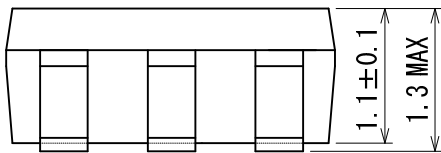
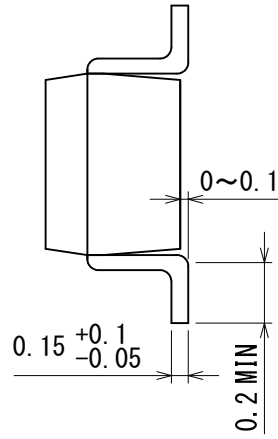
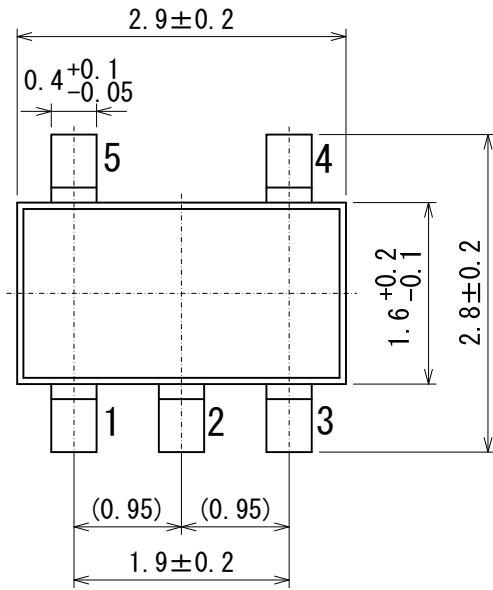
### (10) Ripple Rejection Rate



# XC6206J Series

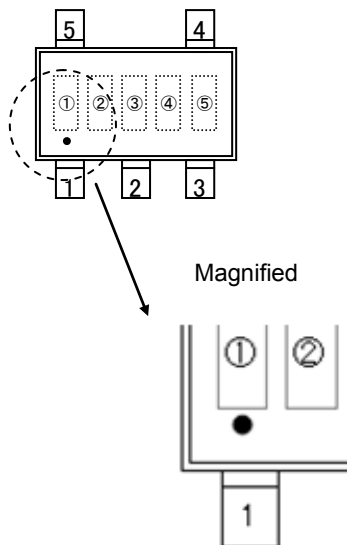
## PACKAGING INFORMATION

●SOT-25-02 (unit: mm)



## MARKING RULE

SOT-25-02 (Under dot)



\* SOT-25-02 with the under-dot marking is used.

① represents products series

MARK	PRODUCT SERIES
A	XC6206J*****-G

② represents type of regulator

MARK	OUTPUT VOLTAGE (V)	PRODUCT SERIES
1	0.9/1.0/1.2/1.5/1.8/2.0/2.5/2.8/3.0/3.3/3.5/3.6/3.7/3.8/3.9/4.0	XC6206J*****-G
2	1.1/1.3/1.4/1.6/1.7/1.9/2.1/2.2/2.3/2.4/2.6/2.7/2.9/3.1/3.2/3.4	

③ represents output voltage

MARK	OUTPUT VOLTAGE (V)		MARK	OUTPUT VOLTAGE (V)	
	MARK②=1	MARK②=2		MARK②=1	MARK②=2
A	1.2	1.4	L	3.6	2.6
B	1.5	1.6	M	3.5	2.7
C	1.8	1.7	N	3.7	2.9
D	2.0	1.9	P	3.8	3.1
E	2.5	2.1	R	3.9	3.2
F	2.8	2.2	S	4.0	3.4
H	3.0	2.3	T	0.9	1.1
K	3.3	2.4	U	1.0	1.3

④,⑤ represents production lot number

01~09, 0A~0Z, 11~9Z, A1~A9, AA~AZ, B1~ZZ in order.

(G, I, J, O, Q, W excluded)

\*No character inversion used.

1. The product and product specifications contained herein are subject to change without notice to improve performance characteristics. Consult us, or our representatives before use, to confirm that the information in this datasheet is up to date.
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