ETR0305 008

# Low ESR Cap. Compatible Positive Voltage Regulators

#### **■**GENERAL DESCRIPTION

The XC6206 series are highly precise, low power consumption, 3 terminal, positive voltage regulators manufactured using CMOS and laser trimming technologies. The series provides large currents with a significantly small dropout voltage.

The XC6206 consists of a current limiter circuit, a driver transistor, a precision reference voltage and an error correction circuit. The series is compatible with low ESR ceramic capacitors. The current limiter's foldback circuit operates as a short circuit protection as well as the output current limiter for the output pin.

Output voltages are internally by laser trimming technologies. It is selectable in 0.1V increments within a range of 1.2V to 5.0V. SOT-23, SOT-89 and USP-6B packages are available.

#### ■APPLICATIONS

- Smart phones / Mobile phones
- Portable game consoles
- Digital still cameras / Camcorders
- Digital audio equipments
- Reference voltage sources
- Multi-function power supplies

#### **■**FEATURES

Maximum Output Current : 200mA (3.0V type)

Dropout Voltage : 250mV @ 100mA (3.0V type)

Maximum Operating Voltage : 6.0V

Output Voltage Range : 1.2V ~ 5.0V (0.1V increments)

**Highly Accurate**  $:\pm 2\% @V_{OUT} \ge 1.5V$ 

<u>+</u>30mV@VouT<1.5V (<u>+</u>1% @VouT≥2.0V)

Low Power Consumption : 1.0µA (TYP.)

Low ESR Capacitor : Ceramic capacitor compatible
Protection : Current Limit Circuit Built-in

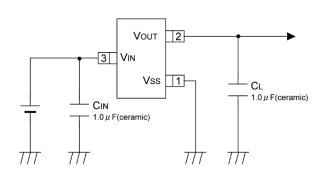
Operating Ambient Temperature:  $-40^{\circ}$ C~  $+85^{\circ}$ C Packages : SOT-23

SOT-89

USP-6B

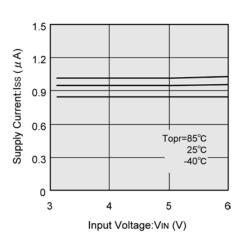
Environmentally Friendly : EU RoHS Compliant, Pb Free

# **■**TYPICAL APPLICATION CIRCUIT

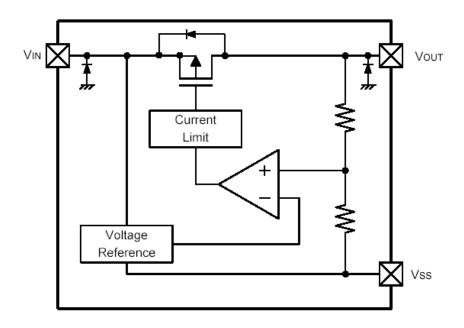


# ■TYPICAL PERFORMANCE CHARACTERISTICS

XC6206P302



### **■BLOCK DIAGRAM**



<sup>\*</sup>Diodes inside the circuit are an ESD protection diode and a parasitic diode.

# **■**PRODUCT CLASSIFICATION

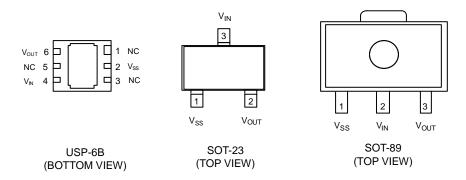
#### Ordering Information

 $\underline{\mathsf{XC6206P} \underbrace{12345} - \underline{6}}^{(*1)}$ 

DESIGNATOR	ITEM	SYMBOL	DESCRIPTION
12	Output Voltage	12~50	e.g. Vouт: 3.0V→①=3, ②=0
3	Accuracy	2	±2% (Vouт≧1.5V), ±30mV (Vouт<1.5V)
3	Accuracy	1	±1% (Vouт≧2.0V)
		MR	SOT-23 (3,000pcs/Reel)
		MR-G	SOT-23 (3,000pcs/Reel)
<b>4</b> (5)-6)	Packages	PR	SOT-89 (1,000pcs/Reel)
40-6	(Order Unit)	PR-G	SOT-89 (1,000pcs/Reel)
		DR	USP-6B (3,000pcs/Reel)
		DR-G	USP-6B (3,000pcs/Reel)

<sup>(\*1)</sup> The "-G" suffix denotes Halogen and Antimony free as well as being fully EU RoHS compliant.

### **■PIN CONFIGURATION**



<sup>\*</sup>The dissipation pad for the USP-6B package should be solder-plated in recommended mount pattern and metal masking so as to enhance mounting strength and heat release.

## **■**PIN ASSIGNMENT

F	IN NUMBER		PIN NAME	FUNCTIONS
SOT-23	SOT-89	USP-6B	FIN NAIVIE	FONCTIONS
1	1	2	Vss	Ground
3	2	4	Vin	Power Input
2	3	6	Vout	Output
-	-	1, 3, 5	NC	No Connection

### ■ABSOLUTE MAXIMUM RATINGS

Ta=25°C

PARAMET	PARAMETER		RATINGS	UNITS	
Input Volta	Input Voltage		-0.3~+7.0	V	
Output Cu	rrent	Іоит	500 (*1)	mA	
Output Vol	tage	Vout	-0.3 ~ V <sub>IN</sub> + 0.3	V	
	SOT-23		250		
	301-23		500(40mm x 40mm Standard board) (*2)		
Dawar Dissipation	SOT-89	Pd	500	m\//	
Power Dissipation	301-69	Pu	1000(40mm x 40mm Standard board) (*2)	mW	
	USP-6B		120		
	USP-0B		1000(40mm x 40mm Standard board) (*2)		
Operating Ambient Temperature		Topr	- 40 ~ + 85	°C	
Storage Temp	erature	Tstg	- 55 ~ <b>+</b> 125	°C	

 $<sup>^{(*1)}</sup>$  Iout $\leq$  Pd / (Vin-Vout)

The mounting condition is please refer to PACKAGING INFORMATION.

If the pad needs to be connected to other pins, it should be connected to the pin number 4 (V<sub>IN</sub>).

 $<sup>^{(*2)}</sup>$  The power dissipation figure shown is PCB mounted and is for reference only.

# **■**ELECTRICAL CHARACTERISTICS

Ta=25°C

PARAMETER	SYMBOL	CONE	DITIONS	MIN.	TYP.	MAX.	UNITS	CIRCUIT
Output Voltage		Іонт=30mА	V <sub>OUT(T)</sub> <1.5V	-0.03		+0.03		
(Standard)(*2)	V <sub>OUT(E)</sub> (*3)	1001=30IIIA	V <sub>OUT(T)</sub> ≧1.5V	×0.98	V <sub>OUT(T)</sub> (*4)	×1.02	V	2
Output Voltage (High Accuracy)(*2)	V OUT(E)	Іоит=30mА	V <sub>OUT(T)</sub> ≧2.0V	×0.99	VOOT(I)	×1.01	v	
Supply Current	I <sub>DD</sub>			-	1.0	3.0	μA	1
Load Regulation	ΔVουτ	V <sub>OUT(T)</sub> ≦1.8 1mA≦I <sub>OUT</sub> ≦		_	_	E-1 <sup>(*5)</sup>	mV	2
Load Negulation	AVOUT	V <sub>OUT(T)</sub> >1.8V 1mA≦I <sub>OUT</sub> ≦			-	E-1\%	IIIV	<b>&amp;</b>
Dropout Voltage 1	Vdif1 <sup>(*6)</sup>	I <sub>OUT</sub> =30mA		-	E-2	<u>o</u> (*5)		
Dropout Voltage 2	Vdif2 <sup>(*6)</sup>	V <sub>OUT(T)</sub> ≦1.8	V, I <sub>OUT</sub> =60mA	_	E-3 <sup>(*5)</sup>		mV	2
Diopout voltage 2	Vull2	V <sub>OUT(T)</sub> >1.8V	Vout(t)>1.8V, lout=100mA		L-\	)\ -/		
Line Regulation	ΔV <sub>OUT</sub> /	$V_{OUT(T)}$ <4.5V, $V_{OUT(T)}$ +1.0V $\leq$ V <sub>IN</sub> $\leq$ 6.0V, $I_{OUT}$ =30mA $V_{OUT(T)}$ $\geq$ 4.5V, 5.5V $\leq$ V <sub>IN</sub> $\leq$ 6.0V, $I_{OUT}$ =30mA			- 0.05		%/V	2
Line Regulation	(ΔV <sub>IN</sub> • V <sub>OUT</sub> )				0.00	0.25	767 V	<b>(2</b> )
Maximum Output Current	IOUTMAX	V <sub>OUT</sub> ≧V <sub>OUT(E)</sub> × 0.9		E-4 <sup>(*5)</sup>	-	-	mA	2
Short Circuit Current	Ishort	Vout=Vss		-	E-5 <sup>(*5)</sup>	-	mA	2
Input Voltage	Vin			1.8	-	6.0	V	2
Output Voltage Temperature Characteristics	ΔV <sub>ОUТ</sub> / ( <b>ΔTopr ·</b> V <sub>ОUТ</sub> )	lо⊔т=30mA, -40°C≦Topr	≦85°C	-	±100	-	ppm/°C	2

<sup>\*1:</sup> Unless otherwise stated,  $V_{IN} = V_{OUT(T)} + 1.0V$ 

 $V_{OUT1}$ : A voltage equal to 98% of the output voltage whenever an amply stabilized  $\{V_{OUT(T)} + 1.0V\}$  is input with each  $I_{OUT}$ .

V<sub>IN1</sub>: The input voltage when V<sub>OUT1</sub> appears as input voltage is gradually decreased.

<sup>\*2: (</sup>Standard): $\pm 2\%$  (1.5V $\leq$ V<sub>OUT(T)</sub>) ,  $\pm 0.03$ V (1.5V>V<sub>OUT(T)</sub>) (High Accuracy): $\pm 1\%$  (2.0V $\leq$ V<sub>OUT(T)</sub>)

<sup>\*3:</sup> V<sub>OUT(E)</sub> :Effective output voltage.

<sup>\*4:</sup> V<sub>OUT(T)</sub> :Nominal voltage

<sup>\*5:</sup> For E-1,E-2,E-3,E-4,E-5, Please refer to Electrical Characteristics Chart.

<sup>\*6:</sup> Vdif =V<sub>IN1</sub> -V<sub>OUT1</sub>

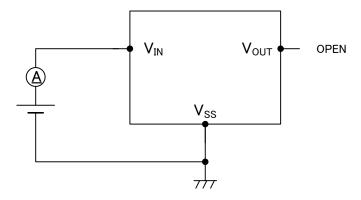
<sup>\*7:</sup> The low ESR capacitors use that is more than 1.0µF as C<sub>L</sub> is possible.

# ■ ELECTRICAL CHARACTERISTICS (Continued) • Electrical Characteristics Chart

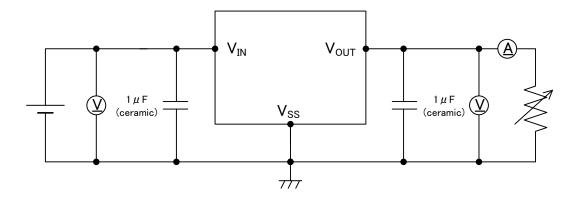
	E-1	E-	-2	Е	:-3	E-4	E-5
NOMINAL VOLTAGE	LOAD REGULATION	DROF VOLTA			POUT TAGE2	MAX. OUTPUT CURRENT	SHORT CURRENT
	∠Vouτ (mV)	V <sub>dif1</sub>			(mV)	IOUTMAX (mA)	I <sub>SHORT</sub> (mA)
V <sub>OUT(T)</sub>	MAX.	TYP.	MAX.	TYP.	MAX.	MIN.	TYP.
1.2		460	760	700	960		
1.3	40	400	650	700	900	00	180
1.4		350	590	500	000	60	
1.5		300	510	580	860		
1.6		250	450	450	040		155
1.7	45	200	410	450	810	80	
1.8		150	390			00	
1.9					780		
2.0							130
2.1							
2.2	50					120	
2.3				350			
2.4		100	370	330			
2.5					710		
2.6							
2.7	55					150	
2.8							
2.9							
3.0							
3.1							
3.2	60						
3.3							
3.4		75	350	250	680	200	
3.5							
3.6	0.5						100
3.7	65						
3.8							
3.9							
4.0							
4.1	70						
4.2	70						
4.3							
4.4		60	320	200	630	250	
4.6						230	
4.6	75						
4.7	75						
4.8							
5.0	80	50	290	175	600	-	
ა.0	ΟU	50	290	170	000		

#### **■TEST CIRCUITS**

#### Circuit ①



#### Circuit 2

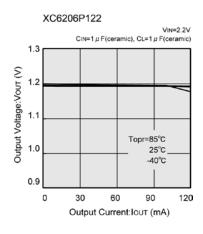


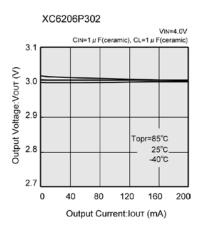
#### ■NOTES ON USE

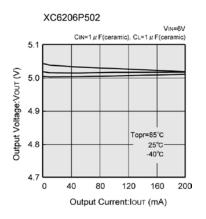
- For temporary, transitional voltage drop or voltage rising phenomenon, the IC is liable to malfunction should the ratings be exceeded.
- 2. Where wiring impedance is high, operations may become unstable due to noise and/or phase lag depending on output current. Please strengthen V<sub>IN</sub> and V<sub>SS</sub> wiring in particular
- 3. Please wire the input capacitor (C<sub>IN</sub>) and the output capacitor (C<sub>L</sub>) as close to the IC as possible.
- 4. Capacitances of these capacitors (C<sub>IN</sub>, C<sub>L</sub>) are decreased by the influences of bias voltage and ambient temperature. Care shall be taken for capacitor selection to ensure stability of phase compensation from the point of ESR influence.
- 5. When it is used in a quite small input / output dropout voltage, output may go into unstable operation. Please test it thoroughly before using it in production.
- 6. Torex places an importance on improving our products and their reliability. We request that users incorporate fail-safe designs and post-aging protection treatment when using Torex products in their systems.

## **■**TYPICAL PERFORMANCE CHARACTERISTICS

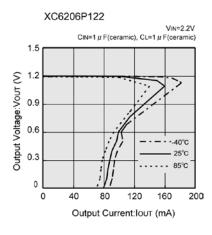
#### (1) Output Voltage vs. Output Current

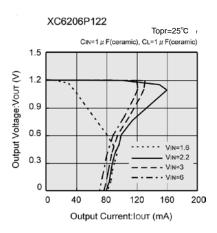


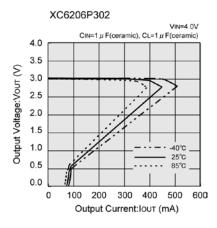


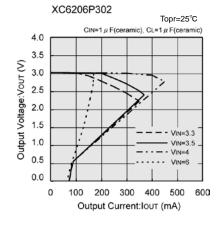


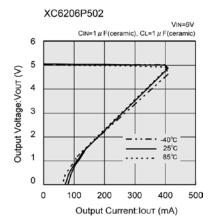
#### (2) Current Limit

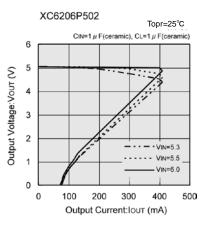








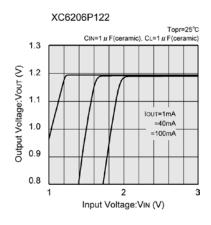


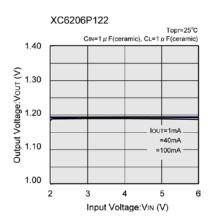


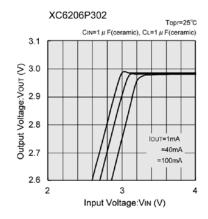
# XC6206 Series

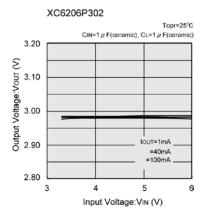
# ■TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

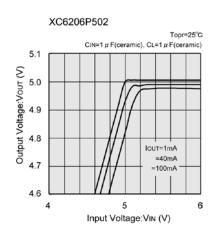
#### (3) Output Voltage vs. Input Voltage

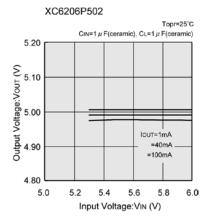




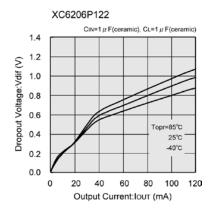


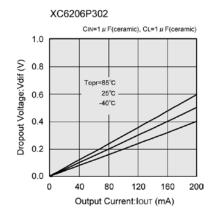


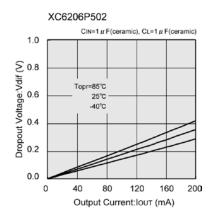




#### (4) Dropout Voltage vs. Output Current

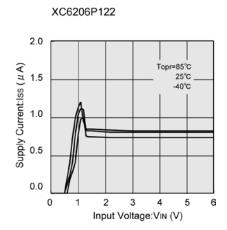


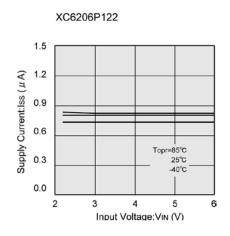


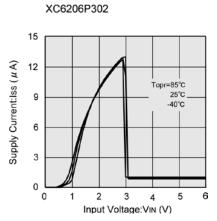


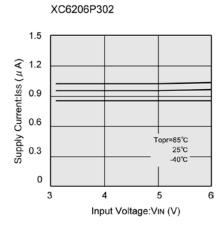
# ■TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

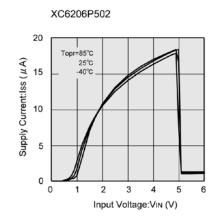
#### (5) Supply Current vs. Input Voltage

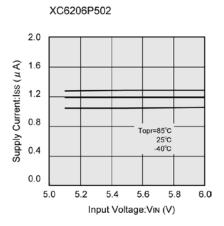




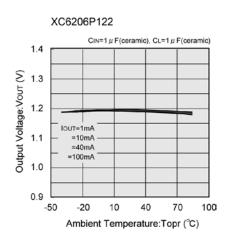


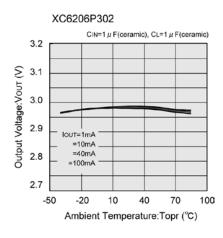


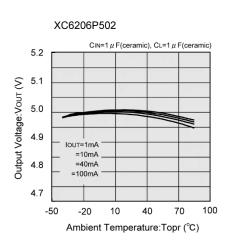




#### (6) Output Voltage vs. Ambient Temperature



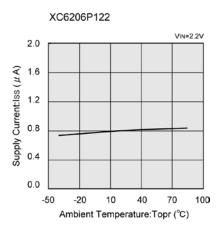


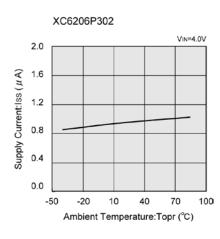


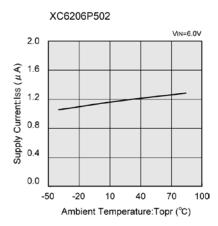
# XC6206 Series

# ■TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

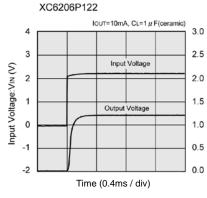
#### (7) Output Voltage vs. Ambient Temperature

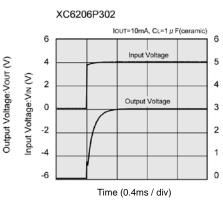


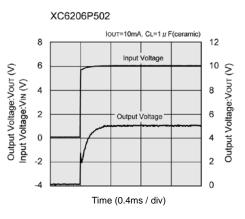




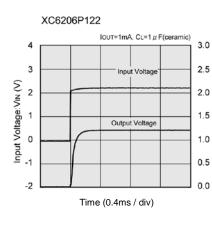
#### (8) Input Transient Response 1

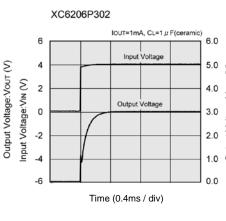


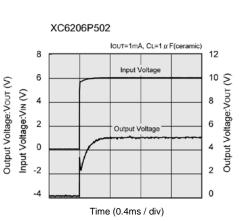




XC6206P122

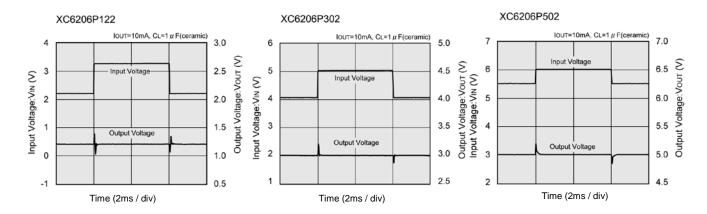


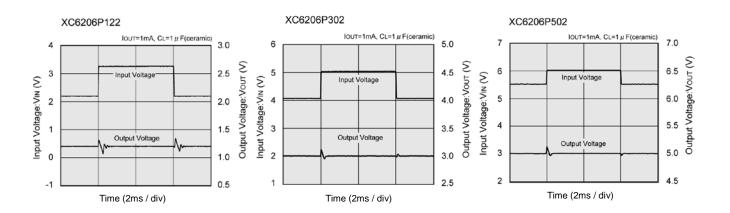




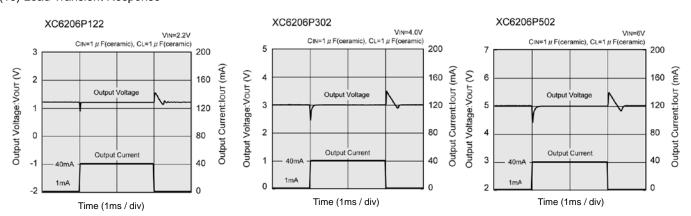
# ■TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

#### (9) Input Transient Response 2



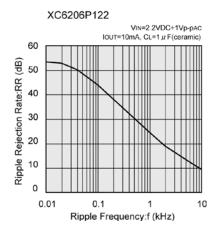


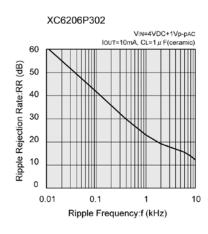
#### (10) Load Transient Response

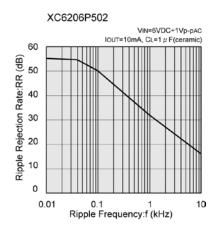


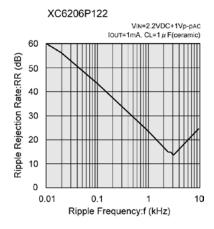
# ■TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

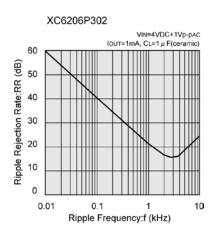
#### (11) Ripple Rejection Rate

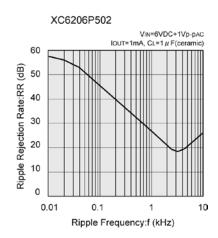












# ■ PACKAGING INFORMATION

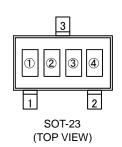
For the latest package information go to, <a href="www.torexsemi.com/technical-support/packages">www.torexsemi.com/technical-support/packages</a>

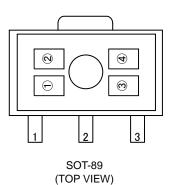
PACKAGE	OUTLIN / LAND PATTERN	THERMAL CHARACTERISTICS		
SOT-23	SOT-23 PKG	Standard Board	SOT-23 Power Dissipation	
SOT-89	SOT-89 PKG	Standard Board	SOT-89 Power Dissipation	
USP-6B	USP-6B PKG	Standard Board	USP-6B Power Dissipation	

# XC6206 Series

## ■MARKING RULE

#### ●SOT-23, SOT-89





#### ${\ensuremath{\mbox{\Large 1}}}$ represents product number

MARK	PRODUCT SERIES
6	XC6206P****

#### 2 represents 3 pins regulator

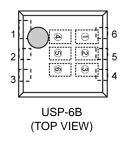
MA	PRODUCT SERIES	
VOLTAGE=0.1 ~ 3.0V	PRODUCT SERIES	
5	6	XC6206P****

#### 3 represents output voltage

MARK	VC	DLTAGE (	(V)	MARK	OUTPL	JT VOLTA	GE (V)
0	-	3.1	-	F	1.6	4.6	-
1	-	3.2	-	Н	1.7	4.7	-
2	-	3.3	-	K	1.8	4.8	-
3	-	3.4	1	L	1.9	4.9	1
4	-	3.5	1	М	2.0	5.0	1
5	-	3.6	-	N	2.1	-	-
6	-	3.7	-	Р	2.2	-	1
7	-	3.8	ı	R	2.3	-	1
8	-	3.9	ı	S	2.4	-	1
9	-	4.0	-	Т	2.5	-	-
Α		4.1	-	U	2.6	-	-
В	1.2	4.2	-	V	2.7	-	-
С	1.3	4.3	-	Х	2.8	-	1
D	1.4	4.4	-	Υ	2.9	-	1
Е	1.5	4.5	-	Z	3.0	-	-

#### 4 represents production lot number 0 to 9, A to Z repeated. (G, I, J, O, Q, W excluded)

#### ●USP-6B



#### ①② represents product number

MA	DDODLICT CEDIEC	
1	2	PRODUCT SERIES
0	6	XC6206P***D*

#### 3 represents 3 pins regulator

MARK	PRODUCT SERIES
Р	XC6206P***D*

#### 45 represents output voltage

MAI	RK	OUTPUT VOLTAGE(V)	PRODUCT SERIES
4	5	OUTFUT VOLIAGE(V)	PRODUCT SERIES
3	3	3.3	XC6206P33*D*
5	0	5.0	XC6206P50*D*

#### 6 represents production lot number

0 to 9, A to Z repeated. (G, I, J, O, Q, W excluded)

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