

XP231N0201TR-G

N-channel MOSFET 30V, 0.2A

ETR11041-001a

■FEATURES

On-State Resistance : $R_{DS}(on) = 5.0\Omega @V_{GS} = 4.5V$

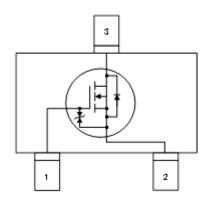
Driving voltage : 2.5V

Environmentally Friendly : EU RoHS Compliant, Pb Free

■ APPLICATIONS

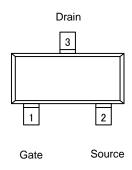
Switching

■EQUIVALENT CIRCUIT



■ PIN CONFIGURATION

●SOT-23(TO-236)



■PRODUCT NAME

PRODUCT NAME	PACKAGE	ORDER UNIT
XP231N0201TR-G *	SOT-23(TO-236)	3,000 pcs/ Reel

^{*} The "-G" suffix denotes Halogen and Antimony free as well as being fully EU RoHS compliant

■ABSOLUTE MAXIMUM RATINGS

Ta=25°C

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	V _{DSS}	30	V
Gate-Source Voltage	V _{GSS}	±20	V
Drain Current (DC)	l _D	0.2	Α
Drain Current(Pulse) (*1)	I _{DP}	0.4	Α
Channel Power Dissipation (*2)	P_d	0.4	W
Junction Temperature	TJ	150	°C
Storage Temperature	T_{stg}	-55~150	°C

 $^{^{(^*1)}}$ PW \leq 10 μ s,duty cycle \leq 1%

^(*2)When implemented on a PCB defined by JESD51-7

XP231N0201TR-G

■ELECTRICAL CHARACTERISTICS

Ta=25°C

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{(BR)DSS}$ $I_D=250\mu A, V_{GS}=0V$		-	-	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} = 30V, V _{GS} = 0V	-	-	1	μΑ
Gate-Source Leakage Current	Igss	V _{GS} = ±20V, V _{DS} = 0V	-	-	±10	μΑ
Gate Threshold Voltage	$V_{GS(off)}$	I _D = 250uA, V _{DS} = V _{GS}	0.8	1.3	1.8	V
Drain-Source On Resistance	D	V _{GS} = 4.5V, I _D = 10mA	-	3	5	Ω
	R _{DS(on)}	V _{GS} = 2.5V, I _D = 10mA	-	5.5	11	Ω
Input Capacitance	Ciss	\/ 40\/ \/ 0\/	-	6.5	-	pF
Output Capacitance	Coss	V _{DS} = 10V, V _{GS} = 0V f= 1MHz	-	2.5	-	рF
Reverse Transfer Capacitance	Crss		-	1.2	-	pF
Turn-on Delay Time	t _{d(on)}		-	7	-	ns
Rise Time	t _r	V _{DD} = 10V, I _D = 100mA	-	5	-	ns
Turn-off Delay Time	t _{d(off)}	V _{GS} = 10V	-	20	-	ns
Fall Time	t _f		-	8	-	ns
Total Gate Charge	Qg	\/ 40\/ L 400~~A	-	0.18	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} = 10V, I _D = 100mA	-	0.03	-	nC
Gate-Drain Charge	Q_gd	V _{GS} = 10V	-	0.08	-	nC
Diode Forward Voltage	V _{SD}	Is= 100mA, V _{GS} = 0V	-	0.8	1.2	V

■NOTES ON USE

1. Please use this IC within the absolute maximum ratings.

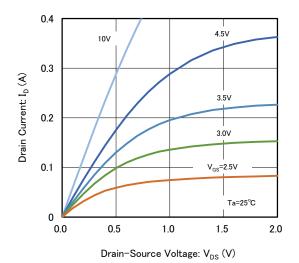
Even within the ratings, in case of high load use continuously such as high temperature, high voltage, high current and thermal stress may cause reliability degradation of the IC.

2. Torex places an importance on improving our products and their reliability.

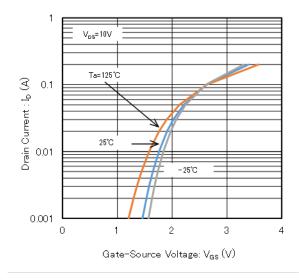
We request that users incorporate fail-safe designs and post-aging protection treatment when using Torex products in their systems.

■TYPICAL PERFORMANCE CHARACTERISTICS

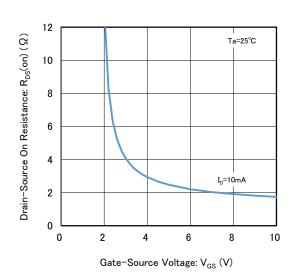
(1) Drain Current vs. Drain-Source Voltage



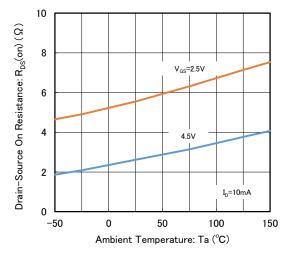
(2) Drain Current vs. Gate-Source Voltage



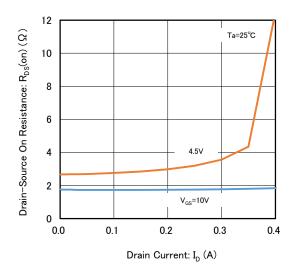
(3) Drain-Source On-State Resistance vs. Gate-Source Voltage



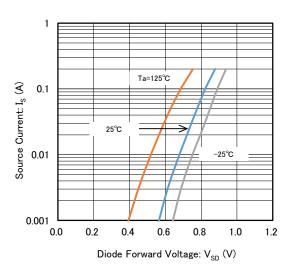
(4) Drain-Source On-State Resistance vs. Ambient Temperature



(5) Drain-Source On-State Resistance vs. Drain Current



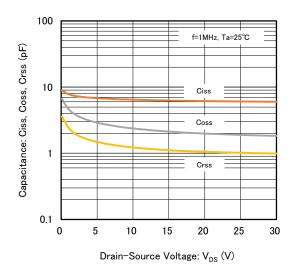
(6) Source Current vs. Diode Forward Voltage



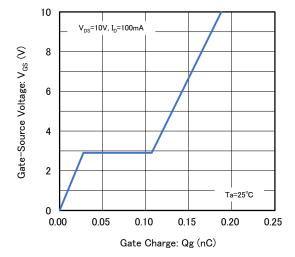
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■TYPICAL PERFORMANCE CHARACTERISTICS

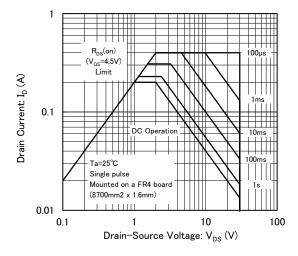
(7) Ciss, Coss, Crss vs. Drain-Source Voltage



(8) Gate-Source Voltage vs. Gate Charge



(9) Area of Safe Operation



■PACKAGING INFORMATION

For the latest package information go to, www.torexsemi.com/technical-support/packages

PACKAGE	OUTLINE / LAND PATTERN	THERMAL CHARACTERISTICS	
SOT-23(TO-236)	<u>SOT-23(TO-236) PKG</u>	JESD51-7 Board	SOT-23(TO-236) PowerDissipation

■ MARKING RULE

●SOT-23(TO-236)

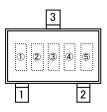
① ②, ③represents product series

	MARK		PRODUCT SERIES
1	2	3	PRODUCT SERIES
3	1	N	XP231N0201**-G

④, ⑤ represents production lot number 01 to 09, 0A to 0Z, 11 to 9Z, A1 to A9, AA to AZ, B1 to ZZ repeated (G, I, J, O, Q, W excluded)

*No character inversion used

SOT-23(TO-236)



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