

# XP231P02013R-G

ETR11051-001

#### P-channel MOSFET -30V, -0.2A

#### **■**FEATURES

**On-State Resistance** : RDS(on)= $5\Omega@VGS = -4.5V$ 

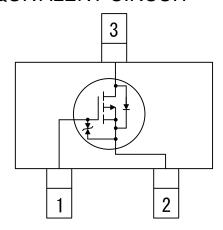
Driving voltage : -2.5V

Environmentally Friendly : EU RoHS Compliant, Pb Free

### ■ APPLICATIONS

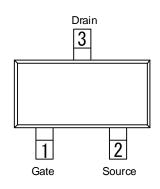
Switching

#### **■**EQUIVALENT CIRCUIT



#### ■ PIN CONFIGURATION

●SOT-323-3A



#### ■ PRODUCT NAME

PRODUCT NAME	PACKAGE	ORDER UNIT	
XP231P02013R-G *	SOT-323-3A	3,000 pcs/ Reel	

<sup>\*</sup> The "-G" suffix denotes Halogen and Antimony free as well as beingfully EU RoHS compliant

### ■ ABSOLUTE MAXIMUM RATINGS

Ta=25°C

PARAMETER	SYMBOL	RATINGS	UNITS
Drain-Source Voltage	V <sub>DSS</sub>	-30	V
Gate-Source Voltage	$V_{GSS}$	±8	V
Drain Current (DC)	$I_D$	-0.2	Α
Drain Current (Pulse) (*1)	I <sub>DP</sub>	-0.4	Α
Channel Power Dissipation (*2)	Pd	0.35	W
Junction Temperature	TJ	150	°C
Storage Temperature	$T_{stg}$	-55~150	°C

<sup>\*(1)</sup> PW≦10μs,duty cycle≦1%

<sup>(\*2)</sup> When implemented on a PCB defined by JESD51-7

## XP231P02013R-G

### **■**ELECTRICAL CHARACTERISTICS

Ta=25°C

PARAMETER	SYMBOL	MBOL TEST CONDITIONS		TYP.	MAX.	UNITS
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	(BR)DSS I <sub>D</sub> = -250μA, V <sub>GS</sub> =0V		-	-	V
Drain-Source Leakage Current	I <sub>DSS</sub>	DSS VDS= -30V, VGS= 0V		-	-10	μA
Gate-Source Leakage Current	Igss	V <sub>GS</sub> = ±8V, V <sub>DS</sub> = 0V	-	-	±10	μA
Gate Threshold Voltage	V <sub>GS(off)</sub>	I <sub>D</sub> = -250uA, V <sub>DS</sub> = V <sub>GS</sub>	-0.5	-0.8	-1.2	V
Drain-Source On Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -100mA	-	3.2	5	Ω
Diani-Source On Resistance		V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -100mA		4	8	Ω
Input Capacitance	Ciss	101/11/101/	-	34	-	pF
Output Capacitance	Coss	V <sub>DS</sub> = -10V, V <sub>GS</sub> = 0V f=1MHz	-	7	-	рF
Reverse Transfer Capacitance	Crss	1 I= I IVITIZ	-	2.5	-	pF
Turn-on Delay Time	t <sub>d(on)</sub>		-	18	-	ns
Rise Time	tr	V <sub>DD</sub> = -10V, I <sub>D</sub> = -100mA	-	20	-	ns
Turn-off Delay Time	t <sub>d(off)</sub>	V <sub>GS</sub> = -4.5V		80	-	ns
Fall Time	t <sub>f</sub>		-	45	-	ns
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> = -100mA, V <sub>G</sub> S= 0V	-	-0.8	-1.2	V

### **■**NOTES ON USE

1. Please use this IC within the absolute maximum ratings.

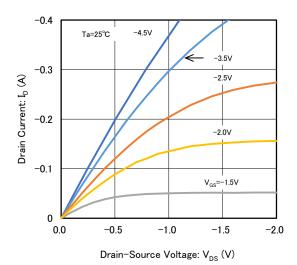
Even within the ratings, in case of high load use continuously such as high temperature, high voltage, high current and thermal stress may cause reliability degradation of the IC.

2. Torex places an importance on improving our products and their reliability.

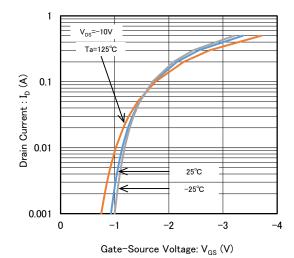
We request that users incorporate fail-safe designs and post-aging protection treatment when using Torex products in their systems.

### **■**TYPICAL PERFORMANCE CHARACTERISTICS

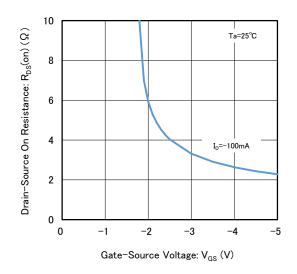
#### (1) Drain Current vs. Drain-Source Voltage



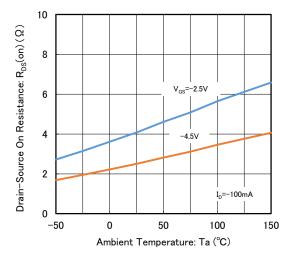
#### (2) Drain Current vs. Gate-Source Voltage



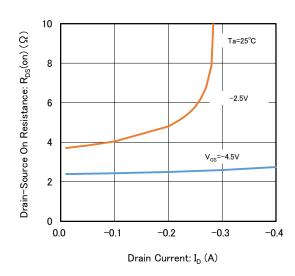
(3) Drain-Source On Resistance vs. Gate-Source Voltage



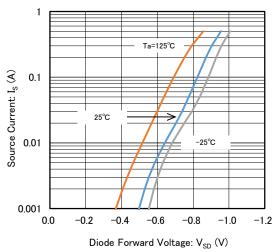
(4) Drain-Source On Resistance vs. Ambient Temperature



(5) Drain-Source On Resistance vs. Drain Current



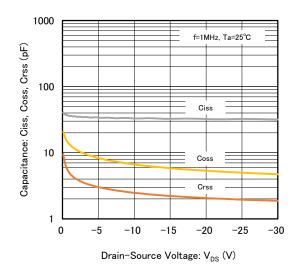
(6) Source Current vs. Diode Forward Voltage



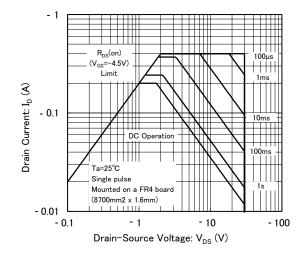
# XP231P02013R-G

## **■**TYPICAL PERFORMANCE CHARACTERISTICS

(7) Ciss, Coss, Crss vs. Drain-Source Voltage



(8) Area of Safe Operation



### **■**PACKAGING INFORMATION

For the latest package information go to, www.torexsemi.com/technical-support/packages

PACKAGE	OUTLINE / LAND PATTERN	THERMAL CHARACTERISTICS	
SOD-323A	SOD-323A PKG	JESD51-7 Board	SOT-323-3A Power Dissipation

### **■** MARKING RULE

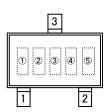
#### ●SOT-323-3A

①, ②, ③represents product series

MARK		DDODUCT CEDIEC	
1	2	3	PRODUCT SERIES
3	1	Р	XP231P0201**-G

④,⑤represents production lot number 01 to 09, 0A to 0Z, 11 to 9Z, A1 to A9, AA to AZ, B1 to ZZ repeated (G, I, J, O, Q, W excluded) \*No character inversion used

SOT-323-3A



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