Unit: mm

TOSHIBA Diode Silicon Epitaxial Schottky Barrier Type

1SS404

High Speed Switching Applications

• Two-pin small packages are suitable for higher mounting densities

Low forward voltage: V_{F (3)} = 0.38 V (typ.)
 Low reverse current: I_R = 50 µA (max)
 Small total capacitance: C_T = 46 pF (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Maximum (peak) reverse voltage	V_{RM}	25	V
Reverse voltage	V _R	20	V
Maximum (peak) forward current	I _{FM}	700	mA
Average forward current	IO	300	mA
Power dissipation	Р	200 (Note 1)	mW
Junction temperature	Tj	125	°C
Storage temperature range	T _{stg}	-55 to 125	°C
Operating temperature range	T _{opr}	-40 to 100	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the

USC

JEDEC —

JEITA —

TOSHIBA 1-1E1A

Weight: 0.004 g (typ.)

reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Mounted on a glass epoxy board of 20 mm \times 20 mm, pad dimension 4 mm \times 4 mm.

Marking



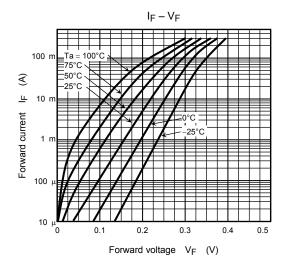
Equivalent Circuit (top view)

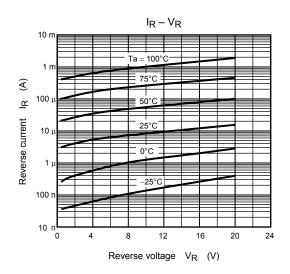


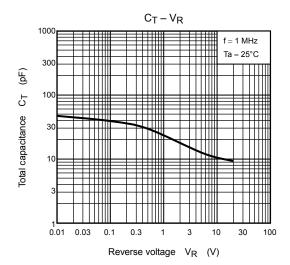
Electrical Characteristics (Ta = 25°C)

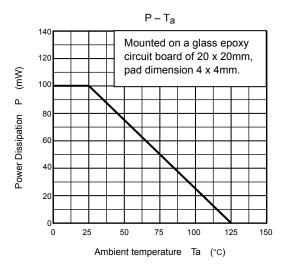
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Forward voltage	V _{F (1)}	I _F = 1 mA	_	0.16	_	
	V _{F (2)}	I _F = 10 mA	_	0.22	_	V
	V _{F (3)}	I _F = 300 mA	_	0.38	0.45	
Reverse current	I _R	V _R = 20 V	_	_	50	μА
Total capacitance	C _T	V _R = 0, f = 1 MHz	_	46	_	pF

Start of commercial production 1999-06









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