Unit: mm

TOSHIBA Transistor Silicon NPN Epitaxial Type

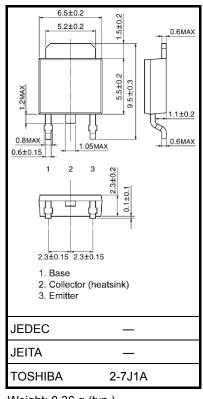
# 2SC5886A

High-Speed Switching Applications DC/DC Converter Applications

- High DC current gain:  $h_{FE}$  = 400 to 1000 (I<sub>C</sub> = 0.5 A)
- Low collector-emitter saturation: V<sub>CE (sat)</sub> = 0.22 V (max)
- High-speed switching: t<sub>f</sub> = 95 ns (typ.)

### Absolute Maximum Ratings (Ta = 25°C)

| Characteristic                 |                    | Symbol           | Rating     | Unit |  |
|--------------------------------|--------------------|------------------|------------|------|--|
| Collector-base voltage         |                    | V <sub>CBO</sub> | 120        | V    |  |
| Collector-emitter voltage      |                    | V <sub>CEX</sub> | 100        | V    |  |
|                                |                    | V <sub>CEO</sub> | 50         |      |  |
| Emitter-base voltage           |                    | V <sub>EBO</sub> | 9          | V    |  |
| Collector current              | DC                 | Ι <sub>C</sub>   | 5          | А    |  |
|                                | Pulse              | I <sub>CP</sub>  | 10         | ~    |  |
| Base current                   |                    | Ι <sub>Β</sub>   | 0.5        | А    |  |
| Collector power<br>dissipation | Ta = 25°C          | Pc               | 1          | W    |  |
|                                | $Tc = 25^{\circ}C$ | FC               | 20         |      |  |
| Junction temperature           |                    | Тј               | 150        | °C   |  |
| Storage temperature range      |                    | T <sub>stg</sub> | –55 to 150 | °C   |  |



Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Electrical Characteristics (Ta = 25°C)

| Characteristic                  |                   | Symbol                | Test Condition   | Min | Тур. | Мах  | Unit |
|---------------------------------|-------------------|-----------------------|--|-----|------|------|------|
| Collector cutoff cut            | rent              | I <sub>CBO</sub>      | $V_{CB} = 120 \text{ V}, \text{ I}_{E} = 0$  |     |      | 100  | nA   |
| Emitter cutoff curre            | ent               | I <sub>EBO</sub>      | $V_{EB} = 9 V, I_{C} = 0$  | _   | _    | 100  | nA   |
| Collector-emitter b             | reakdown voltage  | V (BR) CEO            | $I_{C} = 10 \text{ mA}, I_{B} = 0$   | 50  | _    | _    | V    |
| DC current gain                 |                   | h <sub>FE</sub> (1)   | $V_{CE} = 2 V, I_C = 0.5 A$  | 400 | _    | 1000 |      |
|                                 |                   | h <sub>FE</sub> (2)   | $V_{CE} = 2 V, I_C = 1.6 A$  | 200 | _    | _    |      |
| Collector-emitter s             | aturation voltage | V <sub>CE (sat)</sub> | I <sub>C</sub> = 1.6 A, I <sub>B</sub> = 32 mA   | _   | _    | 0.22 | V    |
| Base-emitter saturation voltage |                   | V <sub>BE (sat)</sub> | I <sub>C</sub> = 1.6 A, I <sub>B</sub> = 32 mA   | _   | _    | 1.10 | V    |
| Switching time                  | Rise time         | tr                    | See Figure 1. $V_{CC} \simeq 24 \text{ V, R}_L = 15 \Omega$ $I_{B1} = 32 \text{ mA, } I_{B2} = -53 \text{ mA}$ | _   | 60   | _    |      |
|                                 | Storage time      | t <sub>stg</sub>      |  |     | 500  |      | ns   |
|                                 | Fall time         | t <sub>f</sub>        |  |     | 95   |      |      |

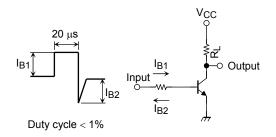
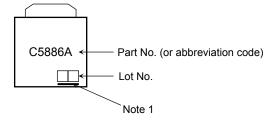


Figure 1 Switching Time Test Circuit & Timing Chart

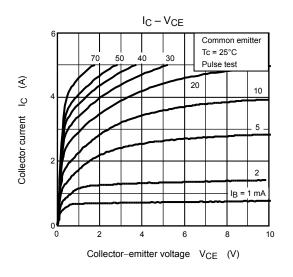
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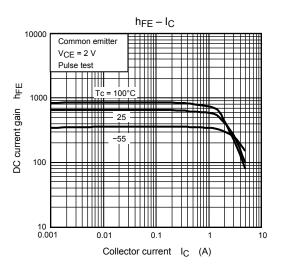


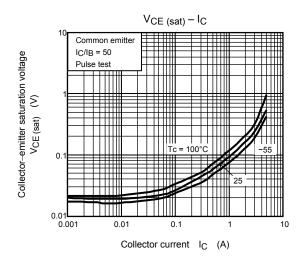
Note 1: A line under a Lot No. identifies the indication of product Labels. Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

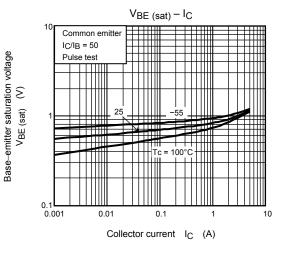
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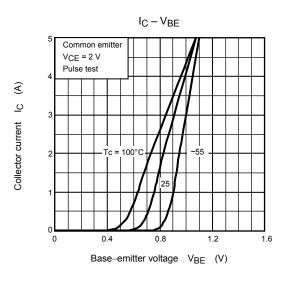
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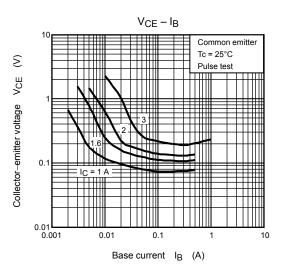


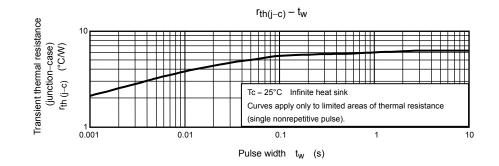


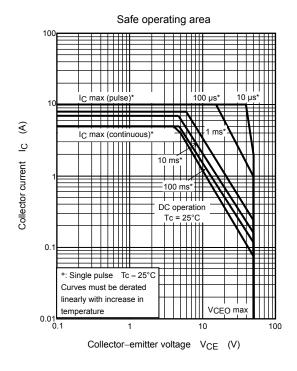












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