Unit: mm

TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (π -MOSIV)

2SK3799

Switching Regulator Applications

• Low drain-source ON-resistance: $R_{DS (ON)} = 1.0 \Omega (typ.)$

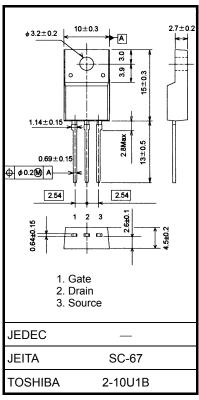
• High forward transfer admittance: |Y_{fs}| = 6.0 S (typ.)

Low leakage current: I_{DSS} = 100 μA (max) (V_{DS} = 720 V)

• Enhancement model: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	900	V	
Drain-gate voltage (F	R _{GS} = 20 kΩ)	V_{DGR}	900	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	I _D	8	Α	
	Pulse (Note 1)	I _{DP}	24	Α	
Drain power dissipat	ion (Tc = 25°C)	PD	50	W	
Single pulse avalanche energy (Note 2)		Eas	1080	mJ	
Avalanche current		I _{AR}	8	Α	
Repetitive avalanche energy (Note 3)		E _{AR}	5	mJ	
Channel temperature)	T _{ch}	150	°C	
Storage temperature	range	T _{stg}	-55 to 150	°C	

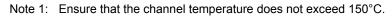


Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

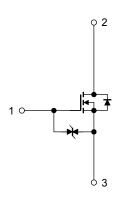
Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	2.5	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W



Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 30.9 mH, R_G = 25 Ω , I_{AR} = 8 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



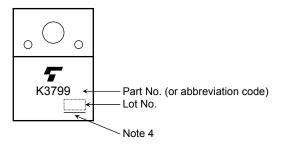
Electrical Characteristics (Ta = 25°C)

Chara	cteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I _{GSS}	V _{GS} = ±30 V, V _{DS} = 0 V	_	_	±10	μΑ
Gate-source bre	akdown voltage	V (BR) GSS	$I_G = \pm 10 \mu A, V_{DS} = 0 V$	±30	_	_	V
Drain cut-off cur	rent	I _{DSS}	V _{DS} = 720 V, V _{GS} = 0 V	_	_	100	μA
Drain-source bre	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	900	_	_	V
Gate threshold v	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source ON	l-resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 4 A	_	1.0	1.3	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 15 V, I _D = 4 A	3.5	6.0	_	S
Input capacitano	е	C _{iss}		_	2200	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	45	_	pF
Output capacitance		Coss		_	190	_	
Switching time	Rise time	t _r	V _{DD} ≈ 400 V	_	25	_	. ns
	Turn-on time	t _{on}		_	65	_	
	Fall time	t _f		_	20	_	
	Turn-off time	t _{off}	Duty ≤ 1%, t _w = 10 μs	_	120	_	
Total gate charge (Gate-source plus gate-drain)		Qg	V _{DD} ≈ 400 V, V _{GS} = 10 V, I _D = 8 A	_	60		nC
Gate-source charge		Q _{gs}			34	_	
Gate-drain ("miller") charge		Q _{gd}			26	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	8	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	24	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 8 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 8 A, V _{GS} = 0 V	_	1700		ns
Reverse recovery charge	Qrr	dl _{DR} / dt = 100 A / μs	_	23	-	μС

Marking

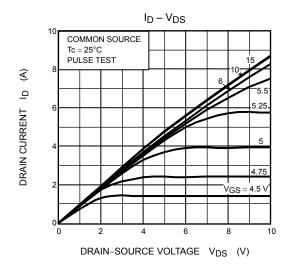


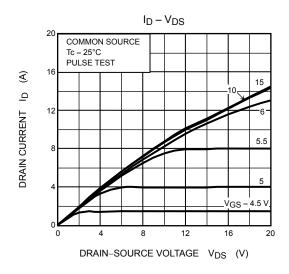
Note 4: A line under a Lot No. identifies the indication of product Labels.

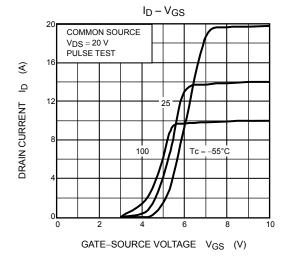
Not underlined: [[Pb]]/INCLUDES > MCV

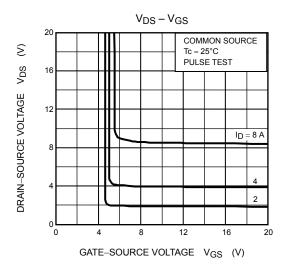
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

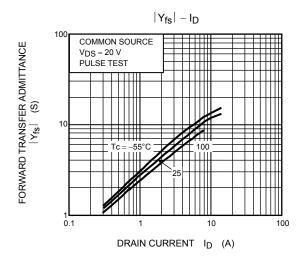
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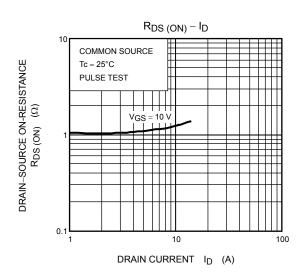


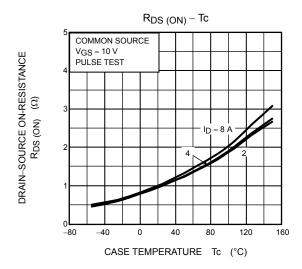


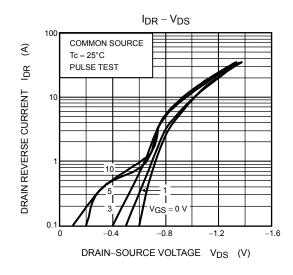


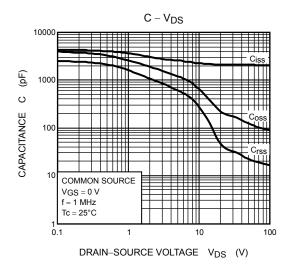


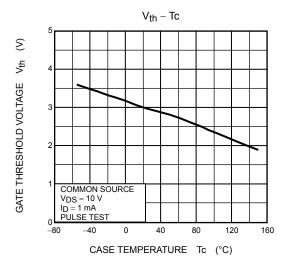


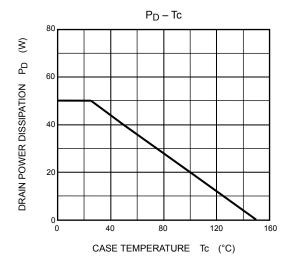


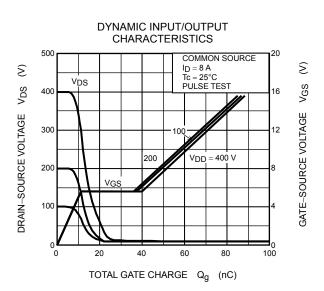


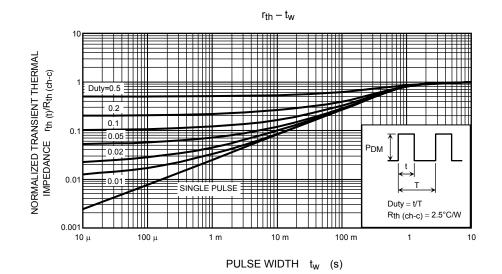


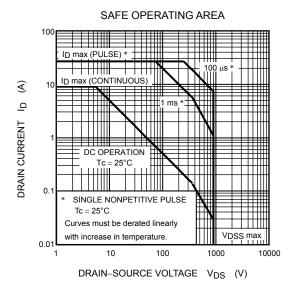


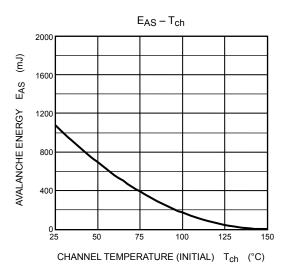


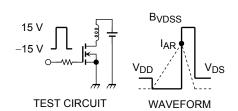












$$\begin{aligned} &R_G = 25~\Omega \\ &V_{DD} = 90~V,~L = 30.9~mH \end{aligned} \qquad \text{EAS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - V_{DD}} \right)$$

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