TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

2SK4037

470 MHz Band Amplifier Applications

(Note)The TOSHIBA products listed in this document are intended for high frequency Power Amplifier of telecommunications equipment. These TOSHIBA products are neither intended nor warranted for any other use. Do not use these TOSHIBA products listed in this document except for high frequency Power Amplifier of telecommunications equipment.

• Output power: $P_0 = 36.5 dBmW$ (typ)

• Gain: $G_p = 11.5 dB \text{ (typ)}$

• Drain Efficiency: $\eta D = 60.0\%$ (typ)

Absolute Maximum Ratings (Ta = 25°C)

| Characteristics | Symbol | Rating | Unit |
|---------------------------|---------------------------|------------|------|
| Drain-source voltage | V _{DSS} | 12 | V |
| Gate-source voltage | V _{GSS} (Note 1) | 3 | V |
| Drain current | I _D | 3 | Α |
| Power dissipation | P _D (Note 2) | 20 | W |
| Channel temperature | T _{ch} | 150 | °C |
| Storage temperature range | T _{stg} | -45 to 150 | °C |

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the

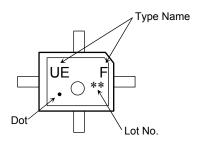
reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Operating Ranges: 0 to 3V

Note 2: Tc = 25°C (When mounted on a 0.8 mm glass epoxy PCB)

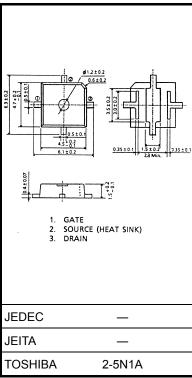
Marking



Caution: This device is sensitive to electrostatic discharge.

Please make enough tool and equipment earthed when you handle.

Unit: mm



Weight: 0.08 g (typ.)

Start of commercial production 2005-01

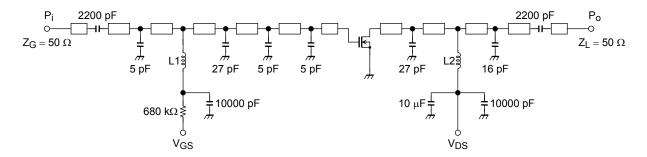


Electrical Characteristics (Ta = 25°C)

| Characteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|-----------------------------|------------------|--|----------------|------|-----|------|
| Output power | PO | V_{DS} = 6.0 V, lidle = 250 mA (V_{GS} = adjust) | 35.5 | 36.5 | _ | dBmW |
| Drain efficiency | ηD | | 55.0 | 60.0 | _ | % |
| Power gain | Gp | $f = 470 \text{ MHz}, P_i = 25 \text{dBmW}$ $Z_G = Z_L = 50 \Omega$ | 10.5 | 11.5 | _ | dB |
| Threshold voltage | V _{th} | V _{DS} = 6.0 V, I _D = 0.5 mA | _ | 1.0 | 1.5 | V |
| Drain cut-off current | I _{DSS} | V _{DS} = 12 V, V _{GS} = 0 V | _ | _ | 10 | μA |
| Gate-source leakage current | I _{GSS} | V _{GS} = 3V, V _{DS} = 0 V | _ | _ | 5 | μA |
| Load mismatch (Note 3) | _ | V_{DS} = 6.0 V, f = 470 MHz, P_i = 25dBmW, P_0 = 36.5dBmW (V_{GS} = adjust) VSWR LOAD 10:1 all phase | No degradation | | _ | |

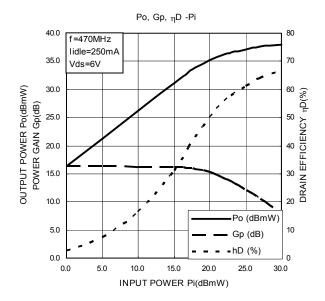
Note 3: These characteristic values are measured using measurement tools specified by Toshiba.

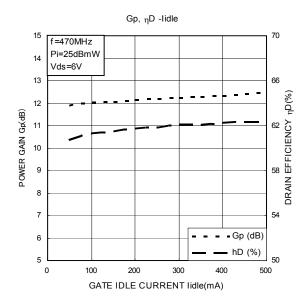
Test Circuit

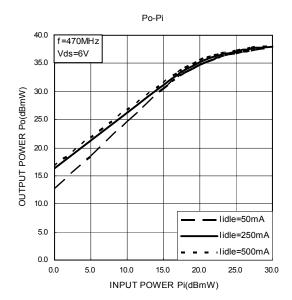


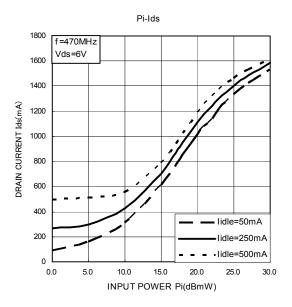
L1: ϕ 0.6 mm enamel wire, 5.8ID, 8T L2: ϕ 0.6 mm enamel wire, 5.8ID, 8T

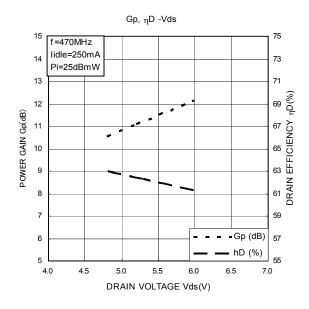
Line: 2mm

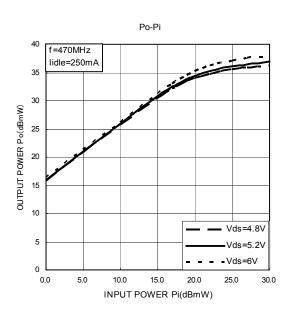


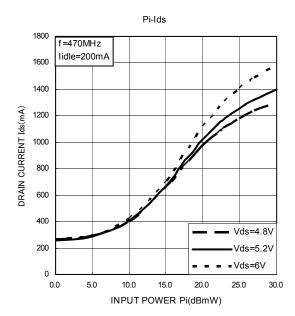












Note 4: These are only typical curves and devices are not necessarily guaranteed at these curves.

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