TOSHIBA Photocoupler GaAs IRed & Photo-Transistor

# 4N35(Short), 4N36(Short), 4N37(Short)

AC Line / Digital Logic Isolator.

Digital Logic / Digital Logic Isolator.

Telephone Line Receiver.

High Frequency Power Supply Feedback Control.

Relay Contact Monitor.

The TOSHIBA 4N35 (short) through 4N37 (short) consists of a gallium arsenide infrared emitting diode coupled with a silicon phototransistor in a dual in–line package.

• Switching speeds: 3µs (typ.)

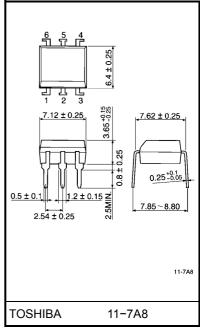
• DC current transfer ratio: 100% (min.)

• Isolation resistance: 10<sup>11</sup>Ω (min.)

• Isolation voltage: 2500Vrms (min.)

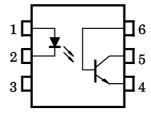
• UL recognized: UL1577, file no. E67349

Unit in mm



Weight: 0.4 g

### Pin Configurations(top view)



1: ANODE

2: CATHODE

3 : N.C.

4: EMITTER

5 : COLLECTOR

6: BASE



# **Maximum Ratings (Ta = 25°C)**

	Characteristic		Symbol	Rating	Unit	
LED	Forward current (continuous)		l <sub>F</sub>	60	mA	
	Forward current derating		ΔI <sub>F</sub> /°C	0.8 (*)	mA / °C	
	Peak forward current (Note 1)		lpF	3	Α	
	Power dissipation		$P_{D}$	100	mW	
	Power dissipation derating		ΔP <sub>D</sub> / °C	1.33 (*)	mW / °C	
	Reverse voltage		$V_{R}$	6	V	
Detector	Collector-emitter voltage	BV <sub>CEO</sub>	30	V		
	Collector-base voltage		BV <sub>CBO</sub>	70	V	
	Emitter-collector voltage		BV <sub>ECO</sub>	7	V	
	Collector current (continuous)		Ic	100	mA	
	Power dissipation		PC	300	mW	
	Power dissipation derating		ΔP <sub>C</sub> / °C	4.0 (*)	mW / °C	
	Storage temperature		T <sub>stg</sub>	-55~150	°C	
	Operating temperature	T <sub>opr</sub>	-55~100	°C		
	Lead soldering temperature (at 10 s)	T <sub>sol</sub>	260	°C		
Coupled	Total package power dissipation		PT	300	mW	
	Total package power dissipation derating		ΔP <sub>T</sub> / °C	3.3 (*)	mW / °C	
			BVS	2500	Vrms	
	Input to output isolation	4N35		2500 / 3550		
	voltage (AC, 1 minute)		BV <sub>S</sub> (**)	1750 / 2500	Vrms / Vpk	
				1050 / 1500		

(Note 1) Pulse width  $1\mu s$ , 300pps

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<sup>(\*)</sup> Above 25°C ambient.

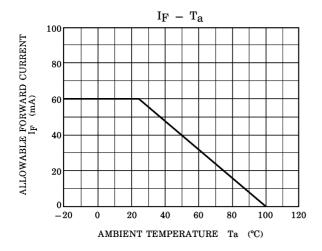
<sup>(\*\*)</sup> JEDEC registered maximum BV<sub>S</sub>, however, TOSHIBA specifies a maxium BV<sub>S</sub> of 2500V<sub>rms</sub>, 1 minute.

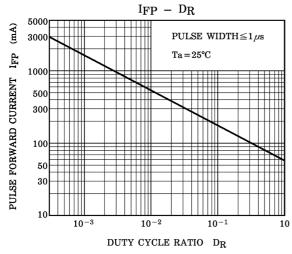


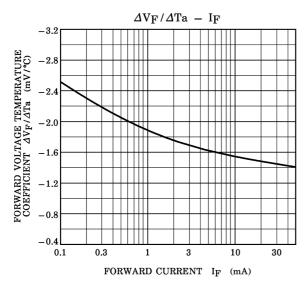
# Electrical Characteristics (Ta = 25°C)

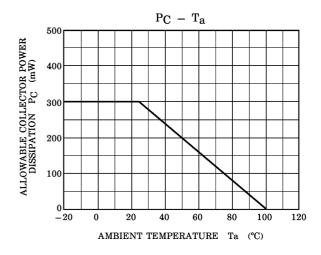
Characteristic		Symbol	Test Condition	Min.	Тур.	Max.	Unit		
red	Forward voltage		V <sub>F</sub>	I <sub>F</sub> = 10 mA	8.0	1.15	1.5		
				I <sub>F</sub> = 10 mA, Ta = -55°C	0.9	_	1.7	V	
				I <sub>F</sub> = 10 mA, Ta = 100°C	0.7	_	1.4		
	Reverse current		I <sub>R</sub>	V <sub>R</sub> = 6 V	_	_	10	μΑ	
	Capacitance		C <sub>D</sub>	V = 0, f = 1 MHz	_	30	100	pF	
Detector	DC forward current gain		h <sub>FE</sub>	V <sub>CE</sub> = 5V, I <sub>C</sub> = 500 μA	_	200	_	_	
	Collector–emitter breakdown voltage		V (BR) CEO	I <sub>C</sub> = 10 mA	30	1	1	V	
	Collector–base breakdown voltage		V (BR) CBO	Ι <sub>C</sub> = 100 μΑ	70			V	
	Emitter–collector breakdown voltage		V (BR) ECO	I <sub>E</sub> = 100 μA	7	_	_	V	
	Collector dark current		I <sub>CEO</sub>	V <sub>CE</sub> = 10 V	_	1	50	nA	
	Collector dark current		I <sub>CEO</sub>	V <sub>CE</sub> = 30 V, Ta = 100°C	_	_	500	μA	
	Collector-emitter capacitance		C <sub>CE</sub>	V = 0, f = 1 MHz	_	10	_	pF	
	Current transfer ratio		I <sub>C</sub> / I <sub>F</sub>	I <sub>F</sub> = 10 mA, V <sub>CE</sub> = 10 V	100	_	_	%	
Coupled				I <sub>F</sub> = 10 mA, V <sub>CE</sub> = 10 V Ta = -55°C	40	_	_		
				I <sub>F</sub> = 10 mA, V <sub>CE</sub> = 10 V Ta = 100°C	40		_		
	Collector–emitter saturation voltage		V <sub>CE (sat)</sub>	I <sub>F</sub> = 10 mA, I <sub>C</sub> = 0.5 mA	_	0.1	0.3	V	
	Capacitance input to output		CS	V <sub>S</sub> = 0, f = 1 MHz	_	0.8	2.5	pF	
	Isolation resistance		R <sub>S</sub>	V <sub>S</sub> = 500 V, R.H.≤ 60 %	10 <sup>11</sup>	_		Ω	
	Input to output	4N35		V <sub>io</sub> = 3550 Vpk	_	_	100		
	isolation current	4N36	lio	V <sub>io</sub> = 2500 Vpk	_	_	100	μΑ	
	(pulse width = 8ms)	4N37		V <sub>io</sub> = 1500 Vpk	_	_	100		
	Turn-on time Turn-off time		t <sub>ON</sub>	V <sub>CC</sub> = 10 V, I <sub>C</sub> = 2 mA	_	3	10	- µs	
			toff	$R_L = 100\Omega$	_	3	10		

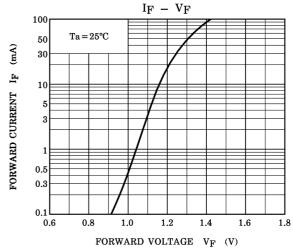
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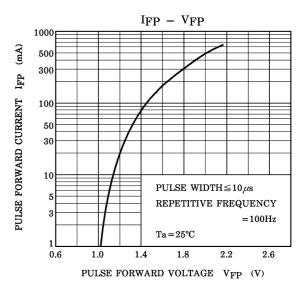


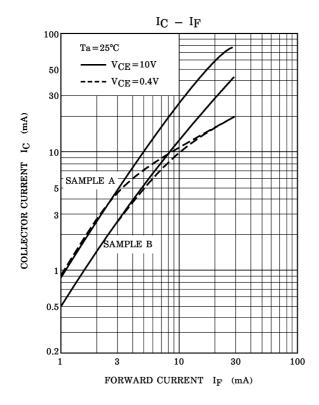


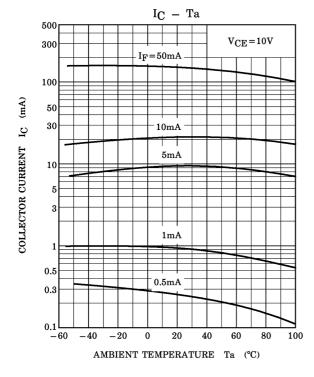


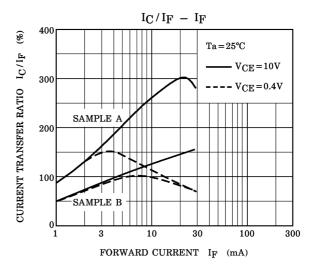


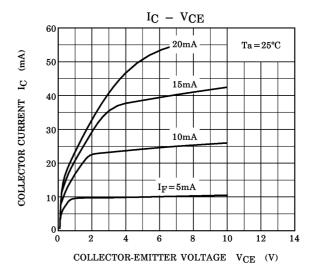


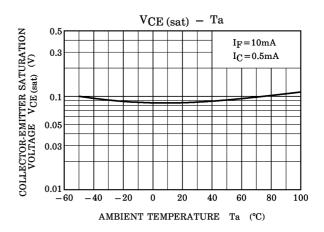




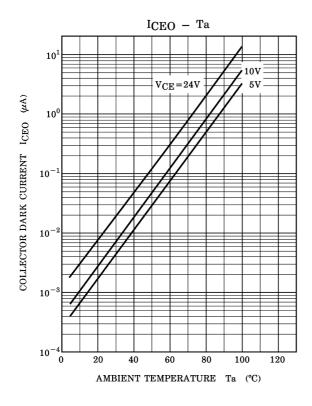


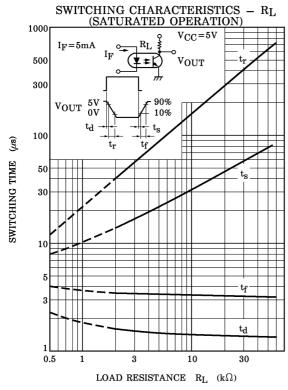


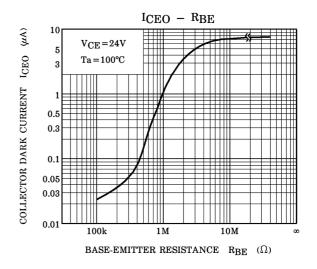


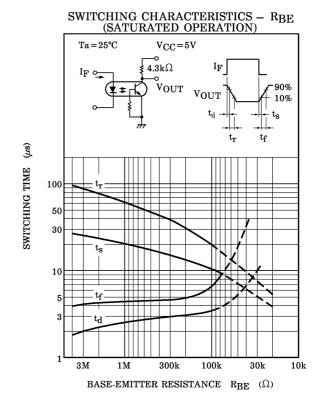


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