Unit: mm

TOSHIBA Schottky Barrier Rectifier Schottky Barrier Type

### **CRS04**

## Switching Mode Power Supply Applications Portable Equipment Battery Applications

- Forward voltage:  $V_{FM} = 0.49 \text{ V (max)}$
- Average forward current:  $I_F(AV) = 1.0 A$
- Repetitive peak reverse voltage:  $V_{RRM} = 40 \text{ V}$
- Suitable for compact assembly due to small surface-mount package "S-FLAT<sup>TM</sup>" (Toshiba package name)

#### **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics	Symbol	Rating	Unit
Repetitive peak reverse voltage	$V_{RRM}$	40	V
Average forward current	I <sub>F (AV)</sub>	1.0 (Note 1)	Α
Peak one cycle surge forward current (non-repetitive)	I <sub>FSM</sub>	20 (50 Hz)	Α
Junction temperature	Tj	-40~150	°C
Storage temperature	T <sub>stg</sub>	-40~150	°C

Note 1:  $Ta = 31^{\circ}C$ 

Device mounted on a glass-epoxy board (board size:  $50 \text{ mm} \times 50 \text{ mm}$ , land size:  $6 \text{ mm} \times 6 \text{ mm}$ )

Note 2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the

reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

# 1. ANODE 2. CATHODE JEDEC — JEITA — TOSHIBA 3-2A1A

Weight: 0.013 g (typ.)

#### **Electrical Characteristics (Ta = 25°C)**

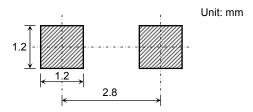
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Peak forward voltage	V <sub>FM (1)</sub>	I <sub>FM</sub> = 0.1 A	_	0.395	_	V	
	V <sub>FM (2)</sub>	I <sub>FM</sub> = 0.7 A	_	0.475	0.49		
	V <sub>FM (3)</sub>	I <sub>FM</sub> = 1.0 A	_	0.51	_		
Repetitive peak reverse current	I <sub>RRM (1)</sub>	V <sub>RRM</sub> = 5 V		0.6		μА	
	I <sub>RRM (2)</sub>	V <sub>RRM</sub> = 40 V		_	100	μΑ	
Junction capacitance	Cj	V <sub>R</sub> = 10 V, f = 1.0 MHz		47		pF	
Thermal resistance (junction to ambient)	R <sub>th (j-a)</sub>	Device mounted on a ceramic board (soldering land: 2 mm × 2 mm)	_	_	70	°C/W	
		Device mounted on a glass-epoxy board (soldering land: 6 mm × 6 mm)	_	_	140		
Thermal resistance (junction to lead)	R <sub>th (j-ℓ)</sub>	_	_	_	20	°C/W	

Start of commercial production 1999-07

#### Marking

Abbreviation Code	Part No.		
S4	CRS04		

#### Standard Soldering Pad



#### **Handling Precaution**

Schottky barrier diodes have reverse current characteristics compared to other diodes.

There is a possibility SBD may cause thermal runaway when it is used under high temperature or high voltage. Please take forward and reverse loss into consideration during design.

The absolute maximum ratings denote the absolute maximum ratings, which are rated values and must not be exceeded during operation, even for an instant. The following are the general derating methods that we recommend when you design a circuit with a device.

VRRM: Use this rating with reference to the above. VRRM has a temperature coefficient of 0.1%/°C. Take this temperature coefficient into account designing a device at low temperature.

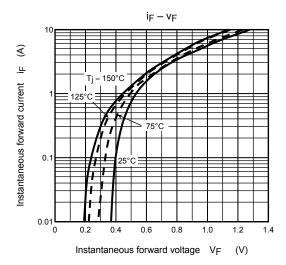
IF(AV): We recommend that the worst case current be no greater than 80% of the absolute maximum rating of IF(AV) and  $T_j$  be below 120°C. When using this device, take the margin into consideration by using an allowable Tamax-IF(AV) curve.

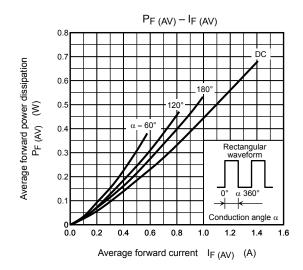
IFSM: This rating specifies the non-repetitive peak current. This is only applied for an abnormal operation, which seldom occurs during the lifespan of the device.

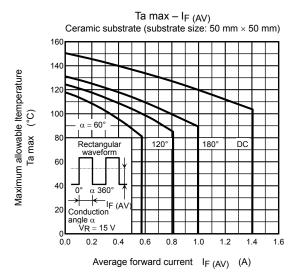
T<sub>j</sub>: Derate this rating when using a device in order to ensure high reliability. We recommend that the device be used at a T<sub>j</sub> of below 120°C.

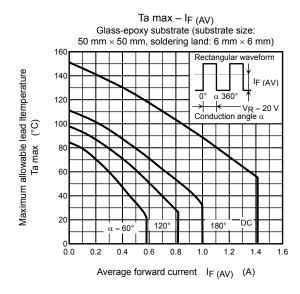
Thermal resistance between junction and ambient fluctuates depending on the device's mounting condition. When using a device, design a circuit board and a soldering land size to match the appropriate thermal resistance value.

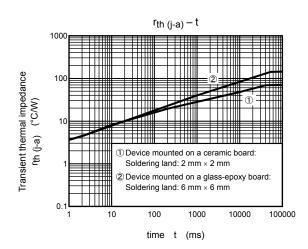
Please refer to the Rectifiers databook for further information.

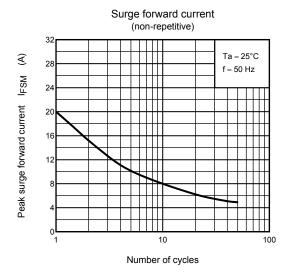


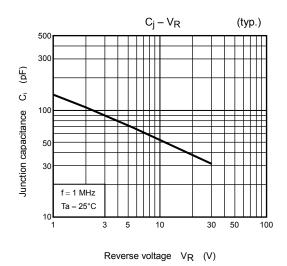


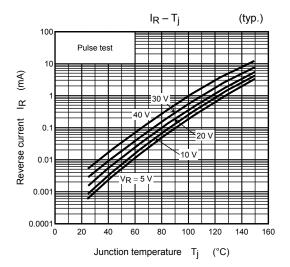


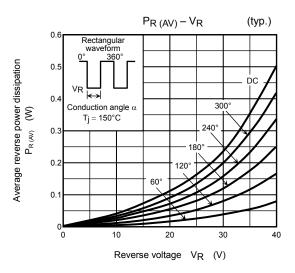












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