

Schottky Barrier Diode Silicon Epitaxial

# **CUS357**

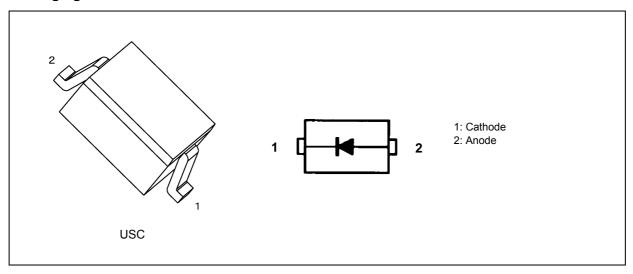
#### 1. Applications

• High-Speed Switching

#### 2. Features

- (1) Low forward voltage :  $V_{F(3)} = 0.54 \text{ V (typ.)}$
- (2) Low reverse current :  $I_{R(1)} = 1\mu A \text{ (max)}$
- (3) General purpose USC package, equivalent to SOD-323 and SC-76 packages

## 3. Packaging and Internal Circuit





### 4. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Note	Rating	Unit
Peak reverse voltage	V <sub>RM</sub>		45	V
Reverse voltage	V <sub>R</sub>		40	
Peak forward current	I <sub>FM</sub>		300	mA
Average rectified current	Io		100	mA
Power dissipation	P <sub>D</sub>	(Note 1)	200	mW
Non-repetitive peak forward surge current	I <sub>FSM</sub>	(Note 2)	1	Α
Junction temperature	Tj		125	Ŝ
Storage temperature	T <sub>stg</sub>		-55 to 125	Ĵ

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Mounted on a glass epoxy circuit board of 20 mm  $\times$  20 mm, Pad dimension of 4 mm  $\times$  4 mm.

Note 2: Measured with a 10 ms pulse.

#### 5. Electrical Characteristics (Unless otherwise specified, T<sub>a</sub> = 25 °C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Forward voltage	V <sub>F(1)</sub>	I <sub>F</sub> = 1 mA		0.21		V
Forward voltage	V <sub>F(2)</sub>	I <sub>F</sub> = 10 mA		0.30		V
Forward voltage	V <sub>F(3)</sub>	I <sub>F</sub> = 100 mA		0.54	0.60	V
Reverse current	I <sub>R(1)</sub>	V <sub>R</sub> = 10 V			1	μΑ
Reverse current	I <sub>R(2)</sub>	V <sub>R</sub> = 40 V			5	μΑ
Total capacitance	Ct	V <sub>R</sub> = 0 V, f = 1 MHz		11		pF

#### 6. Marking

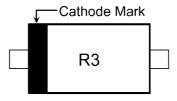


Fig. 6.1 Marking

Marking Code	Part Number		
R3	CUS357		

#### 7. Usage Considerations

• Schottky barrier diodes (SBDs) have reverse leakage greater than other types of diodes. This makes SBDs more susceptible to thermal runaway under high-temperature and high-voltage conditions. Thus, both forward and reverse power losses of SBDs should be considered for thermal and safety design.

## 8. Land Pattern Dimensions (for reference only)

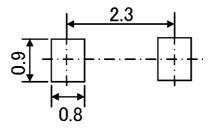


Fig. 8.1 Land Pattern Dimensions for Reference Only (Unit: mm)

# 9. Characteristics Curves (Note)

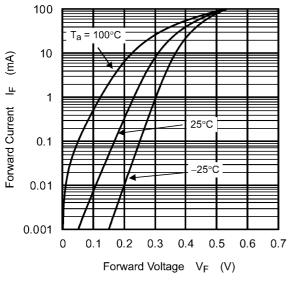


Fig. 9.1 I<sub>F</sub> - V<sub>F</sub>

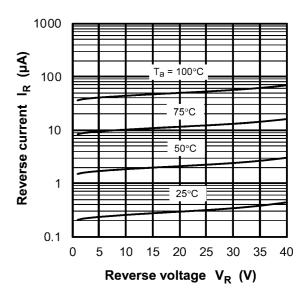


Fig. 9.2 I<sub>R</sub> - V<sub>R</sub>

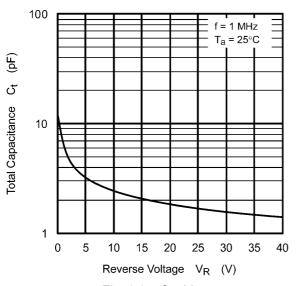


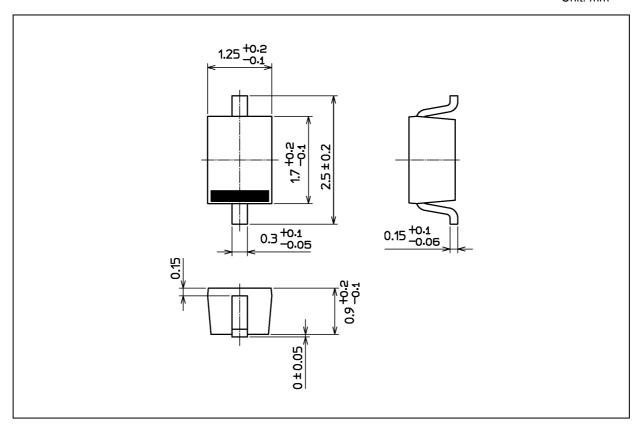
Fig. 9.3 Ct - VR

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



# **Package Dimensions**

Unit: mm



Weight: 4.5 mg (typ.)

	Package Name(s)
TOSHIBA: 1-1E1S	
Nickname: USC	



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