

ESD Protection Diodes Silicon Epitaxial Planar

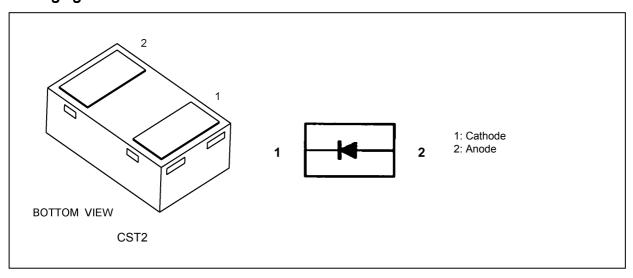
# DF2S24UCT

#### 1. Applications

· ESD Protection

Note: This product is designed for protection against electrostatic discharge (ESD) and is not intended for any other purpose, including, but not limited to, voltage regulation.

### 2. Packaging and Internal Circuit



### 3. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Electrostatic discharge voltage (IEC61000-4-2)(Contact)	V <sub>ESD</sub>	±8	kV
Junction temperature	Tj	150	°C
Storage temperature	T <sub>stg</sub>	-55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



## 4. Electrical Characteristics (Unless otherwise specified, T<sub>a</sub> = 25°C)

 $V_{\text{RWM}}$ : Working peak reverse voltage

V<sub>BR</sub>: Reverse breakdown voltage

I<sub>BR</sub>: Reverse breakdown current

V<sub>R</sub>: Reverse voltage I<sub>R</sub>: Reverse current

V<sub>F</sub>: Forward voltage I<sub>F</sub>: Forward current

V<sub>C</sub>: Clamping voltage I<sub>PP</sub>: Peak pulse current

R<sub>DYN</sub>: Dynamic resistance

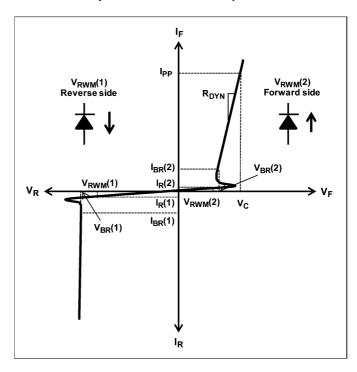


Fig. 4.1 Definitions of Electrical Characteristics

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Working peak reverse voltage	V <sub>RWM</sub> (1)		_	_	_	19	V
Reverse breakdown voltage	V <sub>BR</sub> (1)		I <sub>BR</sub> = 1 mA	22	24	_	V
Reverse current	I <sub>R</sub> (1)		V <sub>RWM</sub> = 19 V	_	_	0.5	μА
Dynamic resistance	R <sub>DYN</sub>	(Note 1)		_	0.5	_	Ω
Total capacitance	Ct		V <sub>R</sub> = 0 V, f = 1 MHz	_	1.6	_	pF
Working peak reverse voltage	V <sub>RWM</sub> (2)		_	_	_	5	V
Reverse breakdown voltage	V <sub>BR</sub> (2)	·	I <sub>BR</sub> = 1 mA	5.3	6.8	_	V
Reverse current	I <sub>R</sub> (2)		V <sub>RWM</sub> = 5 V	_	_	0.1	μА

Note 1: TLP parameter: Z0 = 50  $\Omega$ , tp = 100 ns, tr = 300 ps, averaging window: t1 = 30 ns to t2 = 60 ns, extraction of dynamic resistance using a least-squares fit of TLP characteristics at I<sub>PP</sub> between 3 A to 8 A.

#### 5. Guaranteed ESD Protection (Note)

Test Condition	ESD Protection	
IEC61000-4-2 (Contact discharge)	±8 kV	

Note: Criterion: No damage to devices.



# 6. Marking

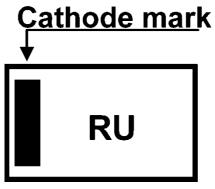


Fig. 6.1 Marking

Marking Code	Part Number	
RU	DF2S24UCT	

# 7. Land Pattern Dimensions (for reference only)

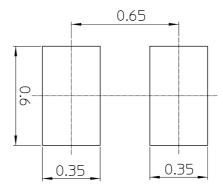


Fig. 7.1 Land Pattern Dimensions (Unit: mm)

### 8. Characteristics Curves (Note)

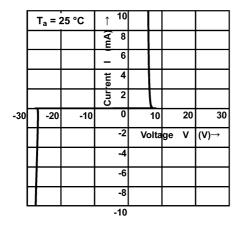


Fig. 8.1 I-V

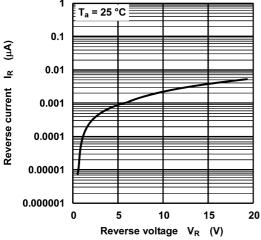


Fig. 8.2  $I_R - V_R (V_{RWM} (1) \text{ side })$ 

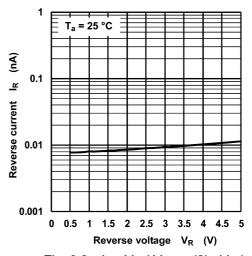
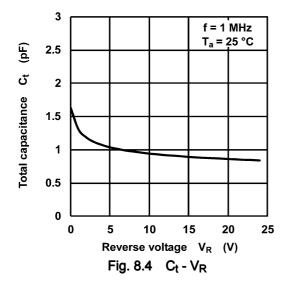
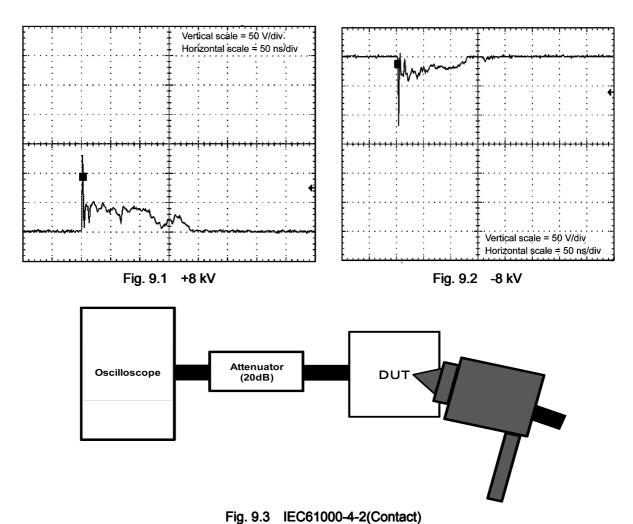


Fig. 8.3  $I_R - V_R (V_{RWM} (2) \text{ side })$ 



Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

# 9. ESD Clamp Waveform (Note)

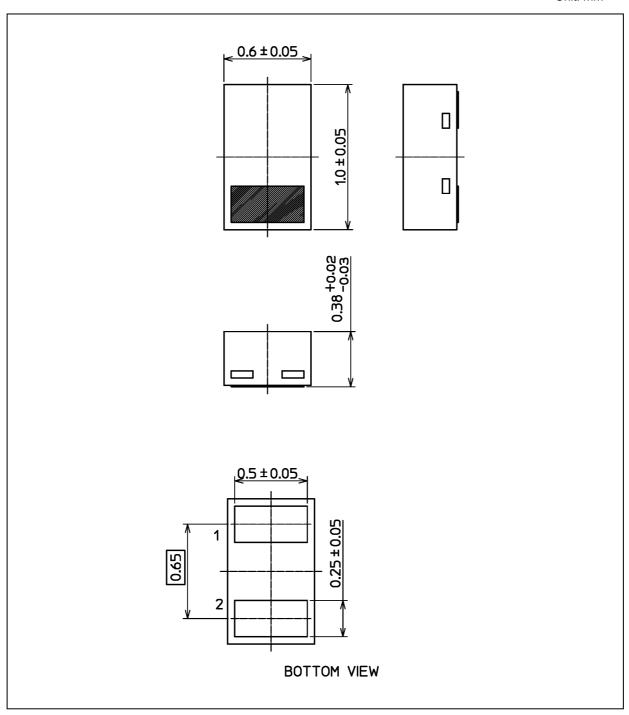


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



# **Package Dimensions**

Unit: mm



Weight: 0.7 mg (typ.)

	Package Name(s)
TOSHIBA: 1-1P1S	
Nickname: CST2	

Rev.3.0



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