ESD Protection Diodes Silicon Epitaxial Planar

DF3D29FU

1. Applications

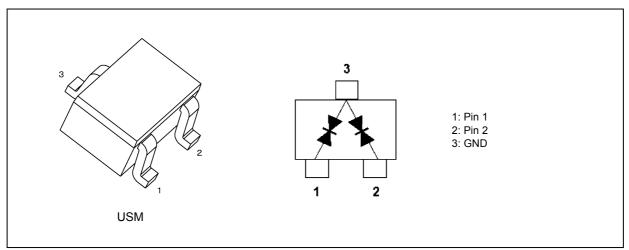
ESD Protection

Note: This product is designed for protection against electrostatic discharge (ESD) and is not intended for any other purpose, including, but not limited to, voltage regulation.

2. Features

(1) AEC-Q101 qualified (Please see the orderable part number list)

3. Packaging and Internal Circuit



4. Orderable part number

Orderable part number	AEC-Q101		Note		
DF3D29FU,LF	—		General Use		
DF3D29FU,LXGF	YES	(Note 1)	Unintended Use	(Note 1)	
DF3D29FU,LXHF	YES		Automotive Use		

Note 1: For more information, please contact our sales or use the inquiry form on our website.

5. Absolute Maximum Ratings (Note) (Unless otherwise specified, $T_a = 25^{\circ}$ C)

Characteristics	Symbol	Note	Rating	Unit
Electrostatic discharge voltage (IEC61000-4-2)(Contact)	V _{ESD}	(Note 1)	±25	kV
Electrostatic discharge voltage(IEC61000-4-2)(Air)				
Electrostatic discharge voltage(ISO10605)(Contact)	V _{ESD}	(Note 2)	±30	kV
Electrostatic discharge voltage(ISO10605)(Air)	1			
Peak pulse power	P _{PK}		140	W
Peak pulse current	I _{PP}	(Note 3)	3	А
Junction temperature	Тj		150	°C
Storage temperature	T _{stg}		-55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: According to IEC61000-4-2.

Note 2: According to ISO10605. (@ C = 330 pF, R = $2 \text{ k}\Omega$)

Note 3: According to IEC61000-4-5.

6. Electrical Characteristics (Unless otherwise specified, $T_a = 25^{\circ}C$)

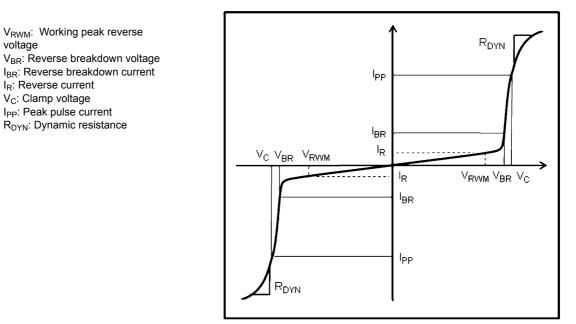


Fig. 6.1 Definitions of Electrical Characteristics

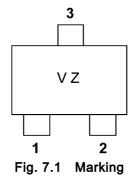
Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Working peak reverse voltage	V _{RWM}		—	_		24	V
Reverse breakdown voltage	V _{BR}		I _{BR} = 1 mA	26	_	32	V
Reverse current	I _R		V _{RWM} = 24 V	_	_	0.1	μA
Clamp voltage	V _C	(Note 1)	I _{PP} = 1 A		30	_	V
			I _{PP} = 3 A	_	37	47	
Dynamic resistance	R _{DYN}	(Note 2)	—	_	1.1	_	Ω
Total capacitance	Ct	(Note 3)	V _R = 0 V, f = 1 MHz	_	9	10	pF

Note 1: Based on IEC61000-4-5 8/20 μs pulse.

Note 2: TLP parameter: Z0 = 50 Ω , tp = 100 ns, tr = 300 ps, averaging window: t1 = 30 ns to t2 = 60 ns,

extraction of dynamic resistance using a least-squares fit of TLP characteristics at I_{PP} between 8 A to 16 A. Note 3: Guaranteed by design.

7. Marking



8. Land Pattern Dimensions (for reference only)

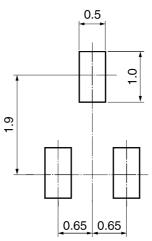
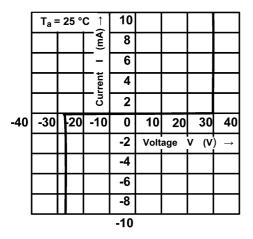
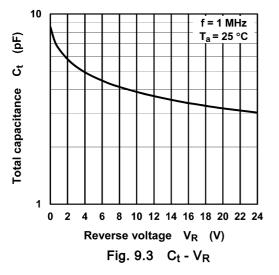


Fig. 8.1 Land Pattern Dimensions (Unit: mm)

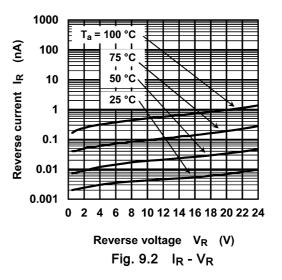
9. Characteristics Curves (Note)



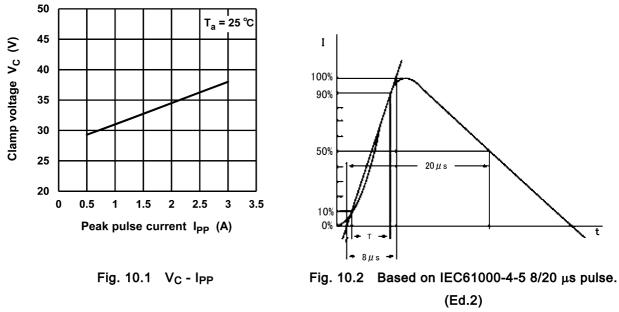




Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



10. Clamp Voltage V_C - Peak Pulse Current (I_{PP}) (Note)



Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

11. ESD Clamp Waveform (Note)

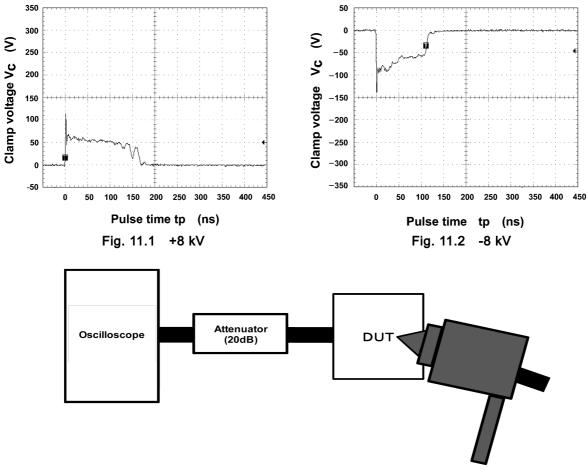


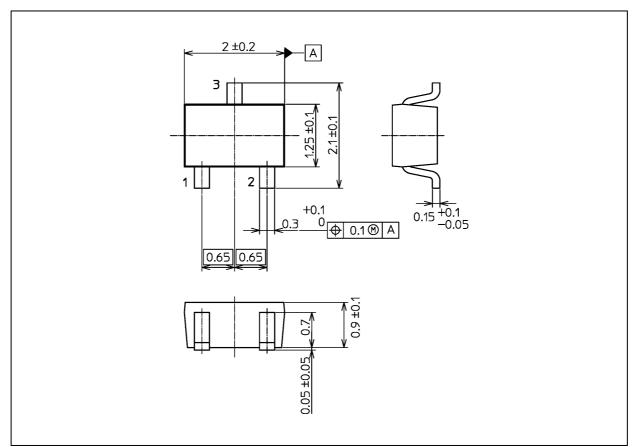
Fig. 11.3 IEC61000-4-2 (Contact)

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

DF3D29FU

Package Dimensions

Unit: mm



Weight: 6.0 mg (typ.)

	Package Name(s)
Nickname: USM	

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