TOSHIBA Diode Silicon Epitaxial Planar Type

HN2D02FU

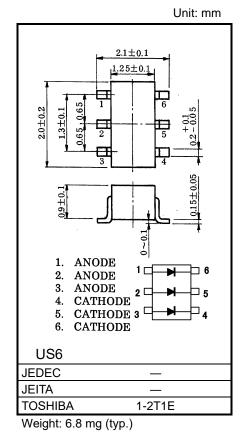
Ultra High Speed Switching Application

- AEC-Q101 Qualified (Note1)
- HN2D02FU is composed of 3 independent diodes.
- Low forward voltage : VF(3) = 0.98 V (typ.)
- Fast reverse recovery time : trr = 1.6 ns (typ.)
- Small total capacitance : CT = 0.5 pF (typ.)

Note1: For detail information, please contact our sales.

Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Maximum (peak) reverse voltage	Vrm	85	V	
Reverse voltage	VR	80	V	
Maximum (peak) forward current	IFM	240 *	mA	
Average forward current	lo	80 *	mA	
Surge current (10 ms)	IFSM	1 *	А	
Power dissipation	P _D (Note 4)	200	mW	
Junction temperature	T _j (Note 2)	150	°C	
	Tj (Note 3)	125		
Storogo tomporaturo	T _{stg} (Note 2)	-55 to 150	°C	
Storage temperature	T _{stg} (Note 3)	-55 to 125		



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: For devices with the ordering part number ending in LF(T.

Note 3: For devices with the ordering part number in other than LF(T.

Note 4: Total rating, Mounted on a FR4 board. (25.4 mm × 25.4 mm × 1.6 mm, Cu pad: 0.32 mm² × 6).

* : This is absolute maximum rating of single diode (Q1, Q2 or Q3).

In the case of using 2 or 3 diodes, the absolute maximum ratings per diodes is 75 % of the single diode one.

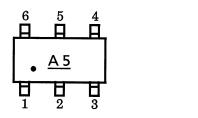
Electrical Characteristics (Q1, Q2, Q3 Common, Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Forward voltage	VF (1)	I _F = 1 mA	—	0.62	_	V
	VF (2)	I _F = 10 mA	—	0.75		
	VF (3)	I _F = 100 mA	_	0.98	1.20	
Reverse current	I _{R (1)}	V _R = 30 V	—	_	0.1	μΑ
	I _{R (2)}	VR = 80 V	—	—	0.5	
Total capacitance	Ст	$V_R = 0 V$, f = 1 MH _z	—	0.5	3.0	pF
Reverse recovery time	t _{rr}	IF = 10mA (Fig.1)	_	1.6	4.0	ns

Marking

Pin Assignment (Top View)

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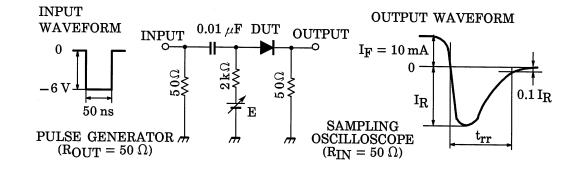


Fig.1 Reverse Recovery Time (trr) Test Circuit

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0.2

0.1

0 0.1

0.3 0.5

1

3 5 10

REVERSE VOLTAGE V_R (V)

 $I_R - V_R$

Ta=100°C

75

50

25

40

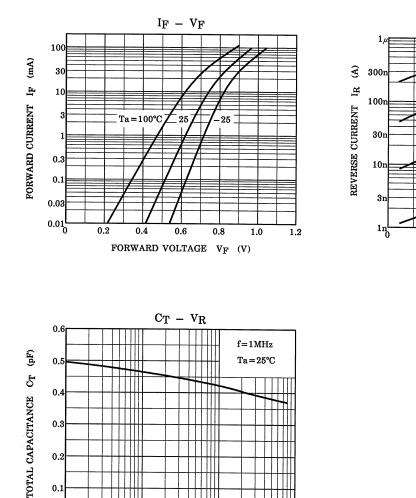
REVERSE VOLTAGE V_R (V)

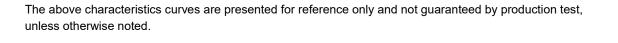
60

80

20

Characteristics Curves (Q1, Q2, Q3 Common, Ta = 25°C)





30 50

100

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