0.6±0.05

1.0 ± 0.05

0.38

Unit: mm

TOSHIBA Diode Silicon Epitaxial PIN Type

# JDP2S02ACT

#### UHF~VHF Band RF Switch Applications

- Suitable for reducing set's size as a result from enabling high-density mounting due to 2-pin small packages.
- Low series resistance:  $r_s = 1.0 \Omega$  (typ.)
- Low capacitance:  $C_T = 0.3 \text{ pF}$  (typ.)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Reverse voltage	V <sub>R</sub>	30	V
Forward current	١ <sub>F</sub>	50	mA
Junction temperature	Tj	150	°C
Storage temperature range	T <sub>stg</sub>	-55 to 150	°C

JEDEC JÉITA TOSHIBA

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

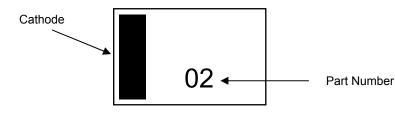
Please design the appropriate reliability upon reviewing the Toshiba Sem Conductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### Electrical Characteristics (Ta = 25°C

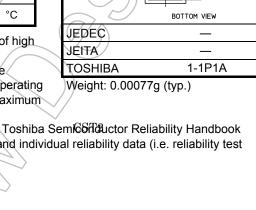
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse voltage	VR	I <sub>R</sub> = 10 μA	30	_	_	V
Reverse current	IR	V <sub>R</sub> <i>≓</i> 30 V	_	_	0.1	μA
Forward voltage	MF	I <sub>F</sub> = 50 mA	_	0.9	0.94	V
Capacitance(Note2)	C1	V <sub>R</sub> = 1 V, f = 1 MHz	_	0.3	0.4	pF
Series resistance	ſs	I <sub>F</sub> = 10 mA, f = 100 MHz		1.0	1.5	Ω

Note1: Signal level when capacitance is measured. Vsig = 100 mVrms

#### Marking

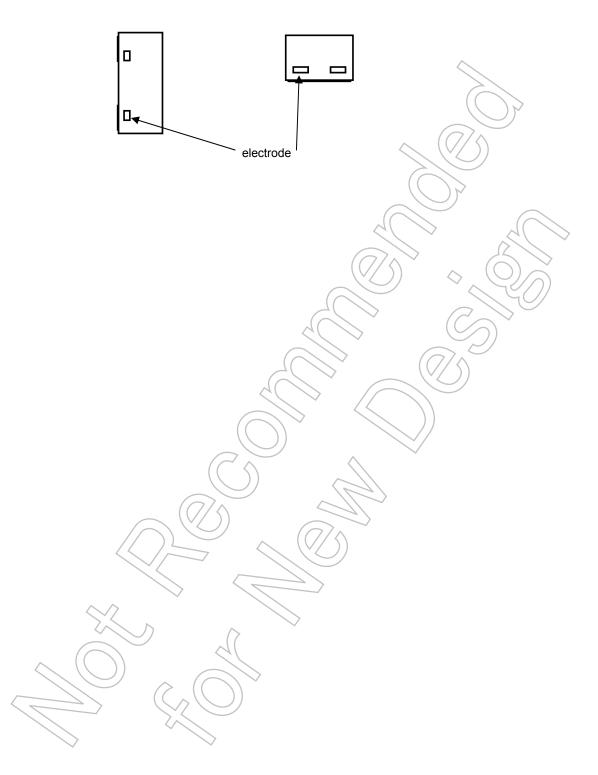


Start of commercial production 2003-11

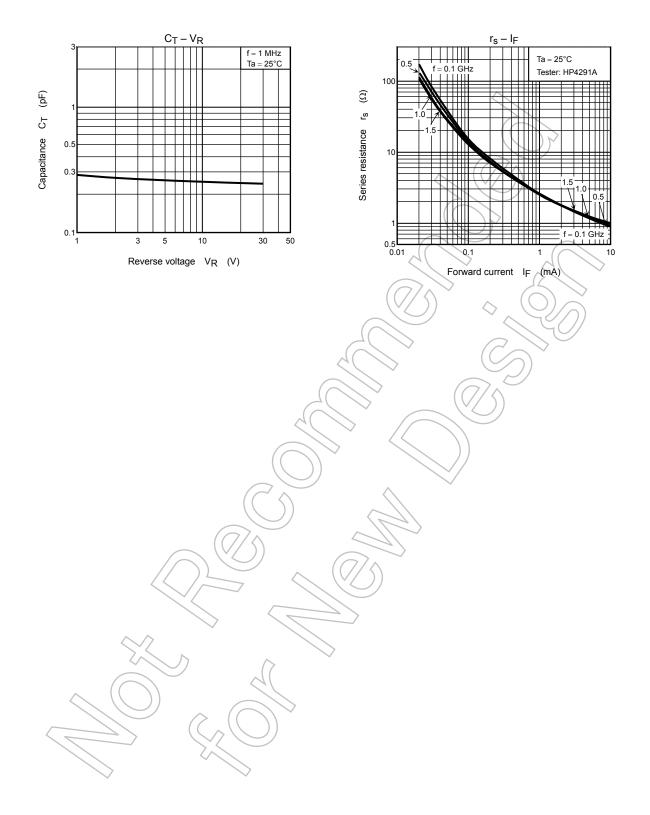


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Note2: This package has 2 exposed electrodes on each side because of the manufacturing process. Please be careful about the handling enough.



#### Typical Performance Curves (Ta = 25°C)



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