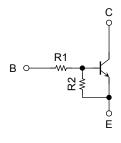
TOSHIBA Transistor Silicon NPN Epitaxial Type (PCT process) (Bias Resistor Built-in Transistor)

### RN1901FS,RN1902FS,RN1903FS RN1904FS,RN1905FS,RN1906FS

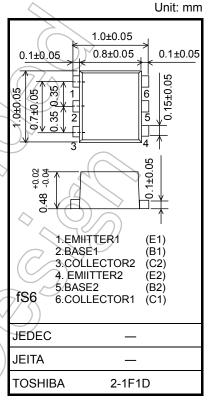
Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- Two devices are incorporated into a fine pitch small mold (6-pin) package.
- Incorporating a bias resistor into a transistor reduces parts count.
   Reducing the parts count enables the manufacture of ever more compact equipment and lowers assembly cost.
- Complementary to RN2901FS~RN2906FS

#### **Equivalent Circuit and Bias Resistor Values**



Type No.	R1 (kΩ)	R2 (kΩ)
RN1901FS	4.7	4.7
RN1902FS	10	10
RN1903FS	22	22
RN1904FS	47	47 //
RN1905FS	2.2	47
RN1906FS	4.7	47

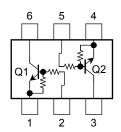


Weight: 0.001g (typ.)

# Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

Characteristics		Symbol	Rating	Unit	
Collector-base voltage	RN1901FS~	V <sub>CBO</sub>	20	V	
Collector-emitter voltage	1906FS	V <sub>CEO</sub>	20	V	
Emitter-base voltage	RN1901FS~ 1904FS	Veno	10	V	
	RN1905FS, 1906FS	VEBO	5		
Collector current	$\rightarrow$ ((	c	50	mA	
Collector power dissipation	RN1901FS~	P <sub>C</sub> (Note 1)	50	mW	
Junction temperature	RN1906FS	Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	-55~150	°C	

# Equivalent Circuit (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Total rating

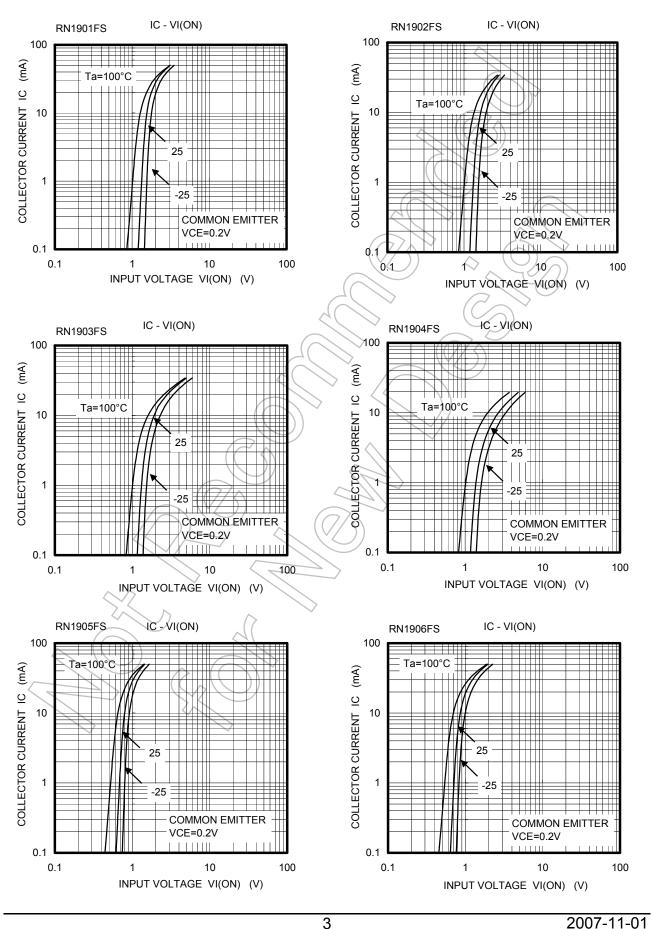


## Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

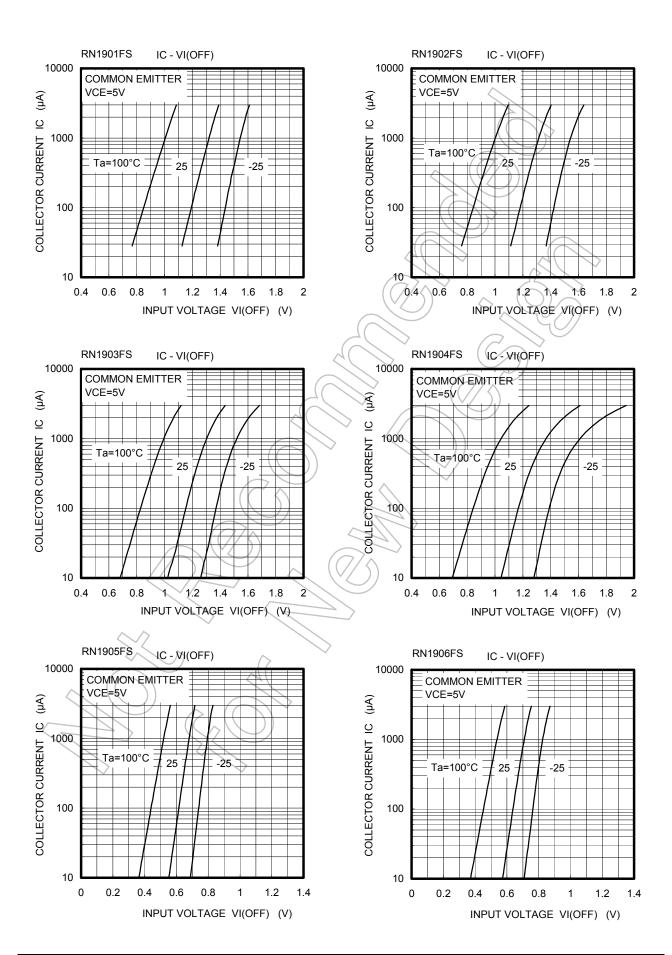
Charac	teristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	RN1901FS~1906FS	I <sub>CBO</sub>	$V_{CB} = 20 \text{ V}, I_E = 0$	_	_	100	nA
		I <sub>CEO</sub>	$V_{CE} = 20 \text{ V}, I_B = 0$		_	500	
Emitter cut-off current	RN1901FS		V <sub>EB</sub> = 10 V, I <sub>C</sub> = 0	0.89	_	1.33	- mA
	RN1902FS	l <sub>EBO</sub>		0.41		0.63	
	RN1903FS			0.18	) /_	0.29	
	RN1904FS			0.088		0.133	
	RN1905FS		V <sub>EB</sub> = 5 V, I <sub>C</sub> = 0	0.085		0.127	
	RN1906FS			0.08	_	0.121	
DC current gain	RN1901FS		V <sub>CE</sub> = 5 V, I <sub>C</sub> = 10 mA	30	_	_	
	RN1902FS			60		_	
	RN1903FS	h		100	14	$\nearrow$	
	RN1904FS	h <sub>FE</sub>		120	2-//	> —	
	RN1905FS			120		) —	
	RN1906FS			120	90	_	
Collector-emitter saturation voltage	RN1901FS~1906FS	V <sub>CE</sub> (sat)	$I_C = 5 \text{ mA},$ $I_B \neq 0.25 \text{ mA}$	$\widehat{\mathcal{A}}$	_	0.15	V
	RN1901FS	V <sub>L</sub> (ON)	V <sub>CE</sub> = 0.2 V, I <sub>C</sub> = 5 mA	1.0	_	2.0	V
	RN1902FS			1.0	_	2.2	
	RN1903FS			1.1	_	2.7	
Input voltage (ON)	RN1904FS			1.2	_	3.6	
	RN1905FS	))		0.6		1.1	
	RN1906FS			0.6	_	1.2	
Input voltage (OFF)	RN1901FS~1904FS	., (	EV I- 0.1 mA	0.8	_	1.5	
	RN1905FS, 1906FS	V <sub>I</sub> (OFF)	$V_{CE} = 5 \text{ V}, I_{C} = 0.1 \text{ mA}$	0.4	_	0.8	V
Collector output capacitance	RN1901FS~1906FS	Cob	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 1 MHz	_	1.2	_	pF
Input resistor	RN1901FS	R1	_	3.76	4.7	5.64	- kΩ
	RN1902FS			8	10	12	
	RN1903FS			17.6	22	26.4	
	RN1904FS			37.6	47	56.4	
	RN1905FS			1.76	2.2	2.64	
	RN1906FS			3.76	4.7	5.64	
	RN1901FS~1904FS			0.8	1.0	1.2	
Resistor ratio	RN1905FS	R1/R2	_	0.0376	0.0468	0.0562	
	RN1906FS			0.08	0.1	0.12	

2

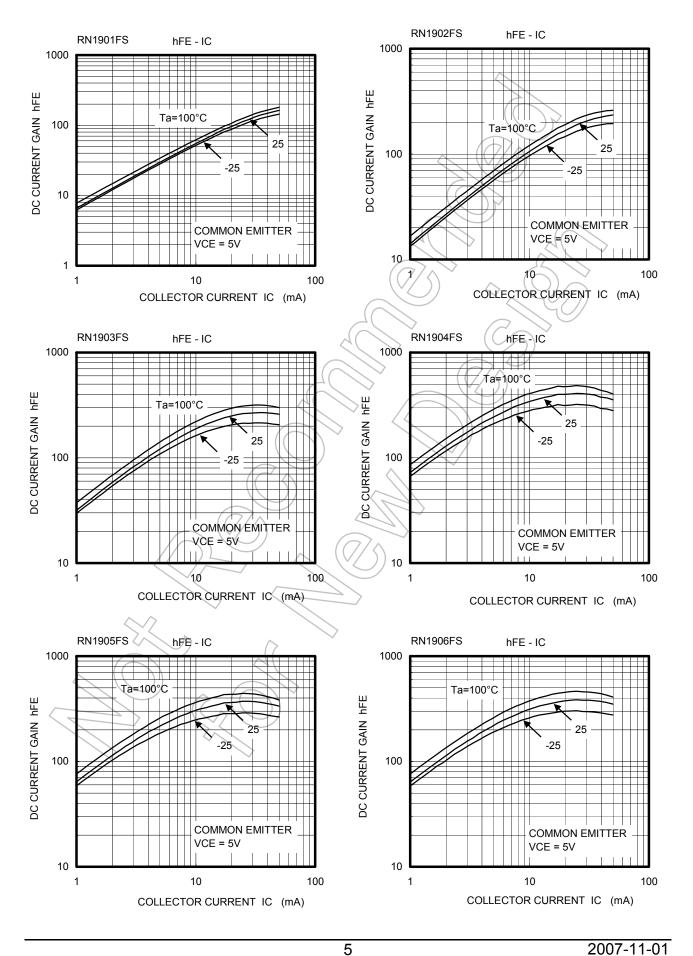
#### (Q1, Q2 Common)



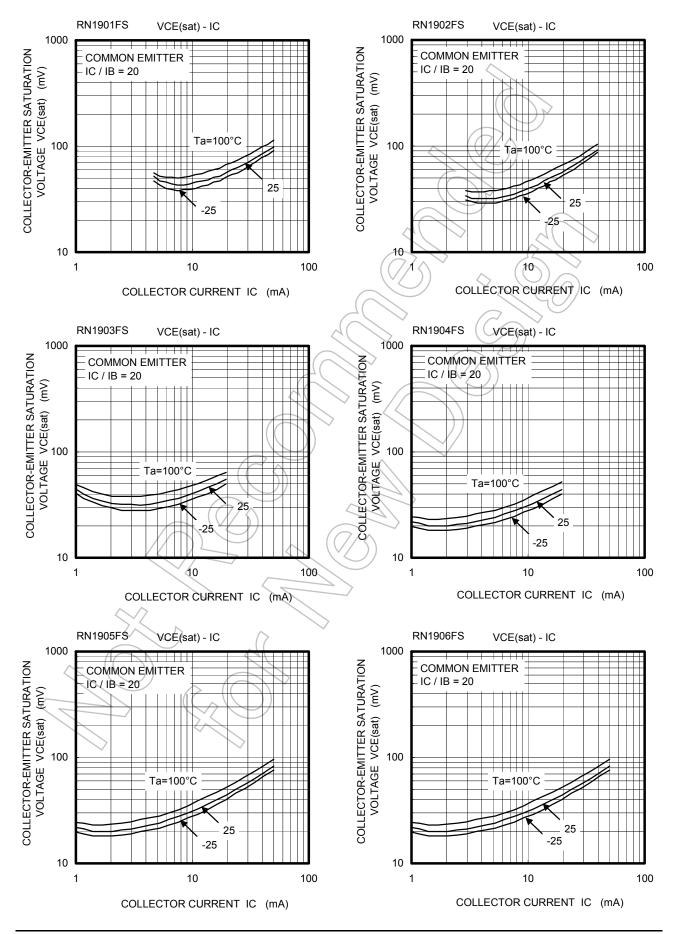
(Q1,Q2 Common)

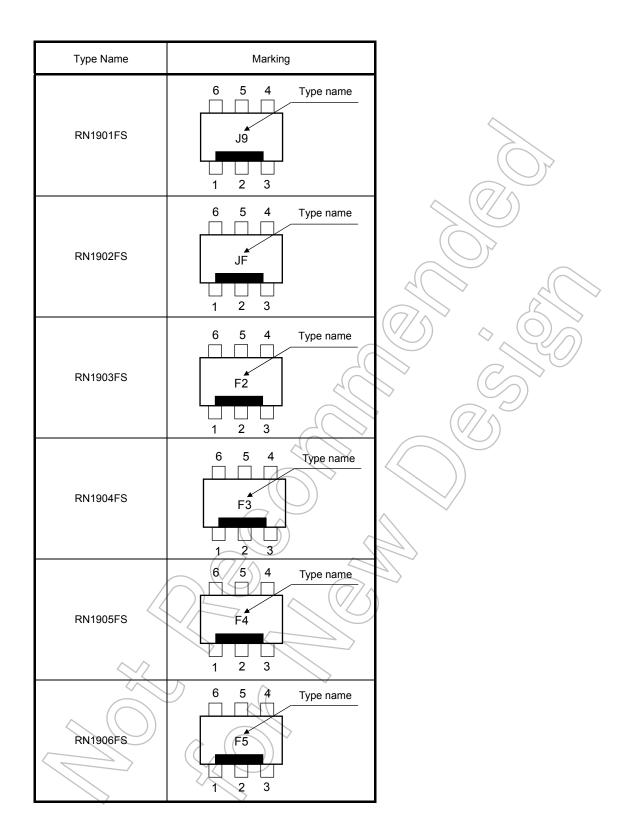


#### (Q1,Q2 Common)



#### (Q1, Q2 Common)





#### **Handling Precaution**

When handling individual devices (which are not yet mounted on a circuit board), be sure that the environment is protected against electrostatic discharge. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

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