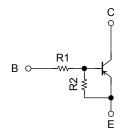
TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT process) (Bias Resistor built-in Transistor)

# RN2107ACT, RN2108ACT, RN2109ACT

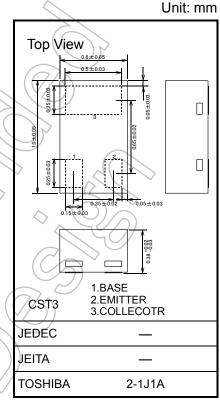
Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- Extra small package (CST3) is applicable for extra high density fabrication.
- Incorporating a bias resistor into a transistor reduces parts count.
- Reducing the parts count enable the manufacture of ever more compact equipment and save assembly cost.
- Complementary to RN1107ACT to RN1109ACT

### **Equivalent Circuit and Bias Resistor Values**



Type No.	R1 (kΩ)	R2 (kΩ)
RN2107ACT	10	47
RN2108ACT	22	<b>47</b>
RN2109ACT	47	22



Weight: 0.75 mg (typ.)

## Absolute Maximum Ratings (Ta = 25°C)

Charact	Symbol	Rating	Unit		
Collector-base voltage	RN2107ACT to RN2109ACT	V <sub>CBO</sub>	-50	V	
Collector-emitter voltage	RNZ107ACT (0 RNZ109ACT	V <sub>CEO</sub>	-50	V	
	RN2107ACT		-6	V	
Emitter-base voltage	RN2108ACT	$V_{EBO}$	-7		
	RN2109ACT		-15		
Collector current		IC	-80	mA	
Collector power dissipation	RN2107ACT to RN2109ACT	PC	100*	mW	
Junction temperature		Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	

<sup>\*:</sup> Mounted on FR4 board (10 mm × 10 mm × 1 mm)

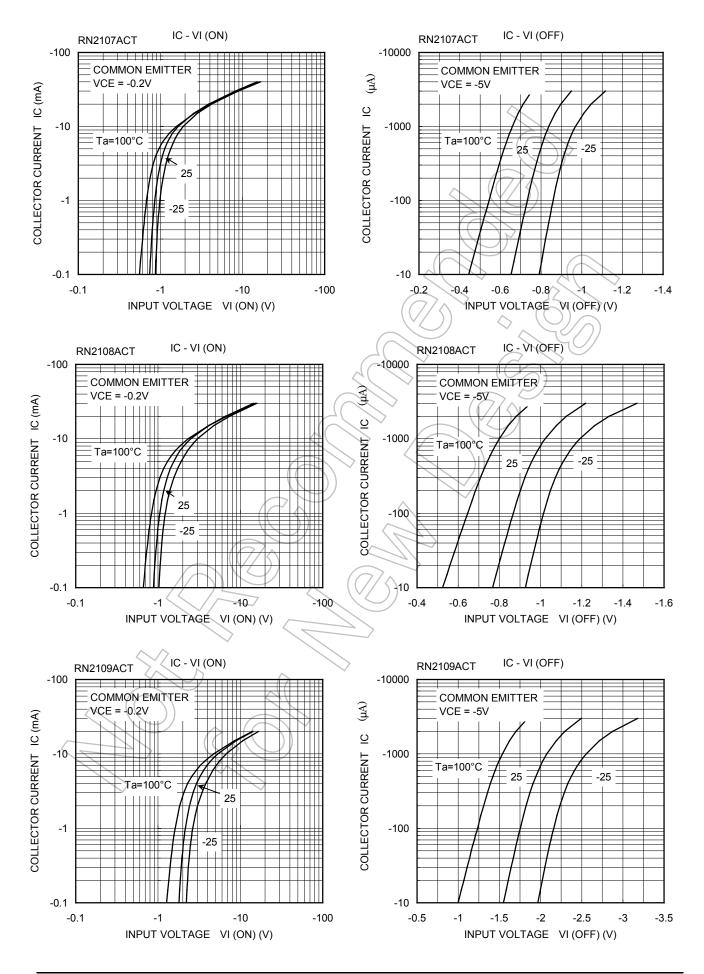
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e.operatingtemperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

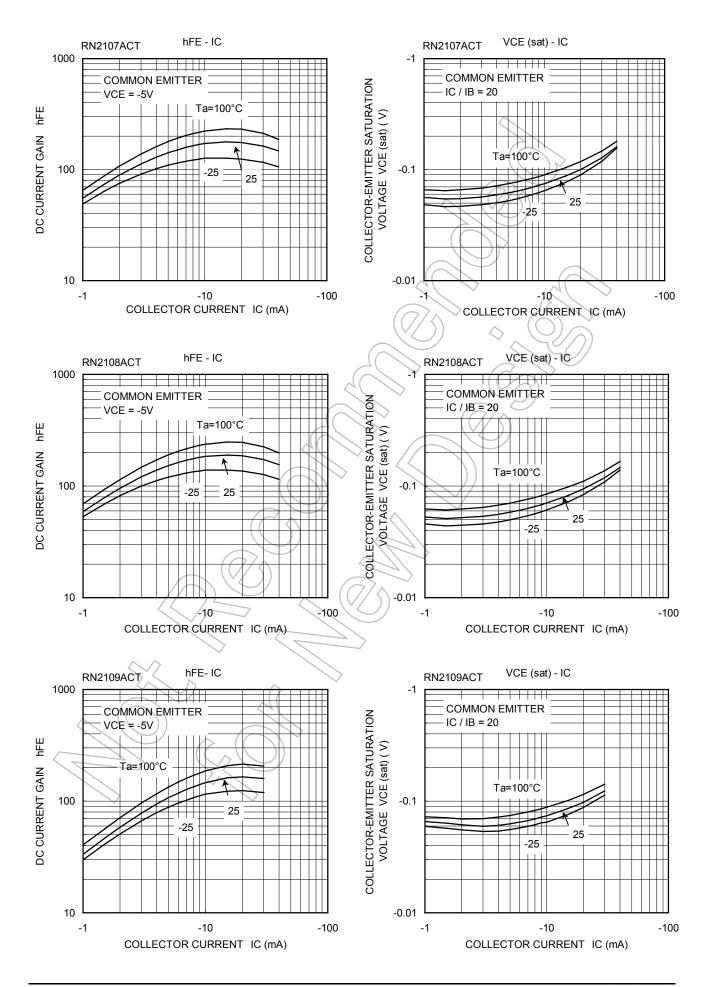
2004-08



## Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector out off ourrent	RN2107ACT to 2109ACT	I <sub>CBO</sub>	$V_{CB} = -50 \text{ V}, I_E = 0$	_	_	-100	nA
Collector cut-off current		I <sub>CEO</sub>	$V_{CE} = -50 \text{ V}, I_B = 0$	_	_	-500	
	RN2107ACT	I <sub>EBO</sub>	$V_{EB} = -6 \text{ V}, I_C = 0$	-0.088	_	-0.131	mA
Emitter cut-off current	RN2108ACT		$V_{EB} = -7 \text{ V}, I_{C} = 0$	-0.085		-0.126	
	RN2109ACT		$V_{EB} = -15 \text{ V}, I_C = 0$	0.182	) /_	-0.271	
	RN2107ACT		$V_{CE} = -5 V$ , $I_{C} = -10 \text{ mA}$	80	_		_
DC current gain	RN2108ACT	h <sub>FE</sub>		80		—	
	RN2109ACT			70			
Collector-emitter saturation voltage	RN2107ACT to 2109ACT	V <sub>CE (sat)</sub>	$I_{C} = -5 \text{ mA},$ $I_{B} = -0.25 \text{ mA}$			-0.15	٧
	RN2107ACT	VI (ON)	V <sub>CE</sub> = -0.2 V, I <sub>O</sub> = -5 mA	-0.8	14	-1.8	V
Input voltage (ON)	RN2108ACT			-1.0	) <del>-</del>	-3.0	
	RN2109ACT			<del>-2.0</del>	4	-6.4	
	RN2107ACT	V <sub>I</sub> (OFF)	$V_{CE} = -5 \text{ V},$ $J_{C} = -0.1 \text{ mA}$	-0.6	> <u></u>	-0.9	V
Input voltage (OFF)	RN2108ACT			-0.7	_	-1.2	
	RN2109ACT			-1.5		-2.6	
Collector output capacitance	RN2107ACT to 2109ACT	Cob	V <sub>CB</sub> = -10 V, I <sub>E</sub> = 0, f = 1 MHz	_	0.9		pF
	RN2107ACT	R1		8	10	12	kΩ
Input resistor	RN2108ACT			17.6	22	26.4	
	RN2109ACT			37.6	47	56.4	
	RN2107ACT	R1/R2		0.17	0.213	0.255	_
Resistor ratio	RN2108ACT		_	0.374	0.468	0.562	
	RN2109ACT		)	1.71	2.14	2.56	





5

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