

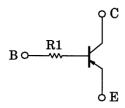
TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT Process)(Bias Resistor built-in Transistor)

## RN2112MFV, RN2113MFV

# Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- Ultra-small package, suited to very high density mounting
- Incorporating a bias resistor into the transistor reduces the number of parts, so
  enabling the manufacture of ever more compact equipment and lowering assembly
  cost
- A wide range of resistor values is available for use in various circuits.
- Complementary to the RN1112MFV, RN1113MFV

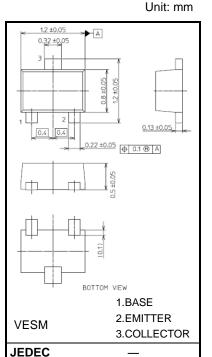
#### **Equivalent Circuit**



Note:

#### **Absolute Maximum Ratings (Ta = 25°C)**

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	-50	V
Collector-emitter voltage	VCEO	-50	V
Emitter-base voltage	VEBO	-5	V
Collector current	Ic	-100	mA
Collector power dissipation	Pc (Note 1)	150	mW
Junction temperature	Tj	150	°C
Storage temperature range	T <sub>stg</sub>	−55 to 150	°C



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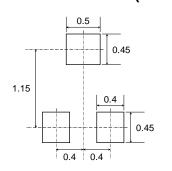
Weight: 1.5 mg (typ.)

JEITA TOSHIBA

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Mounted on an FR4 board (25.4 mm  $\times$  25.4 mm  $\times$  1.6 mm)

#### Land Pattern Dimensions (for reference only)



Start of commercial production 2005-02

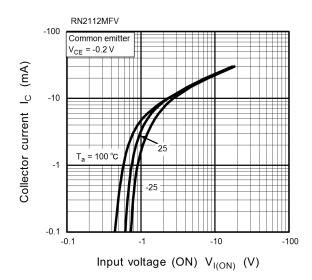
Unit:mm

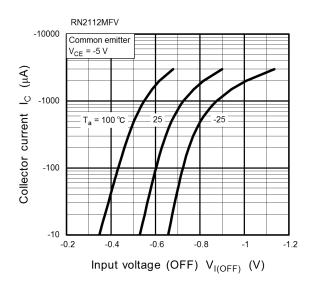


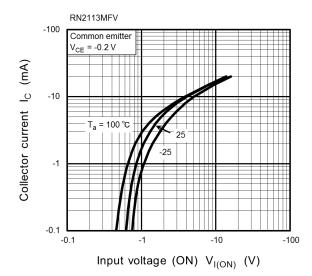
### Electrical Characteristics (Ta = 25°C)

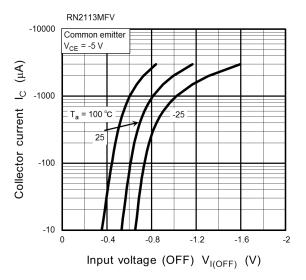
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cutoff current		Ісво	V <sub>CB</sub> = −50 V, I <sub>E</sub> = 0 A	_	_	-100	nA
Emitter cutoff current		IEBO	VEB = -5 V, IC = 0 A	_	_	-100	nA
DC current gain		hFE	VCE = −5 V, IC = −1 mA	120	_	400	_
Collector-emitter saturation voltage		VCE (sat)	IC = -5  mA, IB = -0.5  mA	_	-0.1	-0.3	V
Collector output capacitance		C <sub>ob</sub>	V <sub>CB</sub> = −10 V, I <sub>E</sub> = 0 A, f = 1 MHz	_	0.9	_	рF
Input resistor	RN2112MFV	- R1	_	15.4	22	28.6	kΩ
	RN2113MFV			32.9	47	61.1	



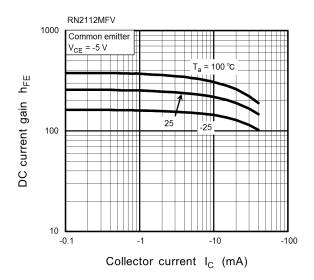


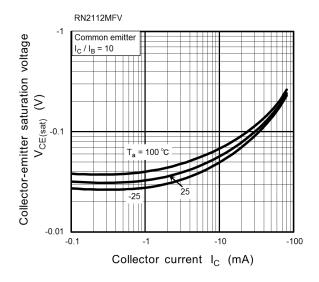


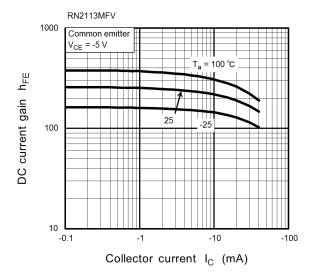


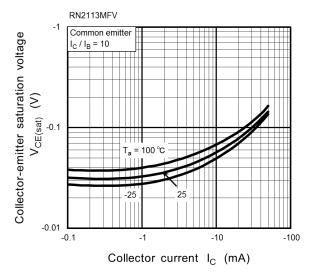














### Marking

Type Name	Marking	
RN2112MFV	Type Name Y N	
RN2113MFV	Type Name Y P	



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