

TOSHIBA Transistor Silicon PNP/NPN Epitaxial Type (PCT Process) (Transistor with Built-in Bias Resistor)

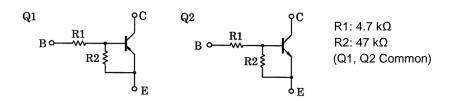
# **RN4906**

### Switching, Inverter Circuit, Interface Circuit and Driver Circuit

- AEC-Q101 Qualified (Note1)
- Including two devices in US6 (ultra super mini type with 6 leads)
- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process and miniaturize equipment.

Note1: For detail information, please contact our sales.

### **Equivalent Circuit and Bias Resistor Values**



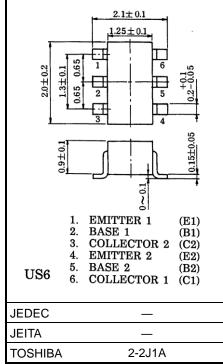
## Q1 Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	-50	V
Collector-emitter voltage	VCEO	-50	V
Emitter-base voltage	VEBO	-5	V
Collector current	Ic	-100	mA

#### Q2 Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	Vсво	50	V
Collector-emitter voltage	VCEO	50	V
Emitter-base voltage	V <sub>EBO</sub>	5	V
Collector current	Ic	100	mA

Unit: mm



Weight: 6.8mg (typ.)

Start of commercial production 1990-10



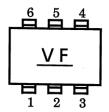
### Q1, Q2 Common Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector power dissipation	Pc *	200	mW
Junction temperature	Tj	150	°C
Storage temperature range	T <sub>stg</sub>	-55 to 150	°C

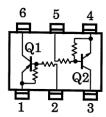
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Total rating

#### Marking



### **Equivalent Circuit (Top View)**





### Q1 Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Ісво	$V_{CB} = -50 \text{ V}, I_{E} = 0 \text{ mA}$	_	_	-100	<b>~</b> ^
Collector cut-off current	ICEO	VCE = -50 V, IB = 0 mA	_	_	-500	nA
Emitter cut-off current	IEBO	$V_{EB} = -5 \text{ V}, I_{C} = 0 \text{ mA}$	-0.074		-0.138	mA
DC current gain	hFE	VCE = −5 V, IC = −10 mA	80	_	_	-
Collector-emitter saturation voltage	VCE (sat)	$I_C = -5 \text{ mA}, I_B = -0.25 \text{ mA}$	_	-0.1	-0.3	V
Input voltage (ON)	VI (ON)	$V_{CE} = -0.2 \text{ V, IC} = -5 \text{ mA}$	-0.7	_	-1.3	V
Input voltage (OFF)	VI (OFF)	VCE = −5 V, IC = −0.1 mA	-0.5	_	-0.8	V
Transition frequency	fΤ	V <sub>CE</sub> = −10 V, I <sub>C</sub> = −5 mA	_	200	_	MHz
Collector output capacitance	C <sub>ob</sub>	V <sub>CB</sub> = −10 V, I <sub>E</sub> = 0 mA, f = 1 MHz	_	3	6	pF

### **Q2 Electrical Characteristics (Ta = 25°C)**

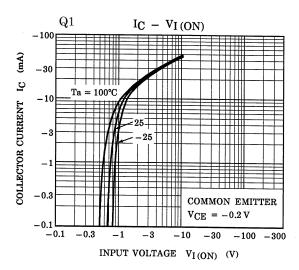
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	I <sub>CBO</sub>	$V_{CB} = 50 \text{ V}, I_{E} = 0 \text{ mA}$	_	_	100	^
Collector cut-off current	ICEO	V <sub>CE</sub> = 50 V, I <sub>B</sub> = 0 mA	_	_	500	nA
Emitter cut-off current	IEBO	VEB = 5 V, IC = 0 mA	0.074	_	0.138	mA
DC current gain	hFE	VCE = 5 V, IC = 10 mA	80	_	_	-
Collector-emitter saturation voltage	V <sub>CE</sub> (sat)	I <sub>C</sub> = 5 mA, I <sub>B</sub> = 0.25 mA	_	0.1	0.3	V
Input voltage (ON)	V <sub>I</sub> (ON)	V <sub>CE</sub> = 0.2 V, I <sub>C</sub> = 5 mA	0.7	_	1.3	V
Input voltage (OFF)	VI (OFF)	VCE = 5 V, IC = 0.1 mA	0.5	_	0.8	V
Transition frequency	fΤ	VCE = 10 V, IC = 5 mA	_	250	_	MHz
Collector output capacitance	Cob	VCB = 10 V, IE = 0 mA, f = 1 MHz	_	3	6	pF

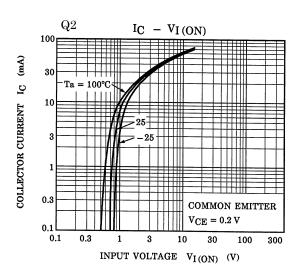
# Q1, Q2 Common Electrical Characteristics (Ta = 25°C)

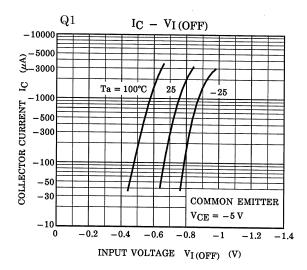
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Input resistor	R1	_	3.29	4.7	6.11	kΩ
Resistor ratio	R1/R2	_	0.09	0.1	0.11	_

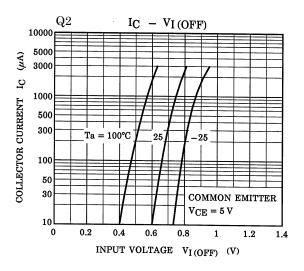


#### **Characteristics Curves**





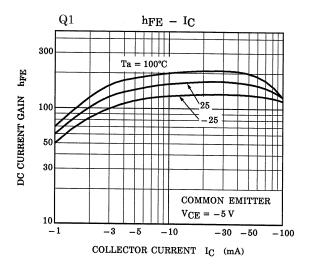


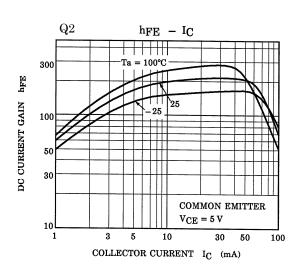


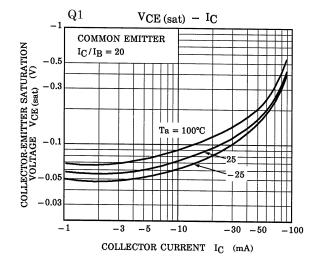
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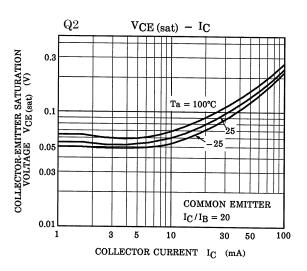


#### **Characteristics Curves**









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