TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOSIII)

# SSM3K48FU

### Load Switching Applications

Unit: mm

2.5-V drive

• Low ON-resistance:  $R_{DS(ON)} = 3.2 \Omega \text{ (max) } (@V_{GS} = 4.0 \text{ V})$ 

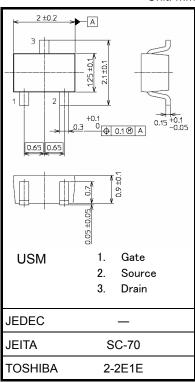
 $R_{DS(ON)} = 5.4 \Omega \text{ (max) } (@V_{GS} = 2.5 \text{ V})$ 

### **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics		Symbol	Rating	Unit	
Drain-Source voltage		V <sub>DSS</sub>	30	٧	
Gate-Source voltage		$V_{GSS}$	±20	<b>V</b>	
Drain current	DC	ΙD	100	mA	
	Pulse	I <sub>DP</sub>	400		
Power dissipation		P <sub>D</sub> (Note 1)	150	mW	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	

Note:

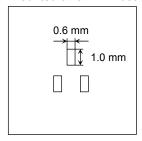
Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.



Weight: 6.0 mg (typ.)

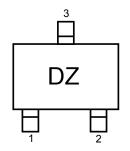
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

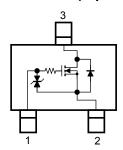
Note 1: Mounted on an FR4 board (25.4 mm  $\times$  25.4 mm  $\times$  1.6 mm, Cu Pad: 0.6 mm<sup>2</sup>  $\times$  3)



#### Marking

#### **Equivalent Circuit (top view)**





Start of commercial production 2010-10

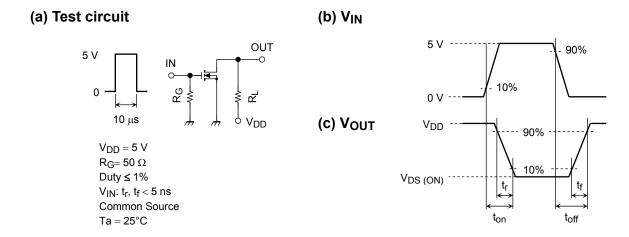
## **Electrical Characteristics (Ta = 25°C)**

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Drain-Source breakdown voltage		V (BR) DSS	$I_D = 0.1 \text{ mA}, V_{GS} = 0 \text{ V}$	30	_	_	V
		V (BR) DSX	$I_D = 0.1 \text{ mA}, V_{GS} = -10 \text{ V}$ (Note 3)	16	_	_	
Drain cut-off current		I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V	_	_	1	μА
Gate leakage current		I <sub>GSS</sub>	$V_{GS} = \pm 12 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μА
Gate threshold voltage		V <sub>th</sub>	$V_{DS} = 3 \text{ V}, I_D = 0.1 \text{ mA}$	0.8	_	1.5	V
Forward transfer admittance		Y <sub>fs</sub>	$V_{DS} = 3 \text{ V}, I_D = 10 \text{ mA}$ (Note 2)	33	_	_	mS
Drain-Source ON resistance		R <sub>DS (ON)</sub>	$I_D = 10 \text{ mA}, V_{GS} = 4 \text{ V}$ (Note 2)	_	2.0	3.2	Ω
			$I_D = 10 \text{ mA}, V_{GS} = 2.5 \text{ V}$ (Note 2)	_	3.0	5.4	
Input capacitance		C <sub>iss</sub>	V <sub>DS</sub> = 3 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	15.1	_	pF
Reverse transfer capacitance		C <sub>rss</sub>		_	7.8	_	
Output capacitance		C <sub>oss</sub>		_	12.4		
Switching time	Turn-on time	t <sub>on</sub>	V <sub>DD</sub> = 5 V, I <sub>D</sub> = 10 mA,	_	35	_	ns
	Turn-off time	t <sub>off</sub>	$V_{GS}$ = 0 to 5 V, $R_G$ = 50 $\Omega$	_	180	_	
Drain-source forward voltage		V <sub>DSF</sub>	$I_D = -100 \text{ mA}, V_{GS} = 0 \text{ V}$ (Note 2)	_	-0.83	-1.2	V

Note 2: Pulse test

Note 3: If a reverse bias is applied between gate and source, this device enters  $V_{(BR)DSX}$  mode. Note that the drain-source breakdown voltage is lowered in this mode.

#### **Switching Time Test Circuit**



#### **Precaution**

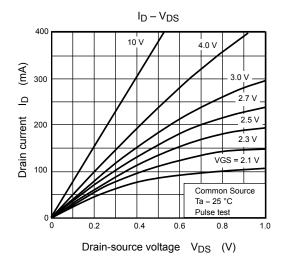
 $V_{th}$  can be expressed as voltage between gate and source when low operating current value is  $I_D$  = 0.1 mA for this product. For normal switching operation,  $V_{GS}$  (on) requires higher voltage than  $V_{th}$  and  $V_{GS}$  (off) requires lower voltage than  $V_{th}$ . (Relationship can be established as follows:  $V_{GS}$  (off) <  $V_{th}$  <  $V_{GS}$  (on) )

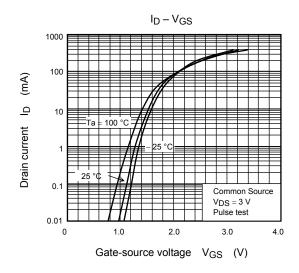
Please take this into consideration for using the device.

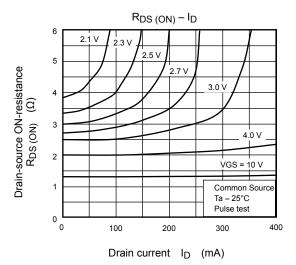
Do not use this device under avalanche mode. It may cause the device to break down.

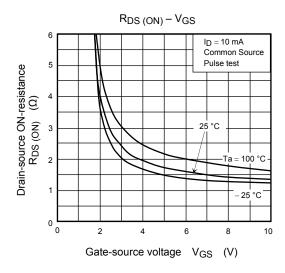
#### **Handling Precaution**

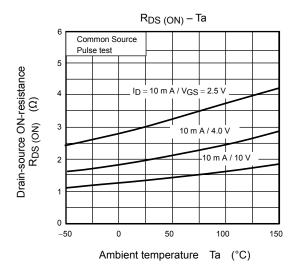
When handling individual devices (which are not yet mounting on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

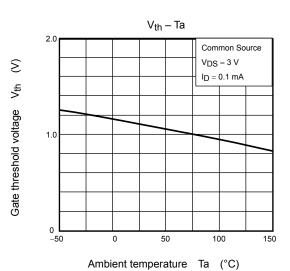




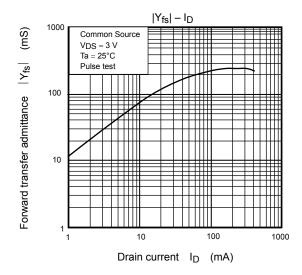


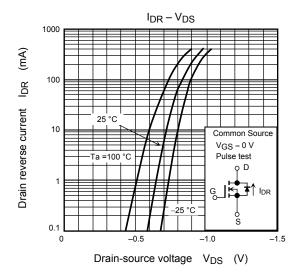


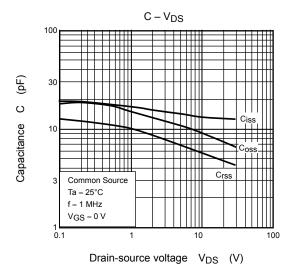


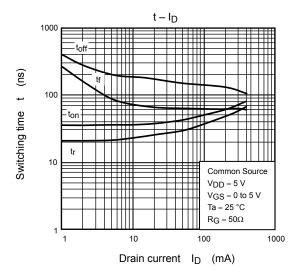


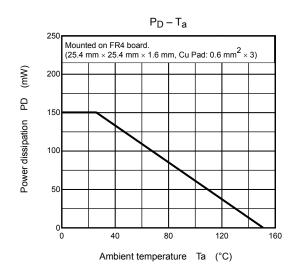
2014-03-01











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