TOSHIBA Field-Effect Transistor Silicon P-Channel MOS Type

SSM6J401TU

- DC/DC Converter Application
- High-Speed Switching Applications

• 4.0V drive

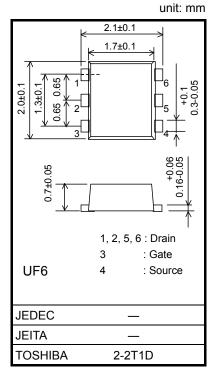
• Low ON-resistance : $R_{DS(ON)} = 145m\Omega$ (max) (@V_{GS} = -4 V)

: $R_{DS(ON)} = 73m\Omega \text{ (max) } (@V_{GS} = -10 \text{ V})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	-30	V	
Gate-source voltage		V_{GSS}	±20	V	
Drain current	DC	ID	-2.5	Α	
	Pulse	I _{DP}	-5.0		
Drain power dissipation		P _D (Note1)	500	mW	
Channel temperature		T _{ch}	150	°C	
Storage temperature		T _{stg}	-55 to 150	°C	

Note 1: Mounted on an FR4 board (25.4 mm \times 25.4 mm \times 1.6 t, Cu Pad: 645 mm²)



Weight: 7.0 mg (typ.)

Electrical Characteristics (Ta = 25°C)

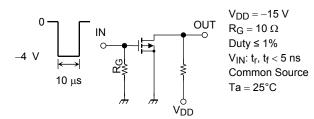
Cha	racteristic	Symbol	Test Condition	Min	Тур.	Max	Unit	
Drain, source breakdown voltege	V (BR) DSS	$I_D = -1 \text{ mA}, V_{GS} = 0 \text{ V}$	-30	_	_	V		
Drain–source breakdown voltage		V (BR) DSX	$I_D = -1 \text{ mA}, V_{GS} = 20 \text{ V}$	-15	_	_	v	
Drain cutoff curre	nt	I _{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	-10	μА	
Gate leakage curi	rent	I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μА	
Gate threshold vo	ltage	V _{th}	$V_{DS} = -5 \text{ V}, I_D = -1 \text{ mA}$	-1.2	_	-2.6	V	
Forward transfer	admittance	Y _{fs}	$V_{DS} = -5 \text{ V}, I_D = -2.0 \text{ A}$ (Note 2)	3.1	6.2	_	S	
Drain-source ON-resistance		R _{DS} (ON)	$I_D = -2.0 \text{ A}, V_{GS} = -10 \text{ V}$ (Note 2)	_	53	73	- mΩ	
			$I_D = -1.5 \text{ A}, V_{GS} = -4 \text{ V}$ (Note 2)	_	85	145		
Input capacitance Output capacitance Reverse transfer capacitance		C _{iss}	$V_{DS} = -15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	730	_	pF	
		C _{oss}		_	110	_		
		C _{rss}		_	90	_		
Total Gate Charge		Q_g	\\ - 45\\ \ - 25 A	_	16	_	nC	
Gate-Source Charge		Q_{gs}	$V_{DS} = -15V$, $I_{D} = -2.5 A$	_	12.8	_		
Gate-Drain Charge		Q_{gd}	- V _{GS} = -10 V		3.2	_		
Switching time	Turn-on time	t _{on}	V _{DD} = -15 V, I _D = -2.0 A	_	33	_	ns	
	Turn-off time	t _{off}	$V_{GS} = 0$ to -4 V, $R_G = 10 \Omega$	_	27	_		
Drain-source forv	vard voltage	V _{DSF}	$I_D = 2.5 \text{ A}, V_{GS} = 0 \text{ V}$ (Note 2)	_	0.8	1.2	V	

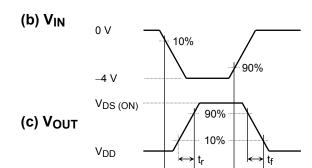
Note 2: Pulse test

Start of commercial production 2007-07

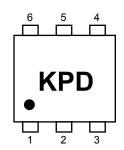
Switching Time Test Circuit

(a) Test Circuit

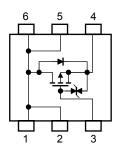




Marking



Equivalent Circuit (top view)



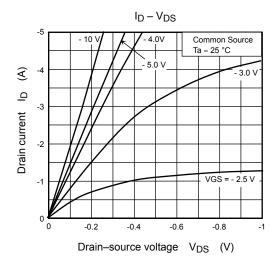
Notice on Usage

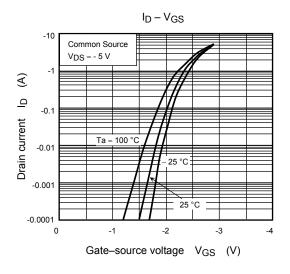
 V_{th} can be expressed as the voltage between gate and source when the low operating current value is I_D = 1 mA for this product. For normal switching operation, V_{GS} (on) requires a higher voltage than V_{th} and V_{GS} (off) requires a lower voltage than V_{th} . (The relationship can be established as follows: V_{GS} (off) < V_{th} < V_{GS} (on).)

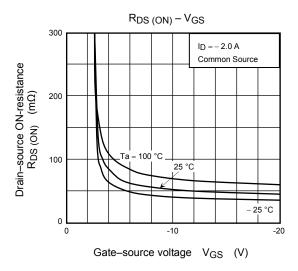
Take this into consideration when using the device.

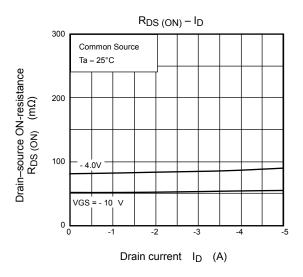
Handling Precaution

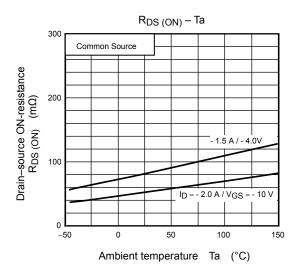
When handling individual devices that are not yet mounted on a circuit board, make sure that the environment is protected against electrostatic discharge. Operators should wear antistatic clothing, and containers and other objects that come into direct contact with devices should be made of antistatic materials.

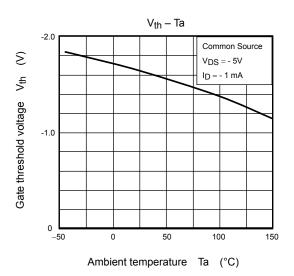




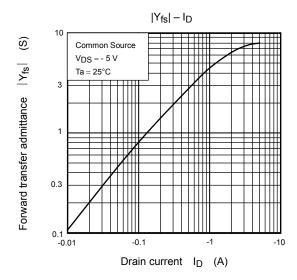


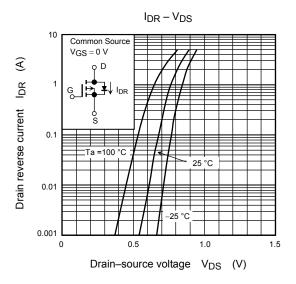


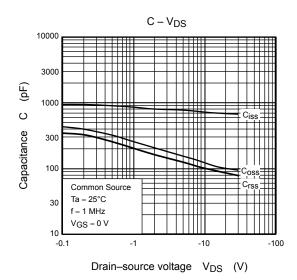


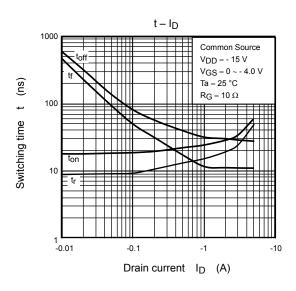


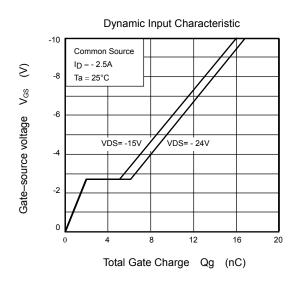
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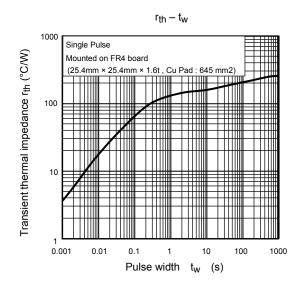


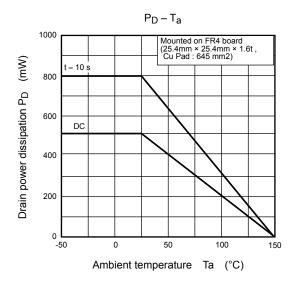






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