TOSHIBA Field-Effect Transistor Silicon N Channel MOS Type (U-MOSIV)

# SSM6N7002BFU

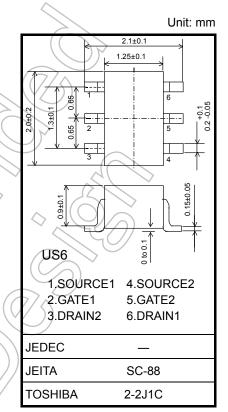
High-Speed Switching Applications

Analog Switch Applications

- Small package
- Low ON-resistance : R<sub>DS(ON)</sub> =
  - :  $R_{DS(ON)} = 3.3 \Omega (max) (@V_{GS} = 4.5 V)$ :  $R_{DS(ON)} = 2.6 \Omega (max) (@V_{GS} = 5 V)$ :  $R_{DS(ON)} = 2.1 \Omega (max) (@V_{GS} = 10 V)$

#### Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 Common)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V <sub>DSS</sub>	60	V
Gate-source voltage		V <sub>GSS</sub>	± 20	$(\checkmark \checkmark)$
Drain current	DC	I <sub>D</sub>	200	mA
	Pulse	I <sub>DP</sub>	800	
Drain power dissipation (Ta = 25°C)		P <sub>D</sub> (Note 1)	300	mW
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C



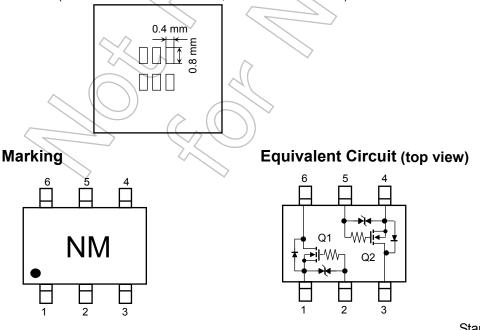
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Weight: 6.8 mg (typ.)

Please design the appropriate reliability upon reviewing the

Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:Total rating, mounted on FR4 board (25.4 mm  $\times$  25.4 mm  $\times$  1.6 mm, Cu Pad: 0.32mm<sup>2</sup>  $\times$ 



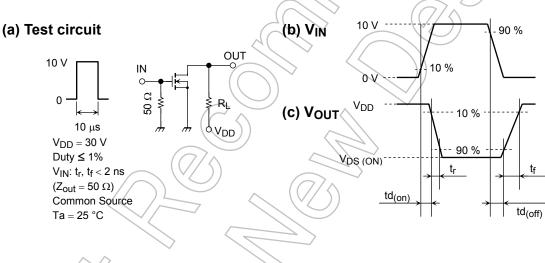
Start of commercial production 2009-08

#### Electrical Characteristics (Ta = 25°C) (Q1, Q2 Common)

Characteristics		Symbol	Test Condition	Min	Тур	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS}=\pm$ 20 V, $V_{DS}$ = 0 V	_	_	±10	μA
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	60	_	_	v
		V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -10 \text{ V}$	45	_	_	
Drain cutoff currer	nt	IDSS	$V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	$\setminus$	_	1	μA
Gate threshold vo	Itage	V <sub>th</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 0.25 \text{ mA}$	1.5		3.1	V
Forward transfer a	admittance	Y <sub>fs</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 200 \text{ mA}$ (Note 2)	225	17-		mS
Drain-source ON-resistance		R <sub>DS (ON)</sub>	$I_D = 500 \text{ mA}, V_{GS} = 10 \text{ V}$ (Note 2)	$\sum_{k=1}^{n}$	1.62	2.1	Ω
			$I_D = 100 \text{ mA}, V_{GS} = 5 \text{ V}$ (Note 2)	$\rightarrow$	1.90	2.6	
			$I_D = 100 \text{ mA}, V_{GS} = 4.5 \text{ V}$ (Note 2)	2_	2.10	3.3	
Input capacitance		C <sub>iss</sub>	(())	—	17.0		pF
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		1.9		
Output capacitance		C <sub>oss</sub>	$\leq \langle \rangle$	_	3.6	$\rightarrow$	
Switching time	Turn-on delay time	td <sub>(on)</sub>	$V_{DD} = 30 \text{ V}, \text{ I}_{D} = 200 \text{ mA}, V_{GS} = 0 \text{ to } 10 \text{ V}$	- 6	3.3	6.6	ns
	Turn-off delay time	td <sub>(off)</sub>		-(C	) 14.5	40	
Drain-source forward voltage		V <sub>DSF</sub>	$I_D = -200 \text{ mA}, V_{GS} = 0 \text{ V}$ (Note 2)	$\langle \cdot \rangle$	-0.84	-1.2	V

Note2: Pulse test

### Switching Time Test Circuit



### Precaution

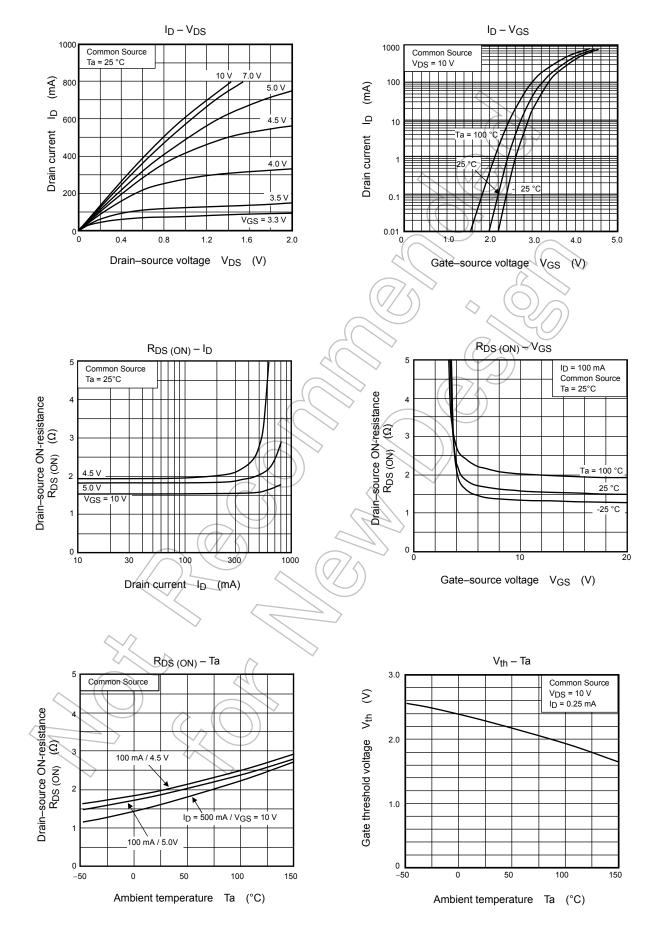
Let V<sub>th</sub> be the voltage applied between gate and source that causes the drain current (I<sub>D</sub>) to be low (0.25 mA for the SSM6N7002BFU). Then, for normal switching operation,  $V_{GS(on)}$  must be higher than  $V_{th}$ , and  $V_{GS(off)}$  must be lower than  $V_{th}$ . This relationship can be expressed as:  $V_{GS(off)} < V_{th} < V_{GS(on)}$ . Take this into consideration when using the device

### **Handling Precaution**

When handling individual devices that are not yet mounted on a circuit board, make sure that the environment is protected against electrostatic discharge. Operators should wear antistatic clothing, and containers and other objects that come into direct contact with devices should be made of antistatic materials.

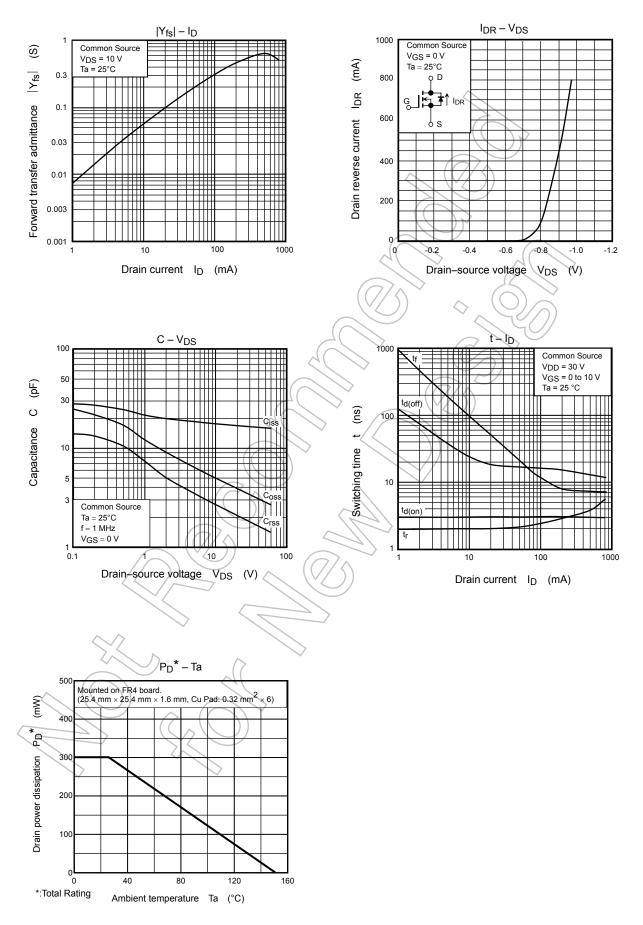
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### (Q1,Q2 Common)



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### (Q1, Q2 Common)



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