TOSHIBA

HSOP3-P-1

Weight: 0.05 g (Typ.)

TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA78L05F,TA78L06F,TA78L07F,TA78L08F,TA78L09F,TA78L10F, TA78L12F,TA78L15F,TA78L18F,TA78L20F,TA78L24F

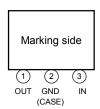
5 V, 6 V, 7 V, 8 V, 9 V, 10 V, 12 V, 15 V, 18 V, 20 V, 24 V

3-Terminal Positive Voltage Regulators

Features

- Best suited to power supply for TTL/CMOS.
- No external parts needed.
- Built-in overheating protection.
- Built-in overcurrent protection.
- Max output current of 150mA. (T_j = 25°C).
- Packaged in PW-mini (SOT-89).

Pin Assignment



Marking

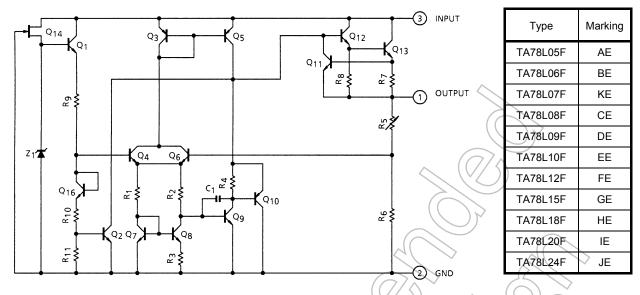
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Part No. (or abbreviation code) *1		Part No. (or abbreviation code)	Part No.
	\sim	AE	TA78L05F
	$\langle \rangle$	BE	TA78L06F
	\square	KE	TA78L07F
Note	\sim	CE	TA78L08F
Lot No. (weekly code)))*1	DE	TA78L09F
(weekly code)		EE	TA78L10F
		FE	TA78L12F
		GE	TA78L15F
		HE	TA78L18F
		IE	TA78L20F
		JE	TA78L24F

Note: A line beside a Lot No. identifies the indication of product Labels. Without a line: [[Pb]]/INCLUDES > MCV With a line: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

The product(s) in this document ("Product") contain functions intended to protect the Product from temporary small overloads such as minor short-term overcurrent or overheating. The protective functions do not necessarily protect Product under all circumstances. When incorporating Product into your system, please design the system (1) to avoid such overloads upon the Product, and (2) to shut down or otherwise relieve the Product of such overload conditions immediately upon occurrence. For details, please refer to the notes appearing below in this document and other documents referenced in this document.

Equivalent Circuit



Absolute Maximum Ratings (Ta = 25°C)

Characteris	tics	Symbol	Rating	Unit	
	TA78L05F		$\square(\bigcirc)$		
	TA78L06F	<			\sim
	TA78L07F			$\langle \langle \rangle$	
	TA78L08F		35		\square
	TA78L09F			~	\sim
0Input voltage	TA78L10F		~	V	
	TA78L12F			$ \rangle\rangle$	
	TA78L15F	// \$			
	TA78L18F	\bigcirc ,	(7/	~	
4	TA78L20F		40		
	TA78L24F				
Output current		Ιουτ	0.15	А	
Power dissipation	(Ta = 25°C)	PD	500	mW	
Operating temperature	\bigcirc	Topr	-30 to 85	°C	
Storage temperature		T _{stg}	-55 to 150	°C	
Junction temperature		(J)	150	°C	
Thermal resistance	$\langle \rangle$	R _{th} (j-a)	250	°C/W	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

TA78L05F Electrical Characteristics (Unless otherwise specified, V_{IN} = 10 V, I_{OUT} = 40 mA, C_{IN} = 0.33 μ F, C_{OUT} = 0.1 μ F, 0°C \leq T_j \leq 125°C)

Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C		4.75	5.0	5.25	V
Line regulation	Regiline	1	T _i = 25°C	7.0 V ≤ V _{IN} ≤ 20 V	(\square)	55	150	mV
	Regiline		1j = 25 C	8.0 V ≤ V _{IN} ≤ 20 V	R	45	100	mv
Load regulation	Reg·load	1	T _j = 25°C	1.0 mA ≤ I _{OUT} ≤ 100 mA	ΖΑ	11	60	mV
Load regulation	Regiloau	1	1j = 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	Y	5.0	30	IIIV
Output voltage	Vout	1	T _i = 25°C	$7.0 V \le V_{IN} \le 20 V$, $1.0 \text{ mA} \le I_{OUT} \le 40 \text{ mA}$	4.65	_	5.35	v
			,	1.0 mA ≤ I _{OUT} ≤ 70 mA	4.65		5.35	
Quiescent current	1-	1	T _j = 25°C	$\langle \langle \rangle \rangle$	_	3.1	6.0	mA
	Ι _Β		T _j = 125°C		- 2	6-\	5.5	ma
Quiessent ourrent change	A1-	1	T _i = 25°C	8.0 V ≤ V _{IN} ≤ 20 V		\mathcal{D}	1.5	mA
Quiescent current change	ΔI _B	1	1j = 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	\mathcal{A}	Ge)	0.1	IIIA
Output noise voltage	V _{NO}	2	Ta = 25°C	, 10 Hz ≤ f ≤ 100 kHz		✓ 40	_	μV _{rms}
Long term stability	ΔV _{OUT} /Δt	1	\sim		77)	12	_	mV/kh
Ripple rejection ratio	R.R.	3	f = 120 Hz 8.0 V ≤ V _{II}	, v ≤ 18 V, Tj = 25°C	41	49	_	dB
Dropout voltage	VD	1	τ _j = 25°C		_	1.7	_	V
Average temperature coefficient of output voltage	T _{CVO}		1 _{OUT} = 5 n	nA		-0.6		mV/°C

TA78L06F Electrical Characteristics (Unless otherwise specified, V_{IN} = 11 V, I_{OUT} = 40 mA, C_{IN} = 0.33 μ F, C_{OUT} = 0.1 μ F, 0°C \leq T_j \leq 125°C)

Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C		5.7	6.0	6.3	V
Line regulation	Reg·line	1	T _i = 25°C	8.1 V ≤ V _{IN} ≤ 21 V	$(\square$	50	150	mV
	Regiline		1j = 25 C	9.0 V ≤ V _{IN} ≤ 21 V	\mathcal{P}	45	110	
Load regulation	Reg·load	1	T _i = 25°C	1.0 mA ≤ I _{OUT} ≤ 100 mA	ZΑ	12	70	mV
	Regillau		1 _j = 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	7	5.5	35	IIIV
Output voltage	Vout	1	T _i = 25°C	8.1 V \leq V _{IN} \leq 21 V, 1.0 mA \leq I _{OUT} \leq 40 mA	5.58	Ι	6.42	v
			,	1.0 mA ≤ I _{OUT} ≤ 70 mA	5.58		6.42	
Quiescent current	IB	1	T _j = 25°C		_	3.1	6.0	mA
	ıВ		T _j = 125°C		- 6	$\leq \sim$	5.5	IIIA
Quiescent current change	Δl _B	1	T _i = 25°C	9.0 V ≤ V _{IN} ≤ 20 V		246	1.5	mA
Quescent current change	ΔIB		1 _j = 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	X	Y	0.1	IIIA
Output noise voltage	V _{NO}	2	Ta = 25°C	, 10 Hz ≤ f ≤ 100 kHz		✓ 40	_	μV _{rms}
Long term stability	$\Delta V_{OUT} / \Delta t$	1			$\sim \rightarrow \sim$	14	_	mV/kh
Ripple rejection ratio	R.R.	3	f = 120 Hz 9.0 V ≤ V _{II}	, N≤ 19 V, Tj = 25°C	39	47	_	dB
Dropout voltage	VD	1	τ _j = 25°C		_	1.7	_	V
Average temperature coefficient of output voltage	T _{CVO}		1 _{OUT} = 5 n	nA	_	-0.7	_	mV/°C

TA78L07F Electrical Characteristics (Unless otherwise specified, V_{IN} = 12 V, I_{OUT} = 40 mA, C_{IN} = 0.33 μ F, C_{OUT} = 0.1 μ F, 0°C \leq T_j \leq 125°C)

Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C		6.65	7.0	7.35	V
Line regulation	Degline	1	$T_{\rm c} = 25^{\circ}$ C	9.2 V ≤ V _{IN} ≤ 22 V	(-)	50	160	mV
Line regulation	Reg·line	I	T _j = 25°C	10 V ≤ V _{IN} ≤ 22 V	J.	45	115	mv
Load regulation	Poglad	1	T _i = 25°C	1.0 mA ≤ I _{OUT} ≤ 100 mA	ΖΑ	13	75	mV
	Reg·load	1	1j = 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	Y	6.0	40	IIIV
Output voltage	Vout	1	T _i = 25°C	$9.2 V \le V_{IN} \le 22 V$, 1.0 mA $\le I_{OUT} \le 40 \text{ mA}$	6.51	_	7.49	v
			,	1.0 mA ≤ I _{OUT} ≤ 70 mA	6.51		7.49	
Quiescent current	1-	1	T _j = 25°C		_	3.1	6,5	mA
	IB	I	T _j = 125°C		- 2	6-/	6.0	IIIA
Quiescent current change	Δl _B	1	T _i = 25°C	10 V ≤ V _{IN} ≤ 22 V	<u> </u>	\mathcal{D}	1.5	mA
Quescent current change	ΔIB	I	1 _j = 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	A	Y	0.1	IIIA
Output noise voltage	V _{NO}	2	Ta = 25°C	, 10 Hz ≤ f ≤ 100 kHz		> 50	_	μV _{rms}
Long term stability	ΔV _{OUT} /Δt	1			~_)	17	_	mV/kh
Ripple rejection ratio	R.R.	3	f = 120 Hz 10 V ≤ VIN	≤ 20 V, Tj = 25°C	37	46	_	dB
Dropout voltage	VD	1	τ _j = 25°C		_	1.7	_	V
Average temperature coefficient of output voltage	T _{CVO}		1 _{OUT} = 5 n	nA	_	-0.75	_	mV/°C

TA78L08F Electrical Characteristics (Unless otherwise specified, V_{IN} = 14 V, I_{OUT} = 40 mA, C_{IN} = 0.33 μ F, C_{OUT} = 0.1 μ F, 0°C \leq T_j \leq 125°C)

Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C		7.6	8.0	8.4	V
Line regulation	Degline	1	$T_{\rm c} = 25^{\circ}$	10.5 V ≤ V _{IN} ≤ 23 V	(-)	20	175	mV
Line regulation	Reg·line	1	T _j = 25°C	11 V ≤ V _{IN} ≤ 23 V	R	12	125	mv
Lood regulation	Pogload	1	T _i = 25°C	1.0 mA ≤ I _{OUT} ≤ 100 mA	ΖΑ	15	80	mV
Load regulation	Reg·load	1	1j = 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	Y	7.0	40	
Output voltage	Vout	1	T _i = 25°C	$10.5 V \le V_{IN} \le 23 V$, $1.0 \text{ mA} \le I_{OUT} \le 40 \text{ mA}$	7.44	_	8.56	v
			,	1.0 mA ≤ I _{OUT} ≤ 70 mA	7.44		8.56	
Quiescent current	1-	1	T _j = 25°C	$\langle \langle \rangle \rangle$	_	3.1	6,5	mA
	Ι _Β	1	T _j = 125°C		- 2	5-7	6.0	mA
Quiescent current change	Δl _B	1	T _i = 25°C	11 V ≤ V _{IN} ≤ 23 V	~_(246	1.5	mA
Quescent current change	ΔIB	1	1j = 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	Ì	Y.	0.1	- IIIA
Output noise voltage	V _{NO}	2	Ta = 25°C	, 10 Hz ≤ f ≤ 100 kHz		○ 60	_	μV _{rms}
Long term stability	ΔV _{OUT} /Δt	1	\sim		~))	20	_	mV/kh
Ripple rejection ratio	R.R.	3	f = 120 Hz 12 V ≤ VIN	s ≥ 23 V, Tj = 25°C	37	45	_	dB
Dropout voltage	VD	1	τ _j = 25°C		_	1.7	_	V
Average temperature coefficient of output voltage	T _{CVO}		1 _{OUT} = 5 n	nA	_	-0.8		mV/°C

TA78L09F Electrical Characteristics (Unless otherwise specified, V_{IN} = 15 V, I_{OUT} = 40 mA, C_{IN} = 0.33 μ F, C_{OUT} = 0.1 μ F, 0°C \leq T_j \leq 125°C)

Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C		8.55	9.0	9.45	V
Line regulation	Degline	1	$T_{\rm c} = 25^{\circ}$ C	11.4 V ≤ V _{IN} ≤ 24 V	(\square)	80	200	mV
Line regulation	Reg·line	I	T _j = 25°C	12 V ≤ V _{IN} ≤ 24 V	R	20	160	mv
Load regulation	Poglad	1	T _i = 25°C	1.0 mA ≤ I _{OUT} ≤ 100 mA	ΖΑ	17	90	mV
Loau regulation	Reg·load	1	1j = 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	Y	8.0	45	
Output voltage	Vout	1	T _i = 25°C	$11.4 V \le V_{IN} \le 24 V$, $1.0 \text{ mA} \le I_{OUT} \le 40 \text{ mA}$	8.37	_	9.63	v
			,	1.0 mA ≤ I _{OUT} ≤ 70 mA	8.37		9.63	
Ouissaant ourrant	1-	1	T _j = 25°C	$\langle \langle \rangle \rangle$	—	3.2	6.5	mA
Quiescent current	IB	I	T _j = 125°C		- 2	6-\	6.0	ma
Quisseent surrent shange	A1-	1	T _i = 25°C	12 V ≤ V _{IN} ≤ 24 V	(\mathcal{D}	1.5	mA
Quiescent current change	ΔI _B	1	1j = 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	\mathcal{A}	GC/	0.1	- IIIA
Output noise voltage	V _{NO}	2	Ta = 25°C	, 10 Hz ≤ f ≤ 100 kHz		65 65	_	μV _{rms}
Long term stability	$\Delta V_{OUT}/\Delta t$	1	\sim		~))	21	_	mV/kh
Ripple rejection ratio	R.R.	3	f = 120 Hz 12 V ≤ V _{IN}	s ≥ 24 V, Tj = 25°C	36	44	_	dB
Dropout voltage	VD	1	τ _j = 25°C		_	1.7	_	V
Average temperature coefficient of output voltage	T _{CVO}		1 _{OUT} = 5 n	nA	_	-0.85	_	mV/°C

TA78L10F Electrical Characteristics (Unless otherwise specified, V_{IN} = 16 V, I_{OUT} = 40 mA, C_{IN} = 0.33 μ F, C_{OUT} = 0.1 μ F, 0°C \leq T_j \leq 125°C)

Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C		9.5	10	10.5	V
Line regulation	Degline	1	$T_{\rm c} = 25^{\circ}$ C	12.5 V ≤ V _{IN} ≤ 25 V	(\subset)	80	230	mV
Line regulation	Reg·line	1	T _j = 25°C	13 V ≤ V _{IN} ≤ 25 V	$\langle \mathcal{E} \rangle$	30	170	mv
Load regulation	Poglad	1	T _i = 25°C	1.0 mA ≤ I _{OUT} ≤ 100 mA	ΖĄ	18	90	mV
	Reg·load	1	1j = 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	Y	8.5	45	
Output voltage	Vout	1	T _i = 25°C	$12.5 V \le V_{IN} \le 25 V$, $1.0 \text{ mA} \le I_{OUT} \le 40 \text{ mA}$	9.3	_	10.7	v
			,	1.0 mA ≤ I _{OUT} ≤ 70 mA	9.3	(10.7	
Quiescent current	1-	1	T _j = 25°C		_	3.2	6,5	mA
	Ι _Β	I	T _j = 125°C		- 2	5-7	6.0	mA
Quiescent current change	Δl _B	1	T _i = 25°C	13 V ≤ V _{IN} ≤ 25 V		246	1.5	mA
Quescent current change	ΔIB	I	1 _j = 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	X	Y)	0.1	IIIA
Output noise voltage	V _{NO}	2	Ta = 25°C	, 10 Hz ≤ f ≤ 100 kHz		> 70	_	μV _{rms}
Long term stability	ΔV _{OUT} /Δt	1			$\langle \mathcal{A} \rangle$	22	_	mV/kh
Ripple rejection ratio	R.R.	3	f = 120 Hz 13 V ≤ VIN	≤ 24 V, Tj = 25°C	36	43	_	dB
Dropout voltage	VD	1	τ _j = 25°C		_	1.7	_	V
Average temperature coefficient of output voltage	T _{CVO}		1 _{OUT} = 5 n	nA	_	-0.9	_	mV/°C

TA78L12F Electrical Characteristics (Unless otherwise specified, V_{IN} = 19 V, I_{OUT} = 40 mA, C_{IN} = 0.33 μ F, C_{OUT} = 0.1 μ F, 0°C \leq T_j \leq 125°C)

Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C		11.4	12	12.6	V
Line regulation	Reg·line	1	T _i = 25°C	14.5 V ≤ V _{IN} ≤ 27 V	(\square)	120	250	mV
	Regime		1j = 25 C	16 V ≤ V _{IN} ≤ 27 V	<i>A</i>	100	200	IIIV
Load regulation	Reg·load	1	T _i = 25°C	1.0 mA ≤ I _{OUT} ≤ 100 mA	ΖΑ	20	100	mV
Loau regulation	Regillau		1 _J = 25 C	$1.0 \text{ mA} \le I_{OUT} \le 40 \text{ mA}$	Y	10	50	111V
Output voltage	Vout	1	T _i = 25°C	$14.5 V \le V_{IN} \le 27 V$, $1.0 \text{ mA} \le I_{OUT} \le 40 \text{ mA}$	11.16	Ι	12.84	v
			5	1.0 mA ≤ I _{OUT} ≤ 70 mA	11.16	(12.84	
Quiescent current	1-	1	T _j = 25°C		_	3.2	6,5	mA
	IB		T _j = 125°C		- 2	5-/	6.0	IIIA
Quiescent current change	ΔI _B	1	T _i = 25°C	16 V ≤ V _{IN} ≤ 27 V		246	1.5	mA
Quescent current change	ΔIB		1 _J = 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	X	Y	0.1	IIIA
Output noise voltage	V _{NO}	2	Ta = 25°C	, 10 Hz ≤ f ≤ 100 kHz		> 80	-	μV _{rms}
Long term stability	ΔV _{OUT} /Δt	1	\sim		77)	24	-	mV/kh
Ripple rejection ratio	R.R.	3	f = 120 Hz 15 V ≤ VIN	≤ 25 V, T _j = 25°C	36	41	_	dB
Dropout voltage	VD	1	τ _j = 25°C		_	1.7	_	V
Average temperature coefficient of output voltage	T _{CVO}		1 _{OUT} = 5 n	nA		-1.0	_	mV/°C

TA78L15F Electrical Characteristics (Unless otherwise specified, V_{IN} = 23 V, I_{OUT} = 40 mA, C_{IN} = 0.33 μ F, C_{OUT} = 0.1 μ F, 0°C \leq T_j \leq 125°C)

Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C		14.25	15	15.75	V
Line regulation	Degline	1	$T_{\rm c} = 25^{\circ}$ C	17.5 V ≤ V _{IN} ≤ 30 V	(\square)	130	300	mV
Line regulation	Reg·line	I	T _j = 25°C	20 V ≤ V _{IN} ≤ 30 V	R	110	250	mv
Load regulation	Declard	1	T _i = 25°C	1.0 mA ≤ I _{OUT} ≤ 100 mA	ΖΑ	25	150	mV
	Reg·load	1	1j = 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	Y	12	75	IIIV
Output voltage	Vout	1	T _i = 25°C	$17.5 V \le V_{IN} \le 30 V$, $1.0 \text{ mA} \le I_{OUT} \le 40 \text{ mA}$	13.95	_	16.05	v
			,	1.0 mA ≤ I _{OUT} ≤ 70 mA	13.95		16.05	
Quiescent current	1-	1	T _j = 25°C		_	3.3	6,5	mA
	Ι _Β	I	T _j = 125°C		- 2	5-\	6.0	IIIA
Quiescent current change	Δl _B	1	T _i = 25°C	20 V ≤ V _{IN} ≤ 30 V		276	1.5	mA
Quescent current change	ΔIB	I	1 _j = 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	X	Y	0.1	IIIA
Output noise voltage	V _{NO}	2	Ta = 25°C	, 10 Hz ≤ f ≤ 100 kHz		> 90	_	μV _{rms}
Long term stability	ΔV _{OUT} /Δt	1			~_)	30	_	mV/kh
Ripple rejection ratio	R.R.	3	f = 120 Hz 18.5 V ≤ V	$V_{\rm IN} \le 28.5 \text{ V}, \text{ T}_{\rm j} = 25^{\circ} \text{C}$	34	40	_	dB
Dropout voltage	VD	1	τ _j = 25°C		_	1.7	_	V
Average temperature coefficient of output voltage	T _{CVO}		1 _{OUT} = 5 n	nA		-1.3		mV/°C

TA78L18F Electrical Characteristics (Unless otherwise specified, V_{IN} = 27 V, I_{OUT} = 40 mA, C_{IN} = 0.33 μ F, C_{OUT} = 0.1 μ F, 0°C \leq T_j \leq 125°C)

Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C		17.1	18	18.9	V
Line regulation	Reg·line	1	T _i = 25°C	21.4 V ≤ V _{IN} ≤ 33 V	(\square)	32	325	mV
	Regulite	1	1j = 25 C	22 V ≤ V _{IN} ≤ 33 V	J.	27	275	
Load regulation	Reg·load	1	T _i = 25°C	1.0 mA ≤ I _{OUT} ≤ 100 mA	ΖΑ	30	170	mV
Loau regulation	Regillau	,	1 _j = 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	Y	15	75	
Output voltage	Vout	1	T _i = 25°C	21.4 V \leq V _{IN} \leq 33 V, 1.0 mA \leq I _{OUT} \leq 40 mA	16.74	Ι	19.26	v
			,	1.0 mA ≤ I _{OUT} ≤ 70 mA	16.74		19.26	
Quiescent current	IB	1	T _j = 25°C		_	3.3	6,5	mA
	чВ	,	T _j = 125°C		-2	$\leq \sim$	> 6.0	
Quiescent current change	ΔI _B	1	T _i = 25°C	22 V ≤ V _{IN} ≤ 33 V		246	1.5	mA
Quescent current change	ΔIB	,	1 _j = 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	A	Y	0.1	
Output noise voltage	V _{NO}	2	Ta = 25°C	, 10 Hz ≤ f ≤ 100 kHz		150	_	μV _{rms}
Long term stability	ΔV _{OUT} /Δt	1	\sim		77)	45	_	mV/kh
Ripple rejection ratio	R.R.	3	f = 120 Hz 23 V ≤ VIN	s, ≰≤ 33 V, Tj = 25°C	32	38	_	dB
Dropout voltage	VD	1	τ _j = 25°C		_	1.7	_	V
Average temperature coefficient of output voltage	T _{CVO}		1 _{OUT} = 5 n	nA		-1.5		mV/°C

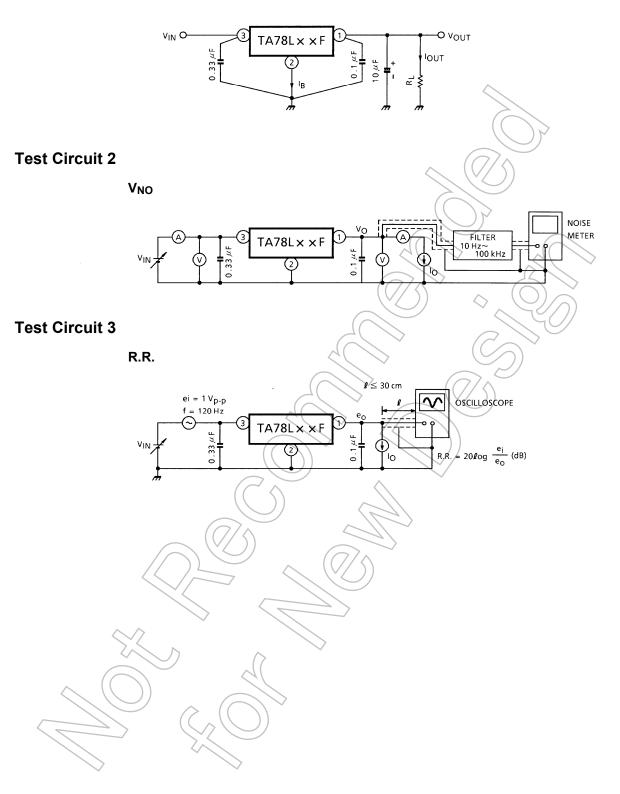
TA78L20F Electrical Characteristics (Unless otherwise specified, V_{IN} = 29 V, I_{OUT} = 40 mA, C_{IN} = 0.33 μ F, C_{OUT} = 0.1 μ F, 0°C \leq T_j \leq 125°C)

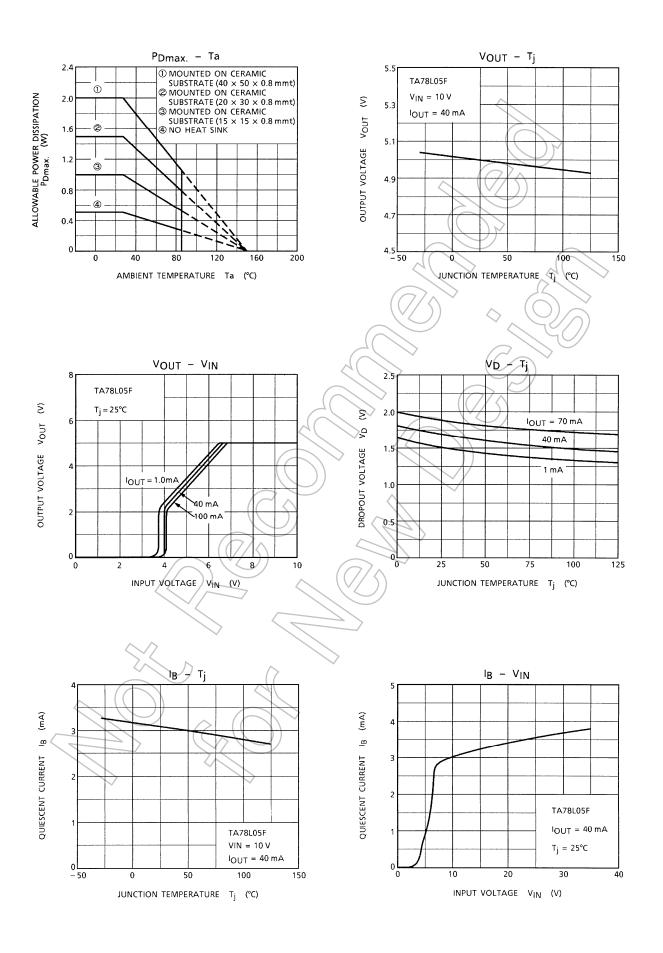
Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C		19.0	20	21.0	V
Line regulation	Bogulino	1	T _i = 25°C	23.5 V ≤ V _{IN} ≤ 35 V	(\square)	33	330	mV
Line regulation	Reg·line	I	$1_{j} = 25 C$	24 V ≤ V _{IN} ≤ 35 V	<i>A</i>	28	285	mv
Load regulation	Poglad	1	T _i = 25°C	1.0 mA ≤ I _{OUT} ≤ 100 mA	ΖΑ	33	180	mV
Load regulation	Reg·load	1	1j = 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	Y	17	90	
Output voltage	Vout	1	T _i = 25°C	23.5 V \leq V _{IN} \leq 35 V, 1.0 mA \leq I _{OUT} \leq 40 mA	18.6	_	21.4	v
			,	1.0 mA ≤ I _{OUT} ≤ 70 mA	18.6		21.4	
Quiescent current	1-	1	T _j = 25°C		_	3.3	6,5	mA
	IB	I	T _j = 125°C		- 2	5-7	6.0	mA
Quieseent eurrent change	A1-	1	T _i = 25°C	24 V ≤ V _{IN} ≤ 35 V		246	1.5	mA
Quiescent current change	ΔI _B	1	1j = 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	\mathcal{A}	Y.	0.1	mA
Output noise voltage	V _{NO}	2	Ta = 25°C	, 10 Hz ≤ f ≤ 100 kHz		170	_	μV _{rms}
Long term stability	$\Delta V_{OUT}/\Delta t$	1	\sim		77)	49	_	mV/kh
Ripple rejection ratio	R.R.	3	f = 120 Hz 25 V ≤ VIN	, ≤ 35 V, Tj = 25°C	31	37	_	dB
Dropout voltage	VD	1	τ _j = 25°C		_	1.7	_	V
Average temperature coefficient of output voltage	T _{CVO}		1 _{OUT} = 5 n	nA		-1.7		mV/°C

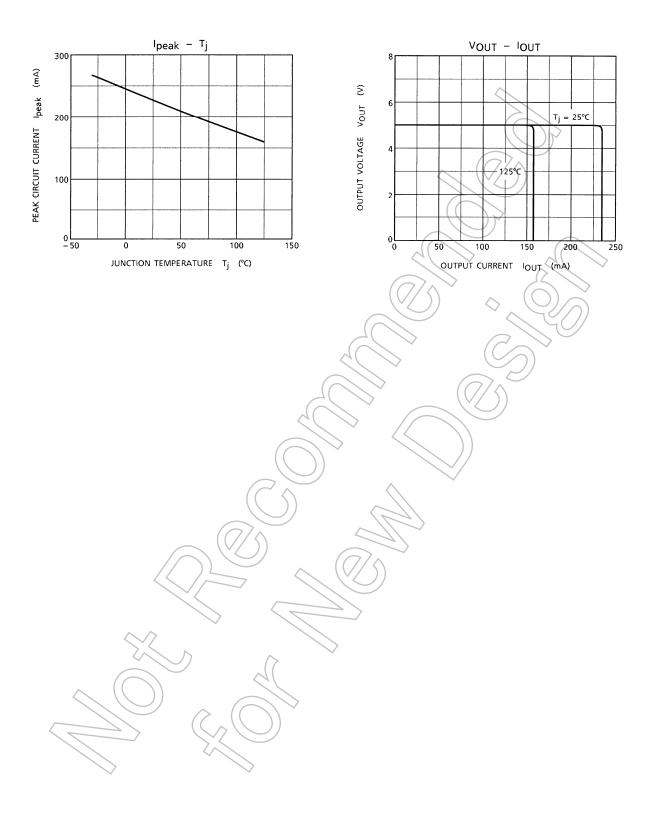
TA78L24F Electrical Characteristics (Unless otherwise specified, V_{IN} = 33 V, I_{OUT} = 40 mA, C_{IN} = 0.33 μ F, C_{OUT} = 0.1 μ F, 0°C \leq T_j \leq 125°C)

Characteristics	Symbol	Test Circuit	Test Condition		Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C		22.8	24	25.2	V
Line regulation	Regiline	1	T _j = 25°C	27.5 V ≤ V _{IN} ≤ 38 V	$(\square$	35	350	mV
				28 V ≤ V _{IN} ≤ 38 V	\mathcal{A}	30	300	
Load regulation	Reg·load	1	T _j = 25°C	1.0 mA ≤ I _{OUT} ≤ 100 mA	74	40	200	mV
				1.0 mA ≤ I _{OUT} ≤ 40 mA	Y	20	100	
Output voltage	VOUT	1	T _j = 25°C	$27.5 V \le V_{IN} \le 38 V$, 1.0 mA $\le I_{OUT} \le 40 \text{ mA}$	22.32	_	25.68	V
				1.0 mA ≤ I _{OUT} ≤ 70 mA	22.32		25.68	
Quiescent current	Ι _Β	1	T _j = 25°C		_	3.5	6,5	mA
			Tj = 125°C		-6	6-/	> 6.0	
Quiescent current change	ΔI _B	1	T _j = 25°C	28 V ≤ V _{IN} ≤ 38 V		276	1.5	- mA
				1.0 mA ≤ I _{OUT} ≤ 40 mA	\mathcal{A}	Ge)	0.1	
Output noise voltage	V _{NO}	2	Ta = 25°C	, 10 Hz ≤ f ≤ 100 kHz		200	_	μV _{rms}
Long term stability	$\Delta V_{OUT}/\Delta t$	1			77)	56	_	mV/kh
Ripple rejection ratio	R.R.	3	f = 120 Hz, 29 V $\leq V_{\text{IN}} \leq 39 \text{ V}, \text{ T}_{\text{j}} = 25^{\circ}\text{C}$		31	35	_	dB
Dropout voltage	VD	1 <	T _j = 25°0		_	1.7		V
Average temperature coefficient of output voltage	T _{CVO}		lout = 5 mA		_	-2.0	_	mV/°C

Test Circuit 1 / Standard Application



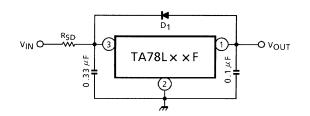




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Usage Precautions

Destruction of the IC may occur if high voltage in excess of the IC output voltage (typ. value) is applied to the IC output terminal. Where this possibility exists, connect a Zener diode between the output terminal and GND to prevent any application of excessive voltage.



If $V_{\mbox{IN}}$ is too high, always connect $R_{\mbox{SD}}$ in order to reduce power consumption of IC.

• Low voltage

Do not apply voltage to the Product that is lower than the minimum operating voltage, or the Product's protective functions will not operate properly and the Product may be permanently damaged.

Overcurrent Protection

The overcurrent protection circuits in the Product are designed to temporarily protect Product from minor overcurrent of brief duration. When the overcurrent protective function in the Product activates, immediately cease application of overcurrent to Product. Improper usage of Product, such as application of current to Product exceeding the absolute maximum ratings, could cause the overcurrent protection circuit not to operate properly and/or damage Product permanently even before the protection circuit starts to operate.

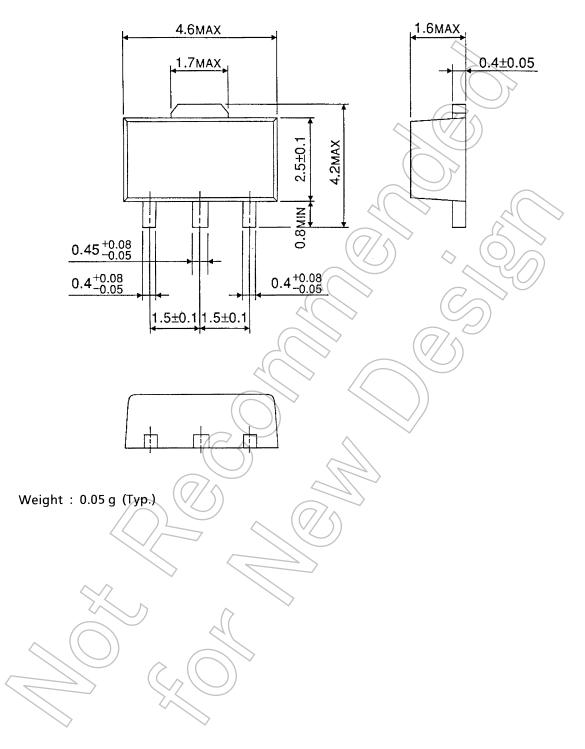
Overheating Protection

The thermal shutdown circuits in the Product are designed to temporarily protect Product from minor overheating of brief duration. When the overheating protective function in the Product activates, immediately correct the overheating situation. Improper usage of Product, such as the application of heat to Product exceeding the absolute maximum ratings, could cause the overheating protection circuit not to operate properly and/or damage Product permanently even before the protection circuit starts to operate.

Package Dimensions

HSOP3-P-1.50

Unit : mm



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