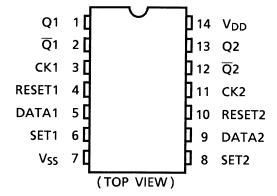
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC4013BP, TC4013BF

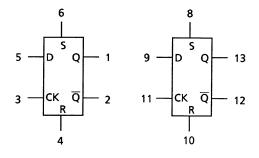
TC4013B Dual D-Type Flip Flop

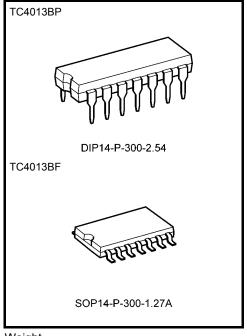
TC4013B contains two independent circuits of D type flip-flop. The input level applied to DATA input are transferred to Q and \overline{Q} output by rising edge of the clock pulse. When SET input is placed at "H", and RESET input is placed at "L", outputs become Q = "H", and \overline{Q} = "L". When RESET input is placed at "H", and SET input is placed at "H", and SET input is placed at "L", outputs become Q = "L", and \overline{Q} = "H". When both of RESET input and SET input are at "H", outputs become Q = "H" and \overline{Q} = "H".

Pin Assignment



Block Diagram





Weight

DIP14-P-300-2.54 : 0.96 g (typ.) SOP14-P-300-1.27A : 0.18 g (typ.)

Truth Table

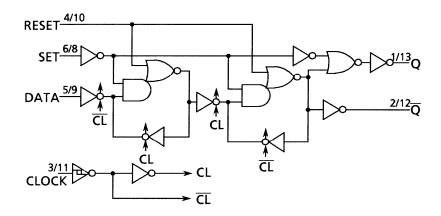
	Inp	Outputs			
RESET	SET	DATA	СКД	Qn + 1	
L	Н	*	*	Н	L
Н	L	*	*	L	Н
Н	Н	*	*	Н	Н
L	L	L		L	Н
L	L	Н		Н	L
L	L	*		Qn [·]	Qn ·

*: Don't care

Δ: Level change

·: No change

Logic Diagram



Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
DC supply voltage	V_{DD}	V_{SS} – 0.5 to V_{SS} + 20	V
Input voltage	V _{IN}	$V_{SS} - 0.5$ to $V_{DD} + 0.5$	V
Output voltage	V _{OUT}	V_{SS} – 0.5 to V_{DD} + 0.5	V
DC input current	I _{IN}	±10	mA
Power dissipation	PD	300 (DIP)/180 (SOP)	mW
Operating temperature range	T _{opr}	−40 to 85	°C
Storage temperature range	T _{stg}	−65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (V_{SS} = 0 V) (Note)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	V_{DD}	_	3	_	18	V
Input voltage	V _{IN}	_	0		V _{DD}	V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{DD} or V_{SS} .

Static Electrical Characteristics (V_{SS} = 0 V)

		Sym-	Test Condition		-40°C		25°C			85°C		
Charac	eteristics	bol		V _{DD} (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit
High-level voltage	loutput	V _{OH}	$ I_{OUT} < 1 \mu A$ $V_{IN} = V_{SS}, V_{DD}$	5 10 15	4.95 9.95 14.95		4.95 9.95 14.95	5.00 10.00 15.00		4.95 9.95 14.95		V
Low-level voltage	output	V _{OL}	$ I_{OUT} < 1 \mu A$ $V_{IN} = V_{SS}, V_{DD}$	5 10 15	_ _ _	0.05 0.05 0.05	_ _ _	0.00 0.00 0.00	0.05 0.05 0.05	_ _ _	0.05 0.05 0.05	V
Output hig	gh current	Іон	$V_{OH} = 4.6 \text{ V}$ $V_{OH} = 2.5 \text{ V}$ $V_{OH} = 9.5 \text{ V}$ $V_{OH} = 13.5 \text{ V}$ $V_{IN} = V_{SS}, V_{DD}$	5 5 10 15	-0.61 -2.50 -1.50 -4.00		-0.51 -2.10 -1.30 -3.40	-1.0 -4.0 -2.2 -9.0		-0.42 -1.70 -1.10 -2.80	1 1 1 1	mA
Output lov	v current	I _{OL}	$V_{OL} = 0.4 \text{ V}$ $V_{OL} = 0.5 \text{ V}$ $V_{OL} = 1.5 \text{ V}$ $V_{IN} = V_{SS}, V_{DD}$	5 10 15	0.61 1.50 4.00	_ _ _	0.51 1.30 3.40	1.2 3.2 12.0	_ _ _	0.42 1.10 2.80	_ _ _	mA
Input high	voltage	V _{IH}	$V_{OUT} = 0.5 \text{ V}, 4.5 \text{ V} \\ V_{OUT} = 1.0 \text{ V}, 9.0 \text{ V} \\ V_{OUT} = 1.5 \text{ V}, 13.5 \text{ V} \\ I_{OUT} < 1 \mu\text{A}$	5 10 15	3.5 7.0 11.0	_ _ _	3.5 7.0 11.0	2.75 5.50 8.25	_ _ _	3.50 7.00 11.00	_ _ _	V
Input low	voltage	V _{IL}	$V_{OUT} = 0.5 \text{ V}, 4.5 \text{ V}$ $V_{OUT} = 1.0 \text{ V}, 9.0 \text{ V}$ $V_{OUT} = 1.5 \text{ V}, 13.5 \text{ V}$ $ I_{OUT} < 1 \mu\text{A}$	5 10 15	_ _ _	1.5 3.0 4.0	_ _ _	2.25 4.50 6.75	1.5 3.0 4.0	_ _ _	1.5 3.0 4.0	V
Input current	"H" level	l _{IH}	V _{IH} = 18 V V _{IL} = 0 V	18 18	_ _	0.1 -0.1	_ _	10 ⁻⁵	0.1 -0.1	_ _	1.0 -1.0	μА
Quiescent current	t supply	I _{DD}	V _{IN} = V _{SS} , V _{DD} (Note)	5 10 15	— — —	1 2 4	— — —	0.002 0.004 0.008	1 2 4	_ _ _	30 60 120	μА

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Note: All valid input combinations.

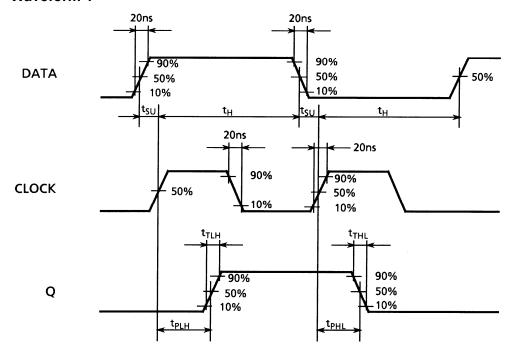
Dynamic Electrical Characteristics (Ta = 25°C, V_{SS} = 0 V, C_L = 50 pF)

		Test Condition					
Characteristics	Symbol		V _{DD} (V)	Min	Тур.	Max	Unit
0.1.11.11			5	_	70	200	
Output transition time	t _{TLH}	_	10	_	35	100	ns
(low to high)			15	_	30	80	
			5	_	70	200	
Output transition time	t _{THL}	_	10	_	35	100	ns
(high to low)			15	_	30	80	
			5	_	130	300	
Propagation delay time	t _{pLH}	_	10	_	65	130	ns
(CK-Q, \overline{Q})	t _{pHL}		15	_	50	90	
			5	_	110	300	
Propagation delay time	t _{pLH}	_	10	_	50	130	ns
(SET, RESET-Q, \overline{Q})			15	_	40	90	
			5	_	110	300	
Propagation delay time	t _{pHL}	_	10	_	50	130	ns
(SET, RESET-Q, \overline{Q})			15	_	40	90	
	fcL	_	5	3.5	8	_	
Max clock frequency			10	8.0	16	_	MHz
			15	12.0	20	_	
		_	5	No limit			μS
Max clock input rise time	t _{rCL}		10				
Max clock input fall time	t _{fCL}		15				
			5	_	60	180	
Min pulse width	t _W	_	10	_	30	80	ns
(SET, RESET)			15	_	25	50	
			5	_	60	140	
Min clock pulse width	t _W	_	10	_	30	60	ns
			15	_	25	40	
			5	_	_	40	
Min set-up time	t _{su}	_	10	_	_	20	ns
(DATA-CK)			15	_	_	15	
			5	_	20	40	
Min hold time	t _H	_	10	_	10	20	ns
(DATA-CK)			15	_	6	15	
			5	_	_	40	
Min removal time	t _{rem}	_	10	_	_	20	ns
(SET, RESET-CK)			15	_	_	15	
Input capacitance	C _{IN}	_		_	5	7.5	pF

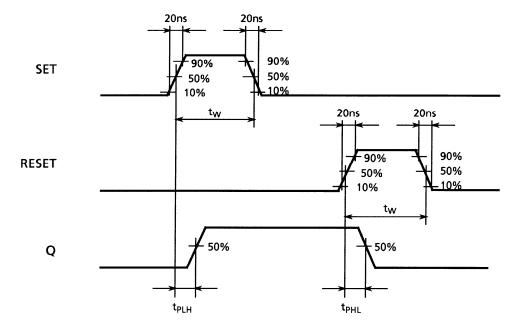
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Waveform for Measurement of Dynamic Characteristics

Waveform 1



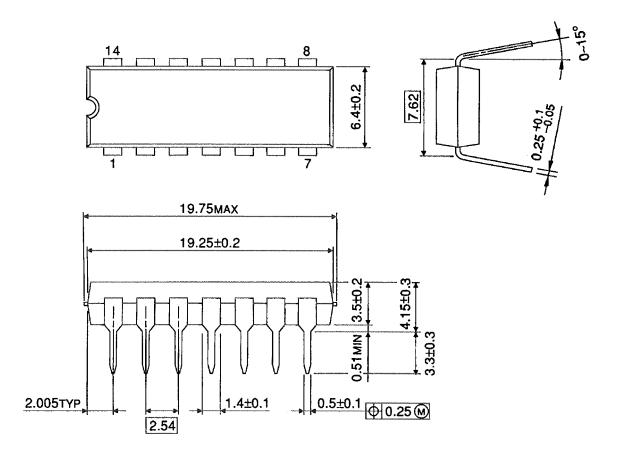
Waveform 2



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Package Dimensions

DIP14-P-300-2.54 Unit: mm

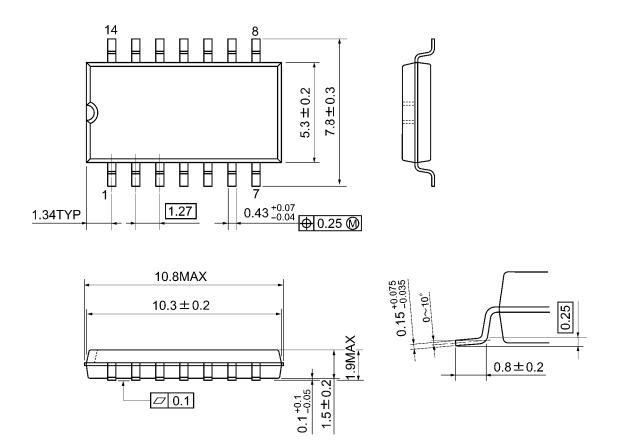


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Weight: 0.96 g (typ.)

Package Dimensions

SOP14-P-300-1.27A Unit: mm



Weight: 0.18 g (typ.)

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