TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TC4S584F

SCHMITT TRIGGER

TC4S584F is the one circuit inverter having the schmitt trigger function at the input terminal.

That is, since the circuit threshold level voltage at the leading and trailing edges of input waveform are different (Vp, V_N), the TC4S584F can be used in the broad range application, including line receiver, waveform shaping circuit, astable multivibrator, etc. In addition to ordinary inverter.

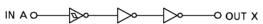
SSOP5-P-0.95

Weight: 0.016g (Typ.)

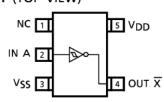
ABSOLUTE MAXIMUM RATINGS

CHARACTERISTIC	SYMBOL	RATING	UNIT
DC Supply Voltage	V_{DD}	Vss - 0.5~Vss + 20	V
Input Voltage	VIN	$V_{SS} - 0.5 \sim V_{DD} + 0.5$	V
Output Voltage	Vout	$V_{SS} - 0.5 \sim V_{DD} + 0.5$	٧
DC Input Current	IN	± 10	mA
Power Dissipation	PD	200	mW
Operating Temperature Range	T _{opr}	- 40~85	°C
Storage Temperature Range	T _{stg}	-65~150	°C
Lead Temperature (10s)	TL	260	°C

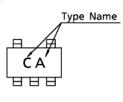
LOGIC DIAGRAM



PIN ASSIGNMENT (TOP VIEW)



MARKING



OPERATING RANGES ($V_{SS} = 0V$)

CHARACTERISTIC	SYMBOL		MIN.	TYP.	MAX.	UNIT
DC Supply Voltage	V_{DD}	_	3		18	٧
Input Voltage	VIN	1	0		V_{DD}	V

STATIC ELECTRICAL CHARACTERISTICS ($V_{SS} = 0V$)

CHARACTERISTIC		SYM-	TEST CONDITION	VDD	– 40°C		25°C			85°C		UNIT
CHARAC	TENISTIC	BOL	1231 CONDITION	V _{DD}	MIN.	MAX.	MIN.	TYP.	MAX.	MIN.	MAX.	OIVIII
High-Leve	ı		llour / 1A	5	4.95	_	4.95	5.00	_	4.95	_	
Output Vo		۷он	I _{OUT} <1μΑ	10	9.95	_	9.95	10.00	_	9.95	_	
Output ve	ortage		$V_{IN} = V_{SS}, V_{DD}$	15	14.95	_	14.95	15.00	-	14.95	-	v
Low-Level	ı		 au= <14	5		0.05	_	0.00	0.05	—	0.05	Ů
Output Vo		VOL	$ I_{OUT} < 1\mu A$	10	_	0.05	—	0.00	0.05	—	0.05	1
Output vi	ortage		$V_{IN} = V_{SS}, V_{DD}$	15	_	0.05	_	0.00	0.05	—	0.05	
			V _{OH} = 4.6V	5	- 0.61		- 0.51	- 1.0		- 0.42		
Output Hi	iah		V _{OH} = 2.5V	5	- 2.5	_	- 2.1	- 4.0	_	- 1.7	_	
Output Hi Current	ign	ЮН	V _{OH} = 9.5V	10	- 1.5	_	- 1.3	- 2.2	_	- 1.1	_	
Current			V _{OH} = 13.5V	15	- 4.0	_	- 3.4	- 9.0	_	- 2.8	_	
			$V_{IN} = V_{SS}, V_{DD}$]								4
			V _{OL} = 0.4V	5	0.61	_	0.51	1.5		0.42	-	mA
Output Lo	ow		$V_{OL} = 0.5V$	10	1.5	_	1.3	3.8	_	1.1	_	
Current		lOL	V _{OL} = 1.5V	15	4.0	_	3.4	15.0	_	2.8	_	
			$V_{IN} = V_{SS}, V_{DD}$	1								
Decitive T	riaasr		V _{OUT} = 0.5V	5	1.95	3.65	2.05	2.9	3.35	2.05	3.75	
Positive T		VΡ	V _{OUT} = 1.0V	10	4.3	7.1	4.5	5.9	7.1	4.7	7.2	
Threshold	voitage*		V _{OUT} = 1.5V	15	6.9	10.7	7.1	9.0	10.6	7.1	10.8	
Negative '	Triagar		V _{OUT} = 4.5V	5	1.05	2.75	1.1	2.1	2.6	0.95	2.65	
Threshold		V_{N}	V _{OUT} = 9.0V	10	2.1	4.9	2.2	3.5	4.7	2.0	4.8	V
Threshold	voitage*		V _{OUT} = 13.5V	15	3.2	7.0	3.3	5.0	6.8	3.1	6.9	
				5	0.1	1.35	0.4	0.75	1.3	0.4	1.50	
Hystersis \	Hystersis Voltage* V	VΗ	_	10	1.7	3.2	1.8	2.4	3.2	1.7	3.4	
				15	3.1	4.8	3.2		4.8	3.2	4.9	
Input	H Level	ΊΗ	V _{IH} = 18V	18	_	0.1	_	10-5	0.1	_	1.0	
Current	L Level	կլ	V _{IL} = 0V	18		- 0.1	1	- 10 ^{- 5}	- 0.1	_	- 1.0	μ A
Quiescent				5	_	1	_	0.001	1	_	7.5	
Device Cu		IDD	$V_{IN} = V_{SS}$, V_{DD}	10	—	2	_	0.002	2	—	15	μ A
Device Cu	ment			15	_	4	_	0.004	4	—	30	

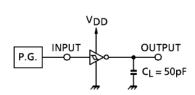
(Note) Values are different to TC4584BP, TC4584BF marked* (Vp, V_N , V_H).

DYNAMIC ELECTRICAL CHARACTERISTICS (Ta:	$= 25^{\circ}$ C, $V_{SS} = 0$ V, $C_{I} = 5$	(7q0
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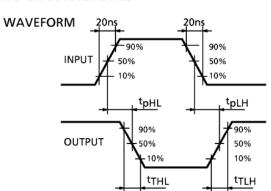
CHARACTERISTIC	SYMBOL	TEST CONDITION	V _{DD} (V)	MIN.	TYP.	MAX.	UNIT
Output Transition Time (Low to High)	tTLH	_	5 10 15	_ _ _	80 50 40	200 100 80	
Output Transition Time (High to Low)	tтнь	_	5 10 15	_ _ _	80 50 40	200 100 80	ns
Propagation Delay Time	t _{pLH} t _{pHL}	_	5 10 15	_ _ _	170 80 60	340 160 120	ns
Input Capacitance	CIN	_	_	5	7.5	pF	

CIRCUIT AND WAVEFORM FOR MEASUREMENT OF DYNAMIC CHARACTERISTICS

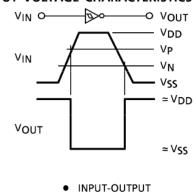
CIRCUIT



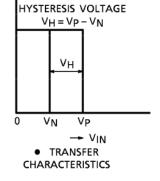
P.G.: PULSE GENERATOR



INPUT-OUTPUT VOLTAGE CHARACTERISTICS



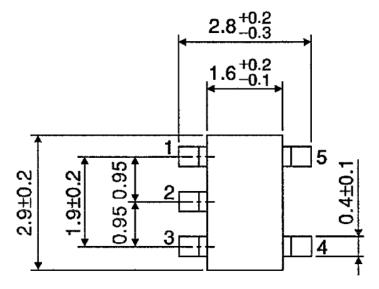
VOLTAGE WAVEFORM

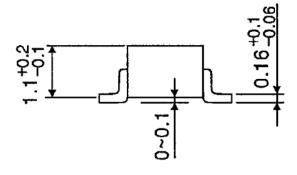


PACKAGE DIMENSIONS

SSOP5-P-0.95

Unit: mm





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Weight: 0.016g (Typ.)

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