TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# T C 4 S 5 8 4 F

### SCHMITT TRIGGER

TC4S584F is the one circuit inverter having the schmitt trigger function at the input terminal. That is, since the circuit threshold level voltage at the leading and trailing edges of input waveform are different (Vp, V<sub>N</sub>), the TC4S584F can be used in the broad range application, including line receiver, waveform shaping circuit, astable multivibrator, etc. In addition to ordinary inverter.

#### ABSOLUTE MAXIMUM RATINGS

CHARACTERISTIC	SYMBOL	RATING	UNIT
DC Supply Voltage	VDD	V <sub>SS</sub> - 0.5~V <sub>SS</sub> + 20	V
Input Voltage	VIN	$V_{SS} - 0.5 \sim V_{DD} + 0.5$	V
Output Voltage	Vout	$V_{SS} - 0.5 \sim V_{DD} + 0.5$	V
DC Input Current	IIN	± 10	mA
Power Dissipation	PD	200	mW
Operating Temperature Range	T <sub>opr</sub>	- 40~85	°C
Storage Temperature Range	T <sub>stg</sub>	- 65~150	°C
Lead Temperature (10s)	TI	260	°C



#### SSOP5-P-0.95

Weight : 0.016g (Typ.)

#### LOGIC DIAGRAM





MARKING



# **TOSHIBA**

#### OPERATING RANGES (V<sub>SS</sub> = 0V)

CHARACTERISTIC	SYMBOL		MIN.	TYP.	MAX.	UNIT
DC Supply Voltage	V <sub>DD</sub>	—	3	_	18	V
Input Voltage	VIN	—	0	—	V <sub>DD</sub>	V

## **STATIC ELECTRICAL CHARACTERISTICS** $(V_{SS} = 0V)$

CHARACTERISTIC		SYM-	TEST CONDITION	Vnn	– 40°C		25°C			85°C		UNIT
		BOL		(V)	MIN.	MAX.	MIN.	TYP.	MAX.	MIN.	MAX.	
	1			5	4.95	_	4.95	5.00	—	4.95	—	
Output Voltage	∨он	$V_{IN} = V_{SS}, V_{DD}$	10	9.95	—	9.95	10.00		9.95	—		
Output voltage			15	14.95	—	14.95	15.00	_	14.95	_		
		VOL	1	5	_	0.05	_	0.00	0.05	—	0.05	v
Low-Level	altana		$ V_{\rm IN} = V_{\rm SS}, V_{\rm DD}$	10	—	0.05	—	0.00	0.05	_	0.05	
Output v	bitage			15	—	0.05	—	0.00	0.05	_	0.05	
			V <sub>OH</sub> = 4.6V	5	- 0.61	-	- 0.51	- 1.0	—	- 0.42	_	
			V <sub>OH</sub> = 2.5V	5	- 2.5	—	- 2.1	- 4.0		– 1.7	—	
Output H	ign	ЮН	V <sub>OH</sub> = 9.5V	10	- 1.5	—	- 1.3	- 2.2		– 1.1	—	
Current			V <sub>OH</sub> = 13.5V	15	- 4.0	—	- 3.4	- 9.0		- 2.8	—	
			$V_{IN} = V_{SS}, V_{DD}$	1								
		lol	$V_{OL} = 0.4V$	5	0.61	_	0.51	1.5	_	0.42	_	mA
Output Lo	w		$V_{OL} = 0.5V$	10	1.5	_	1.3	3.8	—	1.1	—	
Current			$V_{OL} = 1.5V$	15	4.0	—	3.4	15.0		2.8	—	
			$V_{IN} = V_{SS}, V_{DD}$	1								
Desitive T		VP	V <sub>OUT</sub> = 0.5V	5	1.95	3.65	2.05	2.9	3.35	2.05	3.75	
Positive I	rigger		$V_{OUT} = 1.0V$	10	4.3	7.1	4.5	5.9	7.1	4.7	7.2	
Threshold	voltage*		V <sub>OUT</sub> = 1.5V	15	6.9	10.7	7.1	9.0	10.6	7.1	10.8	
Needing	<b>T</b>		V <sub>OUT</sub> = 4.5V	5	1.05	2.75	1.1	2.1	2.6	0.95	2.65	
Negative	i rigger	VN	V <sub>OUT</sub> = 9.0V	10	2.1	4.9	2.2	3.5	4.7	2.0	4.8	v
Threshold	voltage*		VOUT = 13.5V	15	3.2	7.0	3.3	5.0	6.8	3.1	6.9	
Hystersis Voltage*	VH		5	0.1	1.35	0.4	0.75	1.3	0.4	1.50		
		_	10	1.7	3.2	1.8	2.4	3.2	1.7	3.4		
			15	3.1	4.8	3.2	4.0	4.8	3.2	4.9		
Input	H Level	ЧΗ	V <sub>IH</sub> = 18V	18	_	0.1	_	10-5	0.1	_	1.0	
Current	L Level	ΙL	V <sub>IL</sub> = 0V	18	_	- 0.1	_	- 10 <sup>-5</sup>	-0.1	_	- 1.0	μA
Quieccent				5	—	1	—	0.001	1	—	7.5	
Dovice Cu	rrant	IDD	$V_{IN} = V_{SS}, V_{DD}$	10	—	2	—	0.002	2	_	15	$\mu A$
Device Current				15	—	4	—	0.004	4	—	30	

(Note) Values are different to TC4584BP, TC4584BF marked\* (Vp, VN, VH).

#### **DYNAMIC ELECTRICAL CHARACTERISTICS** (Ta = $25^{\circ}$ C, V<sub>SS</sub> = 0V, C<sub>L</sub> = 50pF)

CHARACTERISTIC	SYMBOL	TEST CONDITION	V <sub>DD</sub> (V)	MIN.	TYP.	MAX.	UNIT
Output Transition Time (Low to High)	Ϯτιн	_	5 10 15		80 50 40	200 100 80	
Output Transition Time (High to Low)	тнг	_	5 10 15		80 50 40	200 100 80	ns
Propagation Delay Time	t <sub>pLH</sub> t <sub>pHL</sub>	_	5 10 15	-	170 80 60	340 160 120	ns
Input Capacitance	C <sub>IN</sub>	—	—	5	7.5	рF	

#### CIRCUIT AND WAVEFORM FOR MEASUREMENT OF DYNAMIC CHARACTERISTICS

CIRCUIT





#### INPUT-OUTPUT VOLTAGE CHARACTERISTICS



• INPUT-OUTPUT VOLTAGE WAVEFORM



# TOSHIBA

### PACKAGE DIMENSIONS

SSOP5-P-0.95

Unit : mm





Weight : 0.016g (Typ.)

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