TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74AC138P, TC74AC138F, TC74AC138FT

3-to-8 Line Decoder

The TC74AC138 is an advanced high speed CMOS 3-to-8 LINE DECODER fabricated with silicon gate and double-layer metal wiring C^2MOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

When the device is enabled, 3 Binary Select inputs (A, B and C) determine which one of the outputs $(\overline{Y}0 - \overline{Y}7)$ will go low.

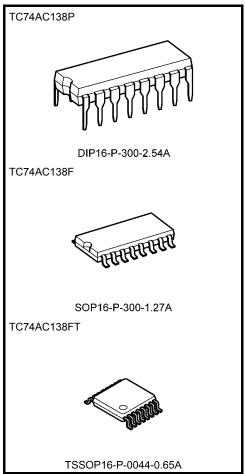
When enable input G1 is held low or either $\overline{G}2A$ or $\overline{G}2B$ is held high, decoding function is inhibited and all outputs go high.

 $\overline{G1}$, $\overline{G2}A$, and $\overline{G2}B$ inputs are provided to ease cascade connection and for use as an address decoder for memory systems

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

Features

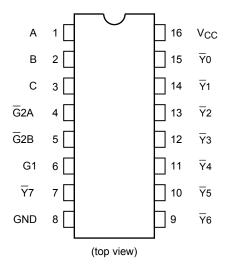
- High speed: $t_{pd} = 5.9 \text{ ns (typ.)}$ at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 8 \mu A \text{ (max)}$ at $T_a = 25 \text{°C}$
- High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min)
- Symmetrical output impedance: $|I_{OH}| = I_{OL} = 24$ mA (min) Capability of driving 50 Ω transmission lines.
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: VCC (opr) = 2 to 5.5 V
- Pin and function compatible with 74F138



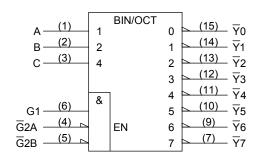
Weight

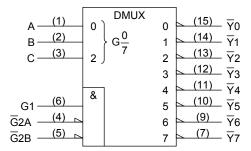
DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.) TSSOP16-P-0044-0.65A : 0.06 g (typ.)

Pin Assignment



IEC Logic Symbol



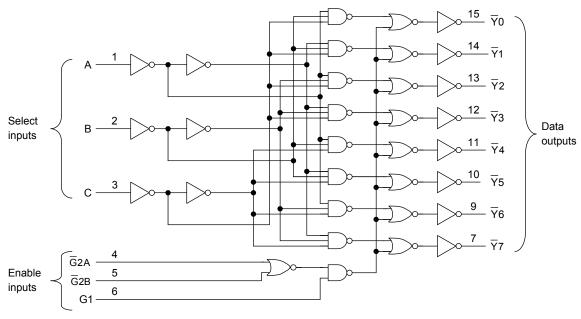


Truth Table

Inputs					Outputs									
Enable			Select		_ Y0	<u>\text{Y}1</u>	<u></u>	- - - -	- ¥4	<u>7</u> 5	- Y6	7 7	Selected Output	
G1	G ₂ A	G2B	С	В	Α	10	1 1	12	13	14	13	10	1 /	
L	Х	Х	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н	None
Х	Н	X	Х	Х	Х	Н	Н	Н	Η	Н	Н	Η	Η	None
Х	Х	Н	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н	None
Н	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	₹0
Н	L	L	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	Y 1
Н	L	L	L	Н	L	Н	Н	L	Н	Н	Н	Н	Н	₹2
Н	L	L	L	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	Y 3
Н	L	L	Н	L	L	Н	Н	Н	Н	L	Н	Н	Н	Y 4
Н	L	L	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н	₹5
Н	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	L	Н	₹6
Н	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	₹7

X: Don't care

Logic Diagram



Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	−0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	−0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}	±20	mA
Output diode current	lok	±50	mA
DC output current	lout	±50	mA
DC V _{CC} /ground current	Icc	±200	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP/TSSOP)	mW
Storage temperature	T _{stg}	−65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = −40 to 65°C. From Ta = 65 to 85°C a derating factor of −10 mW/°C should be applied up to 300 mW.

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	2.0 to 5.5	V	
Input voltage	V _{IN}	0 to V _{CC}	V	
Output voltage	Vout	0 to V _{CC}	V	
Operating temperature	T _{opr}	-40 to 85	°C	
Input rise and fall time	dt/dV	0 to 100 (V _{CC} = 3.3 ± 0.3 V)	ns/V	
input rise and fail time	ai/av	0 to 20 (V _{CC} = 5 ± 0.5 V)	IIS/V	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

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Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition				Ta = 25°C		Ta = −40 to 85°C		Unit	
Sharastonistics	Cymbol				V _{CC} (V)	Min	Тур.	Max	Min	Max	
			_		2.0	1.5	_	_	1.5	_	
High-level input voltage	V _{IH}				3.0	2.10	_	_	2.10	_	V
					5.5	3.85	_	_	3.85	_	
		_			2.0	_	_	0.50	_	0.50	
Low-level input voltage	V_{IL}			3.0	_	_	0.90	_	0.90	V	
					5.5	_	_	1.65	_	1.65	
	V _{OH}		I _{OH} = -50 μA		2.0	1.9	2.0	_	1.9	_	
		V _{IN} = V _{IH} or V _{IL}			3.0	2.9	3.0	_	2.9	_	
High-level output					4.5	4.4	4.5	_	4.4	_	V
voltage	∙ОП		$I_{OH} = -4 \text{ mA}$		3.0	2.58	_	_	2.48	_	v
			I_{OH} = -24 mA		4.5	3.94	_	_	3.80	_	
			I_{OH} = -75 mA	(Note)	5.5	_	_	_	3.85	_	
		VIN = VIH or VIL	I _{OL} = 50 μA		2.0	_	0.0	0.1	_	0.1	
					3.0	_	0.0	0.1	_	0.1	
Low-level output	V_{OL}				4.5	_	0.0	0.1	_	0.1	V
voltage	VOL		I_{OL} = 12 mA		3.0	_	_	0.36	_	0.44	V
			I_{OL} = 24 mA		4.5	_	_	0.36	_	0.44	
			I _{OL} = 75 mA	(Note)	5.5	_	_	_	_	1.65	
Input leakage current	I _{IN}	V _{IN} = V _C	_C or GND		5.5	_	_	±0.1	_	±1.0	μΑ
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND			5.5	_	_	8.0	_	80.0	μΑ

Note: This spec indicates the capability of driving 50 Ω transmission lines.

One output should be tested at a time for a 10 ms maximum duration.



AC Characteristics (C_L = 50 pF, R_L = 500 Ω , input: t_r = t_f = 3 ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = −40 to 85°C		Unit
	-,		V _{CC} (V)	Min	Тур.	Max	Min	Max	
Propagation delay time $(A,B,C\text{-}\overline{Y})$	t _{pLH} t _{pHL}	_	3.3 ± 0.3 5.0 ± 0.5	_ _	8.5 6.4	14.2 9.2	1.0 1.0	16.3 10.5	ns
Propagation delay time $(G1-\overline{Y}\)$	t _{pLH}	_	3.3 ± 0.3 5.0 ± 0.5	_	7.5 6.1	12.8 8.9	1.0 1.0	14.7 10.2	ns
Propagation delay time	t _{pLH}	_	3.3 ± 0.3 5.0 ± 0.5		8.8 7.2	15.0 10.5	1.0 1.0	17.3 12.0	ns
Input capacitance	C _{IN}	_		_	5	10	_	10	pF
Power dissipation capacitance	C _{PD}		(Note)	_	143	_	_	_	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

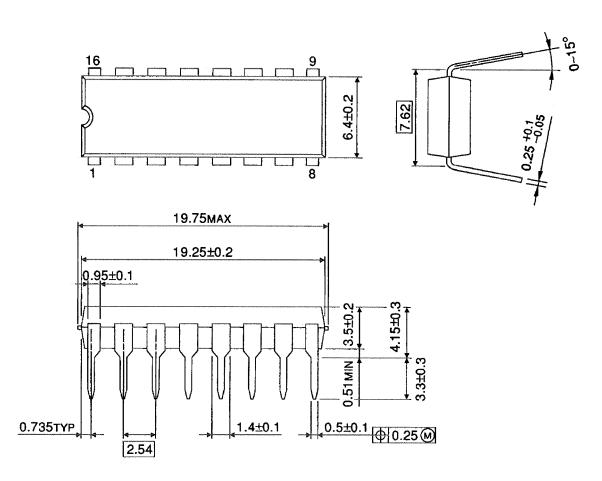
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Average operating current can be obtained by the equation:

$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

Package Dimensions

DIP16-P-300-2.54A Unit: mm

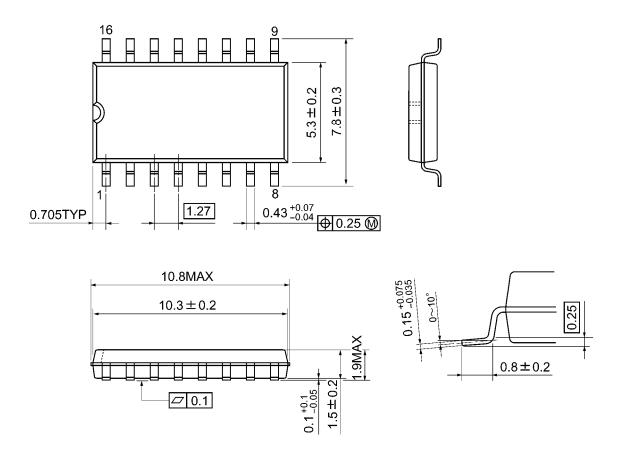


Weight: 1.00 g (typ.)



Package Dimensions

SOP16-P-300-1.27A Unit: mm

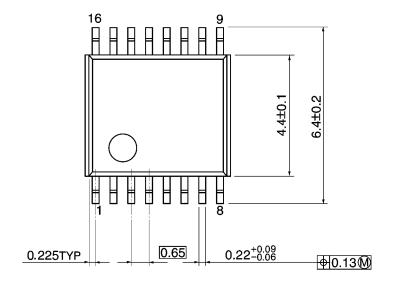


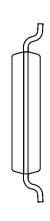
Weight: 0.18 g (typ.)

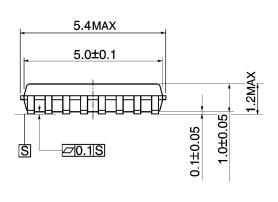
Package Dimensions

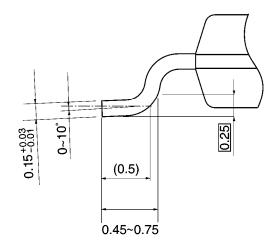
TSSOP16-P-0044-0.65A

Unit: mm









Weight: 0.06 g (typ.)

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