TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# **TC74HC139AP, TC74HC139AF**

#### Dual 2-to-4 Line Decoder

The TC74HC139A is a high speed CMOS 2-to-4 LINE DECODER/DEMULTIPLEXER fabricated with silicon gate  $\rm C^2MOS$  technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

The active low enable input can be used for gating or it can be used as a data input for demultiplexing applications.

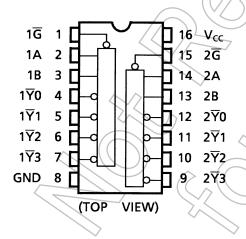
When the enable input is held "H", all four outputs are fixed at a high logic level independent of the other inputs.

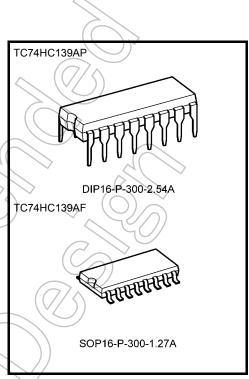
All inputs are equipped with protection circuits against static discharge or transient excess voltage.

#### **Features**

- High speed:  $t_{pd} = 16 \text{ ns (typ.)}$  at  $V_{CC} = 5 \text{ V}$
- Low power dissipation:  $I_{CC} = 4 \mu A$  (max) at  $T_a = 25$ °C
- High noise immunity: V<sub>NIH</sub> = V<sub>NIL</sub> = 28% V<sub>CC</sub> (min)
- Output drive capability: 10 LSTTL loads
- Symmetrical output impedance: | IOH | = IOL = 4 mA (min)
- Balanced propagation delays:  $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: VCC (opr) = 2 to 6 V
- Pin and function compatible with 74LS139

### Pin Assignment

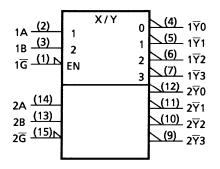


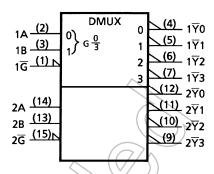


Weight

DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.)

#### **IEC Logic Symbol**





#### **Truth Table**

lı	nputs			Out				
Enable	le Select		<u></u>	<u>-</u>	_ Y2	<del>-</del> 73	Selected Output	
G	В	Α	10	T I	12	13	. (	
Н	Х	Х	Н	Н	Н	Н	None	
L	L	L	L	Н	Н	Н	<u>Y</u> 0	
L	L	Н	Н	L	Н	Н	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	
L	Н	L	Н	Н	L	Н	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	
L	Н	Н	Н	Н	Н	L (	<del>7</del> 3	

X: Don't care

### Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	Vcc	-0.5 to 7	V
DC input voltage	7/ (V <sub>IN</sub>	-0.5 to V <sub>CC</sub> + 0.5	V
DC output voltage	Vout .	-0.5 to V <sub>CC</sub> + 0.5	V
Input diode current	⊃ l <sub>IK</sub>	±20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V <sub>CC</sub> /ground current	Icc	±50	mA
Power dissipation	Pp	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T <sub>stg</sub>	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of  $Ta = -40^{\circ}C$  to 65°C. From  $Ta = 65^{\circ}C$  to 85°C a derating factor of -10 mW/°C shall be applied until 300 mW.

### **Operating Ranges (Note)**

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	2 to 6	V
Input voltage	V <sub>IN</sub>	0 to V <sub>CC</sub>	V
Output voltage	V <sub>OUT</sub>	0 to V <sub>CC</sub>	⟨v
Operating temperature	T <sub>opr</sub>	-40 to 85	°C
		0 to 1000 (V <sub>CC</sub> = 2.0 V)	
Input rise and fall time	t <sub>r</sub> , t <sub>f</sub>	0 to 500 (V <sub>CC</sub> = 4.5 V)	ns
		0 to 400 (V <sub>CC</sub> = 6.0 V)	())

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{CC}$  or GND.

#### **Electrical Characteristics**

#### **DC Characteristics**

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta 40 to	Unit	
			$\mathcal{A}(\mathcal{C})$	VCC (V)	Min	Тур. (	Max	Min	Max	
				2.0	1.50			1.50	_	
High-level input voltage	$V_{IH}$		- 4	4.5	3.15		) —	3.15	_	V
ŭ				6.0	4.20	$\mathcal{N}$	/ _	4.20		
				2.0	_ \	//-	0.50	_	0.50	
Low-level input voltage	$V_{IL}$	((		4.5	1	//-	1.35	_	1.35	V
				6.0		_	1.80	_	1.80	
	V <sub>OH</sub>	((~ <		2.0	1.9	2.0	_	1.9	_	
		V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = -20 μA	4.5	4.4	4.5	_	4.4	_	
High-level output voltage			<	6.0	5.9	6.0	_	5.9	_	V
			$I_{OH} = -4 \text{ mA}$	4.5	4.18	4.31	_	4.13	_	
		$\supset$	$I_{OH} = -5.2 \text{ mA}$	6.0	5.68	5.80	_	5.63	_	
		_		2.0	_	0.0	0.1	_	0.1	
	VoL	· · ·	I <sub>OL</sub> = 20 μA	4.5	_	0.0	0.1	_	0.1	
Low-level output voltage		V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>		6.0	_	0.0	0.1	_	0.1	V
		$\wedge$	$I_{OL} = 4 \text{ mA}$	4.5	_	0.17	0.26	_	0.33	
		4	$I_{OL} = 5.2 \text{ mA}$	6.0	_	0.18	0.26	_	0.33	
Input leakage current	)) I <sub>IN</sub>	V <sub>IN</sub> = V <sub>CC</sub> or	GND	6.0	_		±0.1		±1.0	μΑ
Quiescent supply current	Icc	$V_{IN} = V_{CC}$ or	GND	6.0	_	_	4.0	_	40.0	μΑ

## AC Characteristics (CL = 15 pF, $V_{CC}$ = 5 V, Ta = 25°C, input: $t_r$ = $t_f$ = 6 ns)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Output transition time	t <sub>TLH</sub>			4	8	ns
Output transition time	$t_{THL}$	_	_	•		113
Propagation delay time	t <sub>pLH</sub>	<		12	22	20
$(A, B-\overline{Y})$	$t_{pHL}$	_		12	22	ns
Propagation delay time	t <sub>pLH</sub>			Mo	10	20
$(\overline{G} - \overline{Y})$	t <sub>pHL</sub>	_		10	18	ns

### AC Characteristics ( $C_L = 50 \text{ pF}$ , input: $t_r = t_f = 6 \text{ ns}$ )

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
			V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	
Output transition time	t <sub>TLH</sub> t <sub>THL</sub>	П	2.0 4.5 6.0		30 8 7	75 15 13		95 19 16	ns
Propagation delay time (A, B- $\overline{Y}$ )	t <sub>pLH</sub> t <sub>pHL</sub>	-	2.0 4.5 6.0	_ _ _	45 15 13	130 26 22	) 	165 33 28	ns
Propagation delay time	t <sub>pLH</sub>		2.0 4.5 6.0	_	39 13 11	110 22 19		140 28 24	ns
Input capacitance	C <sub>IN</sub>	$\bigcirc$		/	) 5	10	_	10	pF
Power dissipation capacitance	C <sub>PD</sub> (Note)		$\wedge$		46	_	_	_	pF

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

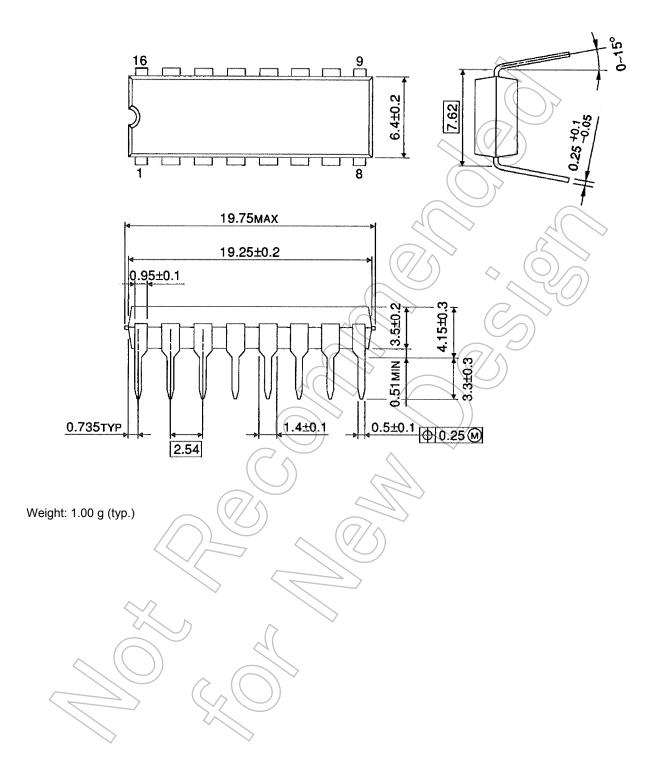
Average operating current can be obtained by the equation:

 $I_{CC}$  (opr) =  $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$  (per decoder)



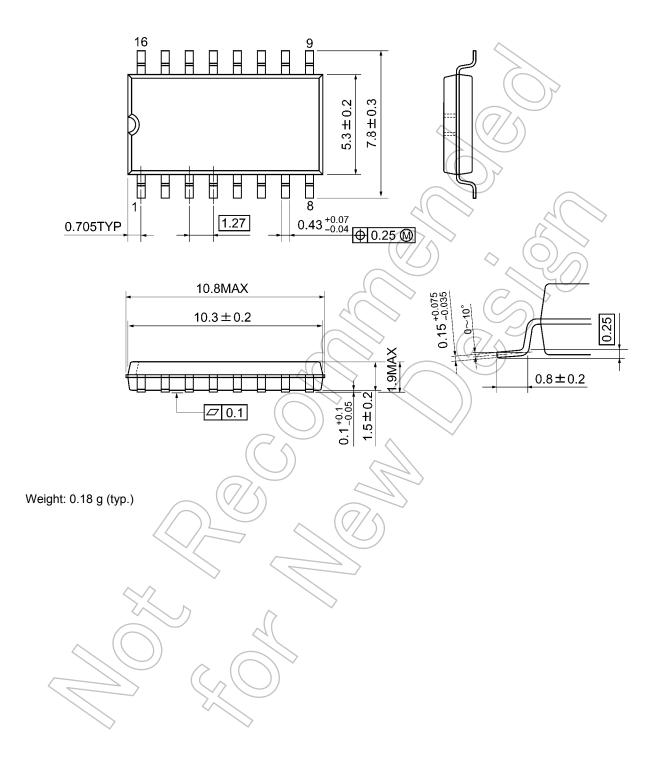
### **Package Dimensions**

DIP16-P-300-2.54A Unit: mm



### **Package Dimensions**

SOP16-P-300-1.27A Unit: mm



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