TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74HC4028AP, TC74HC4028AF

#### BCD-to-Decimal Decoder

The TC74HC4028A is a high speed CMOS BCD-to-DECIMAL DECODER fabricated with silicon gate  $\mathrm{C}^2\mathrm{MOS}$  technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

A BCD code applied to the four inputs (A-D) sets a high level at one of ten decoded outputs. A illegal BCD code such as eleven thru fifteen sets all outputs low. This device can be used as 3-to-8 LINE DECODER when input D is held high.

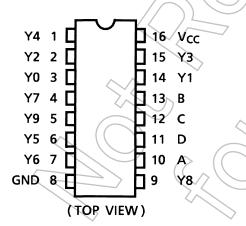
This device is useful for code conversion, address decoding, memory selection, multiplexing, or readout decoding.

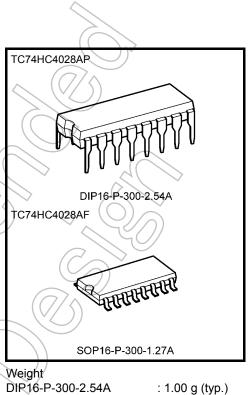
All inputs are equipped with protection circuits against static discharge or transient excess voltage.

#### **Features**

- High speed:  $t_{pd} = 18$  ns (typ.) at V<sub>CC</sub> = 5 V •
- Low power dissipation:  $I_{CC} = 4 \mu A (max)$  at  $Ta = 25^{\circ}C$ •
- High noise immunity: V<sub>NIH</sub> = V<sub>NIL</sub> = 28% V<sub>CC</sub> (min)
- Output drive capability: 10 LSTTL loads •
- Symmetrical output impedance:  $|I_{OH}| = I_{OL} = 4 \text{ mA} (\text{min})$
- Balanced propagation delays:  $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range:  $V_{CC}$  (opr) = 2 to 6 V
- Pin and function compatible with 4028B.

### **Pin Assignment**





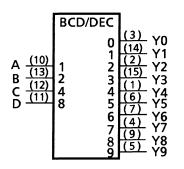
SOP16-P-300-1.27A

: 0.18 g (typ.)

Start of commercial production 1988-11

# TOSHIBA

## **IEC Logic Symbol**

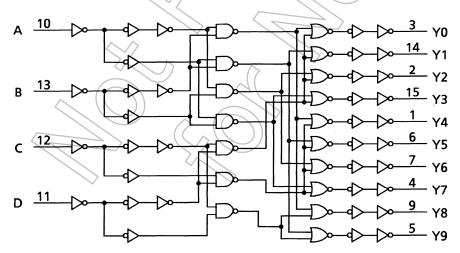


## **Truth Table**

											$\sum D$			
	Inputs				Outputs Selected									
D	С	В	А	Y0	Y1	Y2	Y3	Y4	Y5 <	Y6	¥7	Y8	¥9	Output
L	L	L	L	Н	L	L	L	L	F		L	L	14	Y0
L	L	L	Н	L	Н	L	L	L	Y	))	L	> r (	2/	) Y1
L	L	Н	L	L	L	Н	L	4	/	L	L	4	L.	Y2
L	L	Н	Н	L	L	L	Н	Ĩ	7	L	L (		7	Y3
L	Н	L	L	L	L	L	L (	H	Y	L	L	$\sim D$	L	Y4
L	Н	L	Н	L	L	L	4	F.	н	L	(4)	⟨_L	L	Y5
L	Н	Н	L	L	L	L		L	L	Ŧ	Ľ.	Лг	L	Y6
L	Н	Н	Н	L	L	L (	Ą	∕_L	$\mathcal{A}$	L	н	L	L	Y7
Н	L	L	L	L	L	$(\mathbf{f})$	1	L	7	ľ	))L	Н	L	Y8
Н	L	L	Н	L	L	L.	ノ	L	L	$\sim$	L	L	Н	Y9
Н	Х	Н	Х	L		4	L	L	$\langle \mathbf{r} \rangle$	L	L	L	L	None
Н	Н	Х	Х	L	1	Ŋ	L	L	4	L	L	L	L	None

X: Don't care

### System Diagram



#### Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V <sub>CC</sub>	–0.5 to 7	V
DC input voltage	V <sub>IN</sub>	–0.5 to V <sub>CC</sub> + 0.5	V
DC output voltage	V <sub>OUT</sub>	-0.5 to V <sub>CC</sub> + 0.5	V
Input diode current	IIK	±20	mA
Output diode current	I <sub>OK</sub>	±20	(mA)
DC output current	IOUT	±25	mA
DC V <sub>CC</sub> /ground current	ICC	±50	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T <sub>stg</sub>	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C shall be applied until 300 mW.

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	2 to 6	V
Input voltage	∕ ∕V <sub>IN</sub>	0 to V <sub>CC</sub>	V
Output voltage	VOUT	0 to V <sub>CC</sub>	V
Operating temperature	Topr	-40 to 85	°C
		0 to 1000 (V <sub>CC</sub> = 2.0 V)	
Input rise and fall time	t <sub>r</sub> , t <sub>f</sub>	0 to 500 (V <sub>CC</sub> = 4.5 V)	ns
		0 to 400 (V <sub>CC</sub> = 6.0 V)	

#### **Operating Ranges (Note)**

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{CC}$  or GND.

### **Electrical Characteristics**

#### **DC Characteristics**

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta –40 to	Unit	
			$V_{CC}(V)$	Min	Тур.	Max	Min	Max		
		_		2.0	1.50	_ <	X	1.50	_	
High-level input voltage	VIH			4.5	3.15	—	$\langle \rangle$	3.15	—	V
Ĵ				6.0	4.20		Æ	4.20		
		_		2.0	—	10	0.50	_	0.50	v
Low-level input voltage	VIL			4.5		L + ( / (	1)35	— 1.3	1.35	
-				6.0		$\Delta$	1.80	—	1.80	
		V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = -20 μA	2.0	1.9	2.0	>	1.9	—	
				4.5	4.4	4.5	—	- 4.4	_	V
High-level output voltage	V <sub>OH</sub>			6.0 <	5.9	6.0	- <	5.9	$\rightarrow$	
Ū.			I <sub>OH</sub> = -4 mA	4.5	4.18	4.31	(	4.13	> -	
			$I_{OH} = -5.2 \text{ mA}$	6.0	5.68	5.80	_((	5.63	<u> </u>	
				2.0	2	0.0	0.1	Y)	0.1	
			I <sub>OL</sub> = 20 μA	4.5	—	0.0	0.1	$\geq$	0.1	
Low-level output voltage	V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	40	6.0	—	0.0	0.1)	—	0.1	V
Ũ			$I_{OL} = 4 \text{ mA}$	4.5	_	0.17	0.26	_	0.33	
			I <sub>OL</sub> = 5.2 mA	6.0	_	0,18	0.26	—	0.33	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = V <sub>CC</sub> or	GND	6.0		$\overline{)}$	±0.1	—	±1.0	μΑ
Quiescent supply current	ICC	V <sub>IN</sub> = V <sub>CC</sub> or	GND	6.0	$\nearrow$	/	4.0	—	40.0	μΑ

# AC Characteristics (C<sub>L</sub> = 15 pF, V<sub>CC</sub> = 5 V, Ta = 25°C, input: $t_r = t_f = 6$ ns)

Characteristics	Sýmbol	Test Condition	Min	Тур.	Max	Unit
Output transition time	ттін ттін	- (	_	4	8	ns
Propagation delay time	t <sub>pLH</sub> t <sub>pHL</sub>		_	18	34	ns

### AC Characteristics (C<sub>L</sub> = 50 pF, input: $t_r = t_f = 6 \text{ ns}$ )

Characteristics	Symbol	Test Condition		-	Ta = 25°0	2	Ta –40 to	Unit	
	-,		$V_{CC}(V)$	Min	Тур.	Max	Min	Max	
	<b>4</b>		2.0		30	75		95	
Output transition time	t <sub>TLH</sub>	—	4.5	—	8	15	—	19	ns
	tthl		6.0	—	7	13 —	—	16	
Propagation delay	<b>4</b>		2.0		80	180	2	225	
time	t <sub>pLH</sub>	—	4.5	_	22	36 —	45	ns	
(A, B, C, D-Y)	t <sub>pHL</sub>		6.0	_	18	31	_	38	
Input capacitance	C <sub>IN</sub>			_	5	10		10	pF
Power dissipation capacitance	C <sub>PD</sub> (Note)	_		_(	44	> —	_	_	pF

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

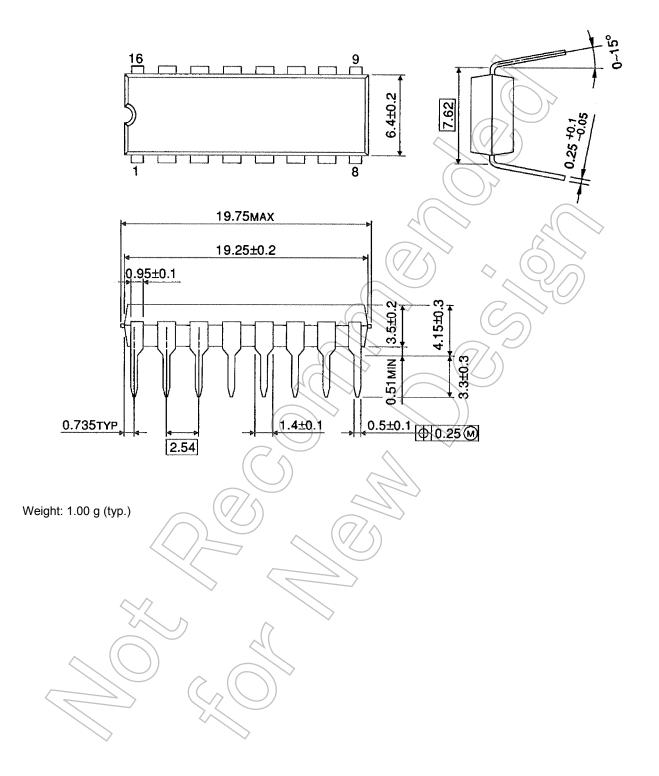
Average operating current can be obtained by the equation:

 $I_{CC}$  (opr) =  $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 

#### **Package Dimensions**

DIP16-P-300-2.54A

Unit : mm

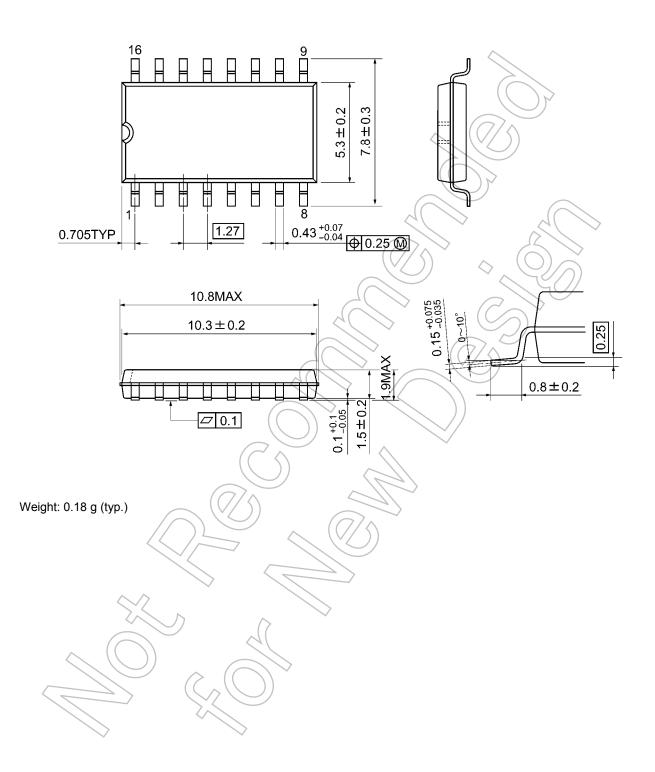




#### **Package Dimensions**

SOP16-P-300-1.27A

Unit: mm



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