TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74HCT245AP,TC74HCT245AF

Octal Bus Transceiver (3-state)

The TC74HCT245A is high speed CMOS OCTAL BUS TRANSCEIVER fabricated with silicon gate C²MOS technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

Its inputs are compatible with TTL, NMOS, and CMOS output voltage levels.

It is intended for two-way asynchronous communication between data busses. The direction of data transmission is determined by the level of the DIR input.

The enable input (\overline{G}) can be used to disable the device so that the busses are effectively isolated.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

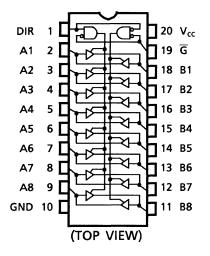
Features (Note 1) (Note 2)

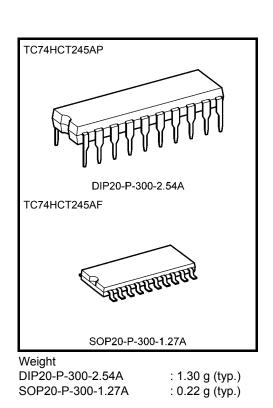
- High speed: $t_{pd} = 10 \text{ ns}$ (typ.) at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 4 \mu A \pmod{at Ta} = 25^{\circ}C$
- Compatible with TTL outputs: $V_{IL} = 0.8 \text{ V} (\text{max})$ $V_{IH} = 2.0 \text{ V} (\text{min})$
 - Wide interfacing ability: LSTTL, NMOS, CMOS
- Output drive capability: 15 LSTTL loads
- Symmetrical output impedance: $|I_{OH}| = I_{OL} = 6 \text{ mA} (\text{min})$
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Pin and function compatible with 74LS245

Note 1: Do not apply a signal to any bus terminal when it is the output mode. Damage may result.

Note 2: All floating (high impedance) bus terminals must have their input levels fixed by means of pull up or down resistors.

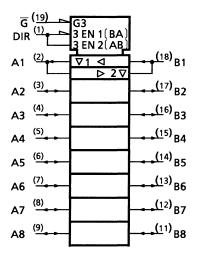
Pin Assignment





TOSHIBA

IEC Logic Symbol



Truth Table

Inputs		Fun	Output			
G	DIR	A Bus	B Bus	Output		
L	L	Output	Input	A = B		
L	Н	Input	Output	B = A		
Н	Х	2	Z			

X: "H" or "L"

Z: High impedance

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	–0.5 to 7	V
DC input voltage	VIN	-0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	IIК	±20	mA
Output diode current	I _{OK}	±20	mA
DC output current	IOUT	±35	mA
DC V _{CC} /ground current	ICC	±75	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C shall be applied until 300 mW.

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	4.5 to 5.5	V
Input voltage	VIN	0 to V _{CC}	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	t _r , t _f	0 to 500	ns

Note: The operating ranges are required to ensure the normal operation of the device. Unused inputs and bus inputs must be tied to either VCC or GND. Please connect both bus inputs and the bus outputs with VCC or GND when the I/O of the bus terminal changes by the function. In this case, please note that the output is not short-circuited.

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition			Ta = 25°C		Ta = -40 to 85°C		Unit	
Characteristics	Symbol			$V_{CC}(V)$	Min	Тур.	Max	Min	Max	Unit
High-level input voltage	V _{IH}	—		4.5 to 5.5	2.0	_	_	2.0	_	V
Low-level input voltage	V _{IL}	—		4.5 to 5.5	_	_	0.8	_	0.8	V
High-level output	V _{OH}	V _{IN} = V _{IH} or V _{IL}	$I_{OH} = -20 \ \mu A$	4.5	4.4	4.5	_	4.4	—	V
voltage			I _{OH} =6 mA	4.5	4.18	4.31	_	4.13	—	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH} or V _{IL}	$I_{OL} = 20 \ \mu A$	4.5	_	0.0	0.1	_	0.1	V
			$I_{OL} = 6 \text{ mA}$	4.5	_	0.17	0.26	_	0.33	
3-state output off-state current	I _{OZ}	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $V_{OUT} = V_{CC} \text{ or } GND$		5.5	_		±0.5	_	±5.0	μA
Input leakage current	I _{IN}	$V_{IN} = V_{CC}$ or GND		5.5	_	_	±0.1	_	±1.0	μA
Quiescent supply current	ICC	$V_{IN} = V_{CC}$ or GND		5.5	_	_	4.0	_	40.0	μA
	IC	Per input: V _I Other input:	5.5	_		2.0		2.9	mA	

AC Characteristics (input: $t_r = t_f = 6 \text{ ns}$)

Characteristics	Symbol	Test Condition C _L (pF) V _{CC} (V)		Ta = 25°C			Ta = -40 to 85°C		Unit	
Characteristics	Symbol			$V_{CC}(V)$	Min	Тур.	Max	Min	Max	Unit
Output transition time	t _{TLH}		50	4.5	_	7	12	_	15	ns
Output transition time	t _{THL}			5.5		6	11	—	14	
			50	4.5	_	13	22	_	28	• ns
Propagation delay	t _{pLH}			5.5		11	20	—	25	
time	t _{pHL}		450	4.5		18	30	_	38	
			150	5.5		16	27	—	34	
	t _p ZL R t _p ZH	$R_L = 1 k\Omega$	50	4.5		19	30	_	38	ns
3-state output enable				5.5		16	27	—	34	
time			150	4.5	_	24	38	_	48	
				5.5	_	22	34	—	43	
3-state output disable time	t _{pLZ}		50	4.5	_	17	30	_	38	20
	t _{pHZ}	$R_L = 1 k\Omega$		5.5		16	27	—	34	ns
Input capacitance	CIN	DIR, G				5	10	_	10	pF
Output capacitance	C _{I/O}	An, Bn				13		_		pF
Power dissipation capacitance	C _{PD}	_	_		_	41	_			pF
	(Note)									

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

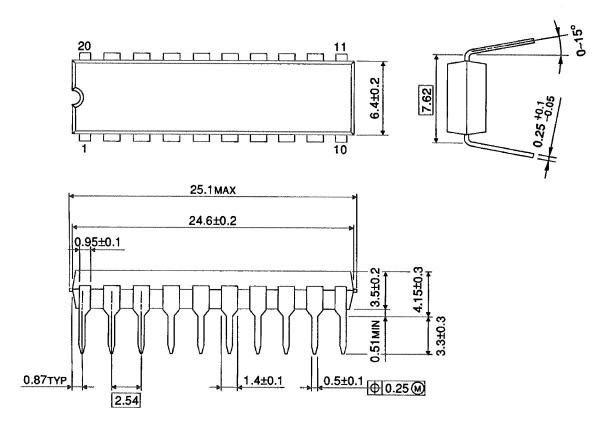
 I_{CC} (opr) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/8$ (per bit)

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Package Dimensions

DIP20-P-300-2.54A

Unit : mm



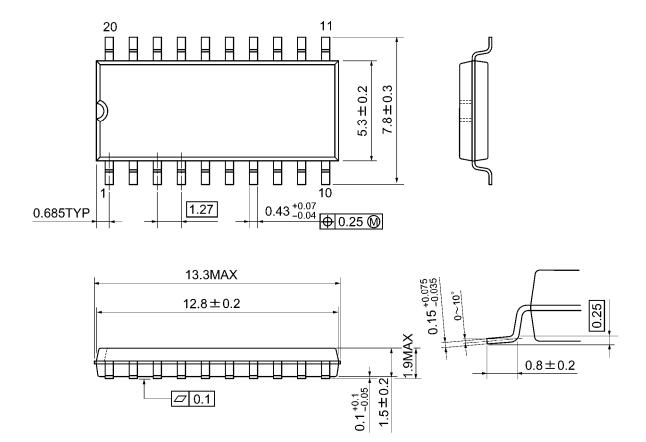
Weight: 1.30 g (typ.)



Package Dimensions

SOP20-P-300-1.27A

Unit: mm



Weight: 0.22 g (typ.)

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