

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74VHC14F, TC74VHC14FK

#### Hex Schmitt Inverter

The TC74VHC14 is an advanced high speed CMOS Schmitt INVERTER fabricated with silicon gate  $\rm C^2MOS$  technology.

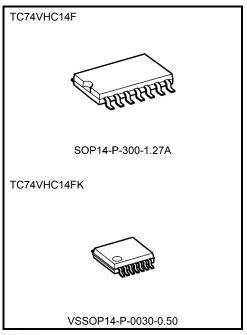
It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

Pin configuration and function are the same as the TC74VHC04 but the inputs have hysteresis and with its schmitt trigger function, the TC74VHC14 can be used as a line receivers which will receive slow input signals.

An input protection circuit ensures that 0 to  $5.5 \, V$  can be applied to the input pins without regard to the supply voltage. This device can be used to interface  $5 \, V$  to  $3 \, V$  systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

#### **Features**

- High speed:  $t_{pd} = 5.5 \text{ ns (typ.)}$  at  $V_{CC} = 5 \text{ V}$
- Low power dissipation:  $ICC = 2 \mu A \text{ (max)}$  at Ta = 25 °C
- Power down protection is provided on all inputs.
- Balanced propagation delays: t<sub>pLH</sub> ≃ t<sub>pHL</sub>
- Wide operating voltage range: VCC (opr) = 2 V to 5.5 V
- Low noise: VOLP = 0.8 V (max)
- Pin and function compatible with 74ALS14



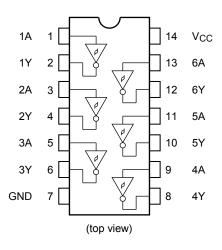
Weight

SOP14-P-300-1.27A : 0.18 g (typ.) VSSOP14-P-0030-0.50 : 0.02 g (typ.)

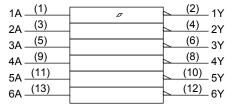
Start of commercial production 1991-05



#### **Pin Assignment**



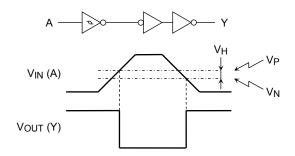
#### **IEC Logic Symbol**



#### **Truth Table**

Α	Υ
L	Н
Н	L

#### System Diagram, Waveform



#### **Absolute Maximum Ratings (Note)**

Characteristics	Symbol	Rating	Unit
Supply voltage range	Vcc	−0.5 to 7.0	V
DC input voltage	V <sub>IN</sub>	−0.5 to 7.0	V
DC output voltage	Vout	-0.5 to V <sub>CC</sub> + 0.5	V
Input diode current	lıK	-20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V <sub>CC</sub> /ground current	Icc	±50	mA
Power dissipation	PD	180	mW
Storage temperature	T <sub>stg</sub>	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



### **Operating Ranges (Note)**

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	2.0 to 5.5	V
Input voltage	V <sub>IN</sub>	0 to 5.5	V
Output voltage	Vout	0 to V <sub>CC</sub>	V
Operating temperature	Topr	−40 to 85	°C

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

#### **Electrical Characteristics**

#### **DC Characteristics**

Characteristics	Test Condition			Ta = 25°C		Ta = −40 to 85°C		Unit		
	- J			Vcc (V)	Min	Тур.	Max	Min	Max	<b>5</b>
				3.0	_	_	2.20	_	2.20	
Positive threshold voltage	VP		_	4.5	_	_	3.15	_	3.15	V
				5.5	_	_	3.85	_	3.85	
Negative threehold				3.0	0.90	_	_	0.90	_	
Negative threshold voltage	VN		_	4.5	1.35	_	_	1.35	_	V
1 11 9 1				5.5	1.65	_	—	1.65	_	
				3.0	0.30	_	1.20	0.30	1.20	
Hysteresis voltage	$V_{H}$	_		4.5	0.40	_	1.40	0.40	1.40	V
				5.5	0.50	_	1.60	0.50	1.60	
	Voн	V <sub>IN</sub> = V <sub>IL</sub>		2.0	1.9	2.0	_	1.9	_	
			$I_{OH}$ = -50 $\mu$ A	3.0	2.9	3.0	_	2.9	_	
High-level output voltage				4.5	4.4	4.5	_	4.4	_	V
voltage			I <sub>OH</sub> = -4 mA	3.0	2.58	_	_	2.48	_	
			$I_{OH} = -8 \text{ mA}$	4.5	3.94	_	_	3.80	_	
				2.0	_	0.0	0.1	_	0.1	
		V <sub>IN</sub> = V <sub>IH</sub>	I <sub>OL</sub> = 50 μA	3.0	_	0.0	0.1	_	0.1	
Low-level output voltage	$V_{OL}$			4.5	_	0.0	0.1	_	0.1	V
voltage			I <sub>OL</sub> = 4 mA	3.0	-	_	0.36	_	0.44	
			$I_{OL} = 8 \text{ mA}$		_	_	0.36	_	0.44	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V or GND		0 to 5.5	_	_	±0.1	_	±1.0	μА
Quiescent supply current	Icc	V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5	_	_	2.0	_	20.0	μА

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#### AC Characteristics (input: $t_r = t_f = 3 \text{ ns}$ )

Characteristics Symbol	Test Condition		Ta = 25°C			Ta = −40 to 85°C		Unit											
		V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min	Тур.	Max	Min	Max	01										
Propagation delay tpLH time tpHL		00.00	15	_	8.3	12.8	1.0	15.0											
		_	$3.3 \pm 0.3$	50	_	10.8	16.3	1.0	18.5	- ns									
			5.0 ± 0.5	15		5.5	8.6	1.0	10.0										
				3.0 ± 0.3	3.0 ± 0.3	3.0 ± 0.5	5.0 ± 0.5	5.0 ± 0.5	3.0 ± 0.5	3.0 ± 0.5	3.0 ± 0.5	3.0 ± 0.3	5.0 ± 0.5	5.0 ± 0.5	3.0 ± 0.3	50	_	7.0	10.6
Input capacitance	C <sub>IN</sub>		_		_	4	10	_	10	pF									
Power dissipation capacitance	CPD			(Note)	-	21	_		_	pF									

Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

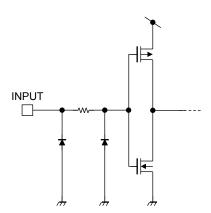
Average operating current can be obtained by the equation:

ICC (opr) = CPD·VCC·fIN + ICC/6 (per gate)

### Noise Characteristics (input: $t_r = t_f = 3$ ns)

Characteristics	Curredo el	Test Condition		Ta = 25°C		l lmi4
Characteristics	Symbol		V <sub>CC</sub> (V)	Тур.	Limit	Unit
Quiet output maximum dynamic V <sub>OL</sub>	VOLP	C <sub>L</sub> = 50 pF	5.0	0.4	0.8	V
Quiet output minimum dynamic V <sub>OL</sub>	Volv	C <sub>L</sub> = 50 pF	5.0	-0.4	-0.8	V
Minimum high level dynamic input voltage	VIHD	C <sub>L</sub> = 50 pF	5.0	_	3.5	V
Maximum low level dynamic input voltage	VILD	C <sub>L</sub> = 50 pF	5.0	_	1.5	V

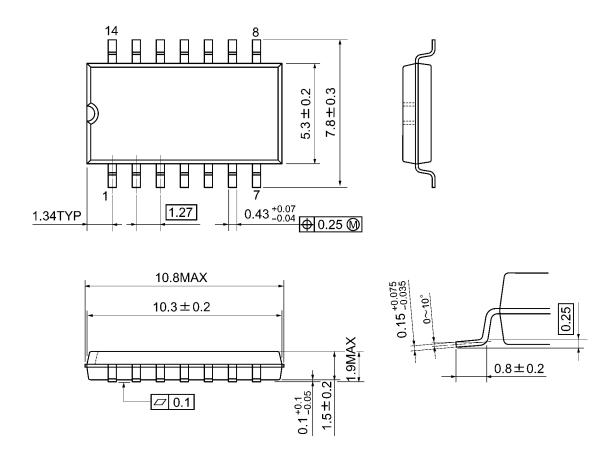
### **Input Equivalent Circuit**





### **Package Dimensions**

SOP14-P-300-1.27A Unit: mm

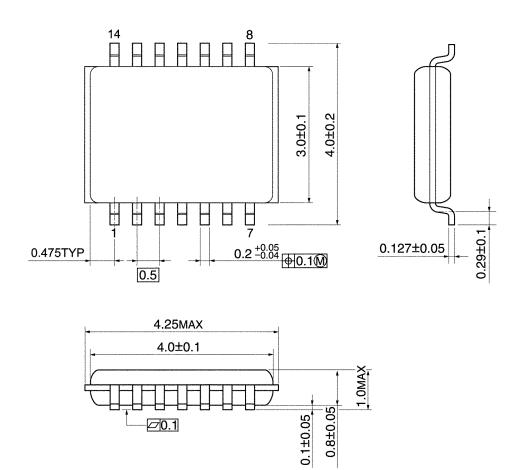


Weight: 0.18 g (typ.)



### **Package Dimensions**

VSSOP14-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)



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