TOSHIBA CMOS Linear Integrated Circuit Silicon Monolithic

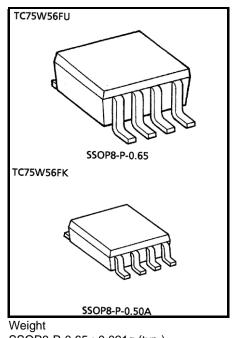
# **TC75W56FU, TC75W56FK**

#### **Dual Comparator**

TC75W56 is a CMOS type general-purpose dual comparator capable of single power supply operation and using lower supply currents than the conventional bipolar comparators. Its push-pull output can connect directly to local IC's such as TTL and CMOS circuits.

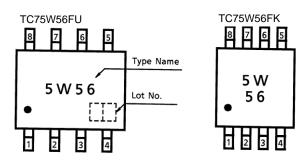
### Features

- Low supply current: IDD = 20µA (typ.)
- Single power supply operation
- Wide common mode input voltage range:  $V_{SS}$  to  $V_{DD}$ -0.9V
- Push-pull output circuit
- Low input bias current
- Small package

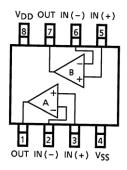


SSOP8-P-0.65 : 0.021g (typ.) SSOP8-P-0.50A : 0.01g (typ.)

### Marking (Top View)



### Pin Connection (Top View)



Start of commercial production 1997-05

### Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Ν	
Supply voltage	V <sub>DD</sub> , V <sub>SS</sub>	±3.5 or 7	V	
Differential input voltage	DVIN	±7	V	
Input voltage	VIN	$V_{\mbox{\scriptsize SS}}$ to $V_{\mbox{\scriptsize DD}}$	V	
Output current	lout	±35	mA	
Dever dissinction	D-	250 (TC75W56FU)		
Power dissipation	PD	200 (TC75W56FK)	mW	
Operating temperature	T <sub>opr</sub>	-40 to 85	°C	
Storage temperature	T <sub>stg</sub>	-55 to 125	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- Note: Since this product sometimes brings about latchcap, which is peculiar to CMOS devices, note the following points:
  - Don't raise the voltage level of I/O pins beyond VDD, nor lower it below VSS. Consider the timing for power supply, too.
  - Don't let any abnormal noise enter the device.

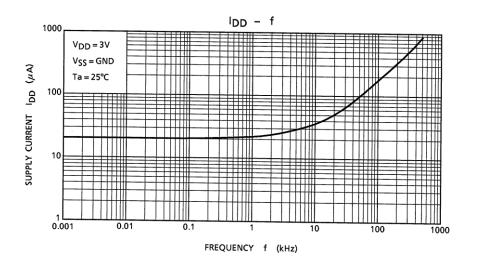
#### **Electrical Characteristics (VDD = 5V, VSS = GND, Ta = 25°C)**

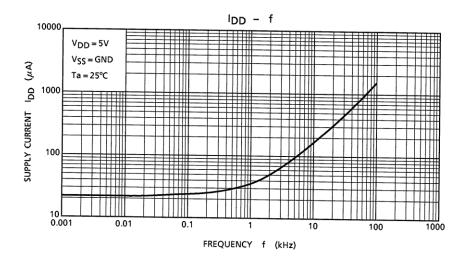
Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Input offset voltage	VIO	—	—	_	±1	±7	mV
Input offset current	lio	_	—	_	1	_	pА
Input bias current	lı	_	—	_	1	_	pА
Common mode input voltage	CMVIN	_	—	0	_	4.1	V
Supply current	I <sub>DD</sub> (Note)	_	—	_	22	44	μA
Voltage gain	Gv	_	—	_	94	_	dB
Sink current	I <sub>sink</sub>	_	V <sub>OL</sub> = 0.5V	13	25	_	mA
Source current	I <sub>source</sub>	_	V <sub>OH</sub> = 4.5V	9	21	_	mA
Output voltage	VOL	_	I <sub>sink</sub> = 5.0mA	_	0.1	0.3	V
	Vон	_	I <sub>source</sub> = 5.0mA	4.7	4.9	_	
Operating supply voltage	V <sub>DD</sub>	_	—	1.8	_	7.0	V
Propagation delay time (turn on)	t <sub>PLH</sub> (1)	_	Over drive = 100mV	_	680	_	ns
	t <sub>PLH</sub> (2)	_	TTL step input	_	500	_	
Propagation delay time (turn off)	t <sub>PHL</sub> (1)	_	Over drive = 100mV	_	250	_	ns
	t <sub>PHL</sub> (2)	_	TTL step i nput	_	380	_	
Response time	t <sub>TLH</sub>	_	Over drive = 100mV	—	60	—	ns
	tTHL	-	Over drive = 100mV	_	8	_	

#### Electrical Characteristics (VDD = 3V, VSS = GND, Ta = 25°C)

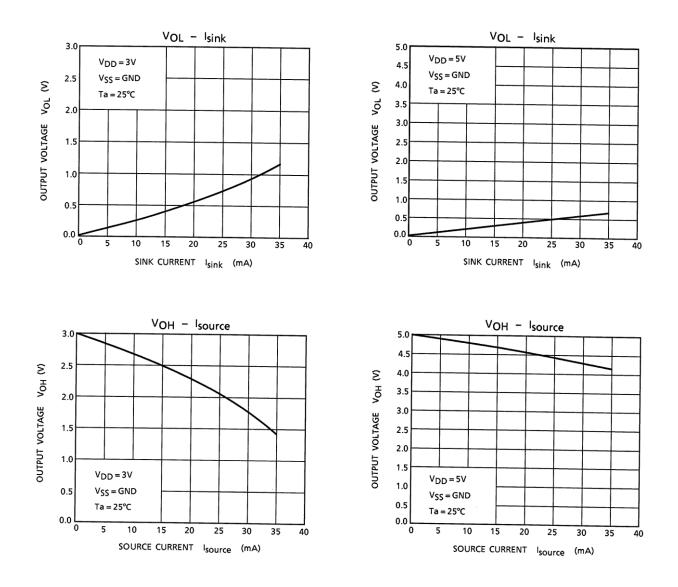
Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Input offset voltage	VIO	_	—	—	±1	±7	mV
Input offset current	lio	_	—	_	1	—	pА
Input bias current	li	_	—	_	1	—	pА
Common mode input voltage	CMVIN	_	—	0	—	2.1	V
Supply current	IDD (Note)	_	_	_	20	40	μA
Sink current	Isink	_	Vol = 0.5V	6	18	_	mA
Source current	Isource	_	VOH = 2.5V	3	15	—	mA
Output voltage	Vol	_	I <sub>sink</sub> = 5.0mA	_	0.15	0.35	V
	Vон	_	I <sub>source</sub> = 5.0mA	2.65	2.85	—	
Propagation delay time (turn on)	tPLH	_	Over drive = 100mV	_	550	_	ns
Propagation delay time (turn off)	tPHL	_	Over drive = 100mV	_	250	_	ns
Response time	ttlh	_	Over drive = 100mV	_	30	—	ns
	t <sub>THL</sub>	—	Over drive = 100mV	_	8	—	

Note: Since this product causes an increase in current consumption with a rise in operational frequency, make sure that power consumption does not exceed the allowable dissipation.





## **TOSHIBA**

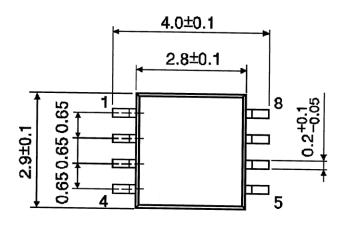


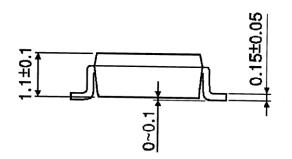
### **TOSHIBA**

### Package Dimensions

SSOP8-P-0.65

Unit: mm





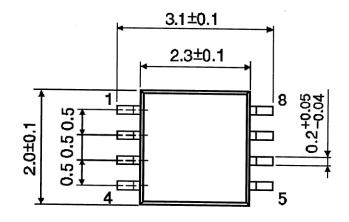
Weight: 0.021g(typ.)

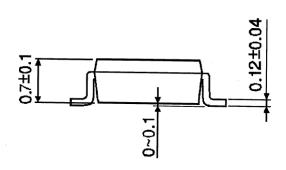
### **TOSHIBA**

### Package Dimensions

SSOP8-P-0.50A

Unit: mm





Weight: 0.01g(typ.)

# <u>TOSHIBA</u>

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