TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7MBD3245AFK

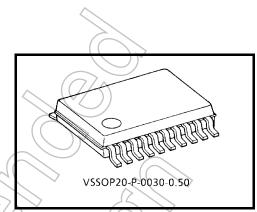
#### Octal Bus Switch

The TC7MBD3245AFK provides eight bits of high-speed TTL-compatible bus switching in a standard '245 device pinout. The low on-state resistance of the switch allows connections to be made with minimal propagation delay.

The device is organized as one 8-bit switch. When output enable  $(\overline{OE})$  is low, the switch is on and port A is connected to port B. When  $\overline{OE}$  is high, the switch is open and a high-impedance state exists between the two ports.

The device is enable to realize the shift of signal level from 5 V to 3.3 V.

All inputs are equipped with protection circuits against static discharge.

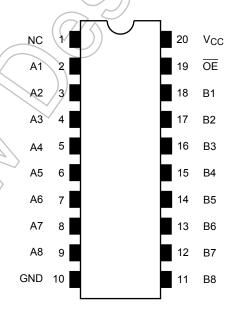


Weight: 0.03 g (typ.)

#### **Features**

- Operating voltage:  $V_{CC} = 4.5$  to 5.5 V
- High speed:  $t_{pd} = 0.32 \text{ ns (max)}$
- Low on resistance:  $RON = 5 \Omega$  (typ.)
- ESD performance: Machine model  $\geq \pm 200 \text{ V}$ Human body model  $\geq \pm 2000 \text{ V}$
- Compatible with TTL outputs (control inputs)
- Low Power Dissipation: Icc = 10 μA (max.)
- Package: VSSOP (US20)
- Pin compatible with the 74xx245 type.
- Functionally equivalent to (FST/CBT) 3245.

## Pin Assignment (top view)

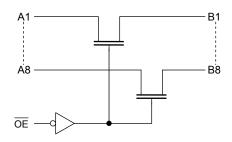


NC-No Internal Connection

#### **Truth Table**

Inputs	Function				
ŌĒ	FULCUOII				
L	A port = B port				
Н	Disconnect				

## **System Diagram**



## **Absolute Maximum Ratings (Note)**

Characteristics	Symbol	Rating	Unit
Power supply range	V <sub>CC</sub>	-0.5 to 7.0	V
DC input voltage	V <sub>IN</sub>	-0.5 to 7.0	У
DC switch voltage	Vs	-0.5 to 7.0	/[/
Input diode current	I <sub>IK</sub>	50	mA
Continuous channel circuit	Is	)) 128	mA
Power dissipation	PD	180	mW
DC V <sub>CC</sub> /ground current	I <sub>CC</sub> /I <sub>GND</sub>	±100	mA
Storage temperature	√T <sub>stg</sub>	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

# **Operating Ranges (Note)**

Characteristics	Symbol	Rating	Unit
Supply voltage	$V_{CC}$	4.5 to 5.5	V
Input voltage	V <sub>IN</sub>	0 to 5.5	V
Switch voltage	Vs	0 to 5.5	V
Operating temperature	T <sub>opr</sub>	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 10	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device.



#### **Electrical Characteristics**

## DC Characteristics ( $Ta = -40 \sim 85$ °C)

Charac	teristics	Symbol	Test Condition V <sub>CC</sub> (V)		Min	Typ. (Note 1)	Max	Unit	
Input voltage	"H" level	$V_{IH}$	_		4.5 to 5.5	2.0	_	_	V
iliput voitage	"L" level	V <sub>IL</sub>	_			X	_	0.8	V
High-level output voltage			IOU 1A		4.75	2.3	2.8	3.2	
r ligit-level outp	(Note 2)	$V_{OH}$	IOH = $-1 \mu A$ V <sub>IS</sub> = V <sub>CC</sub>		5.0	2.5	3.0	3.4	V
	(14010 2)		VIS - VCC	<	5.25	2.7	3.2	3.6	
Input leakage o	current	I <sub>IN</sub>	V <sub>IN</sub> = 0 to 5.5 V		4.5 to 5.5	<i></i>	_	±1.0	μА
Power off leaka	age current	I <sub>OFF</sub>	A, B, <del>OE</del> = 0 to 5.5 V		(0)	> —		±1.0	μΑ
Off-STATE leal (switch off)	kage current	I <sub>SZ</sub>	A, B = 0 to 5.5 V, $\overline{OE} = V_{CC}$		4.5 to 5.5	_		±1.0	μΑ
ON resistance (Note 3) R <sub>C</sub>	D	V <sub>IS</sub> = 0 V	I <sub>IS</sub> = 64 mA	4.5		<b>5</b>	9	Ω	
				4.75	-6	5	8		
				4.5	~_(	5	9		
	NON		1 S = 30 11 A	4.75	7	5	8	52	
		V <sub>IS</sub> = 2.3 V, I <sub>IS</sub> = 15 mA		4.5		35	65		
				4.75		35	50		
Quiescent supp	oly current	Icc	V <sub>IN</sub> = V <sub>CC</sub> or GND, I <sub>OUT</sub> = 0		5.5	$\overline{)}$	_	10	μА
Increase in I <sub>CC</sub>	per input	Δlcc	V <sub>IN</sub> = 3.4 V (one input)	, (	5.5	/ _		2.5	mA

Note 1: Typical values are at  $V_{CC} = 5 \text{ V}$ ,  $Ta = 25^{\circ}\text{C}$ .

Note 2: It recommends that this device uses Pull-up resistance when adding and using resistance for an output terminal. Since it couses to drop a VOH voltage level when using Pull-down resistance for an output terminal.

Note 3: Measured by the voltage drop between A and B pins at the indicated current through the switch. On resistance is determined by the lower of the voltages on the two (A or B) pins.

# AC Characteristics (Ta = -40~85°C)

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Max	Unit
Propagation delay time (bus to bus)	t <sub>pLH</sub>	Figure 1, Figure 2 (Note)	4.5	_	0.32	ns
Output enable time	t <sub>p</sub> ZL t <sub>p</sub> ZH	Figure 1, Figure 3	4.5	l	7.0	ns
Output disable time	t <sub>pLZ</sub>	Figure 1, Figure 3	4.5	_	7.0	ns

Note: The propagation delay time is calculated by the RC (on-resistance and load capacitance) time constant.

## **Capacitive Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Control pin input capacitance	C <sub>IN</sub>	(Note)	5.0	3	pF
Switch terminal capacitance	C <sub>I/O</sub>	$\overline{OE} = V_{CC}$ (Note)	5.0	10	pF

Note: This parameter is guaranteed by design.

#### **AC Test Circuit**

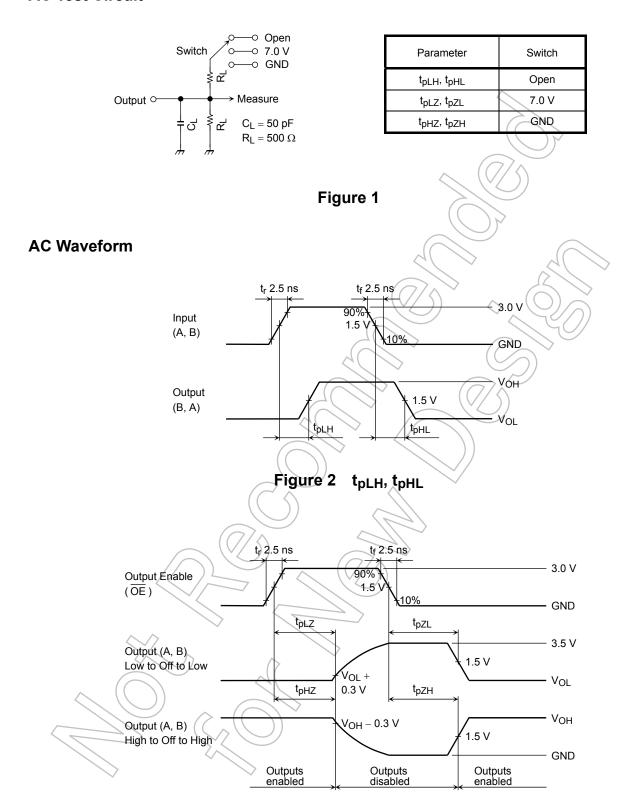


Figure 3  $t_{pLZ}$ ,  $t_{pHZ}$ ,  $t_{pZL}$ ,  $t_{pZH}$ 

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## **V<sub>OH</sub> – V<sub>CC</sub> Characteristics** (typ.)

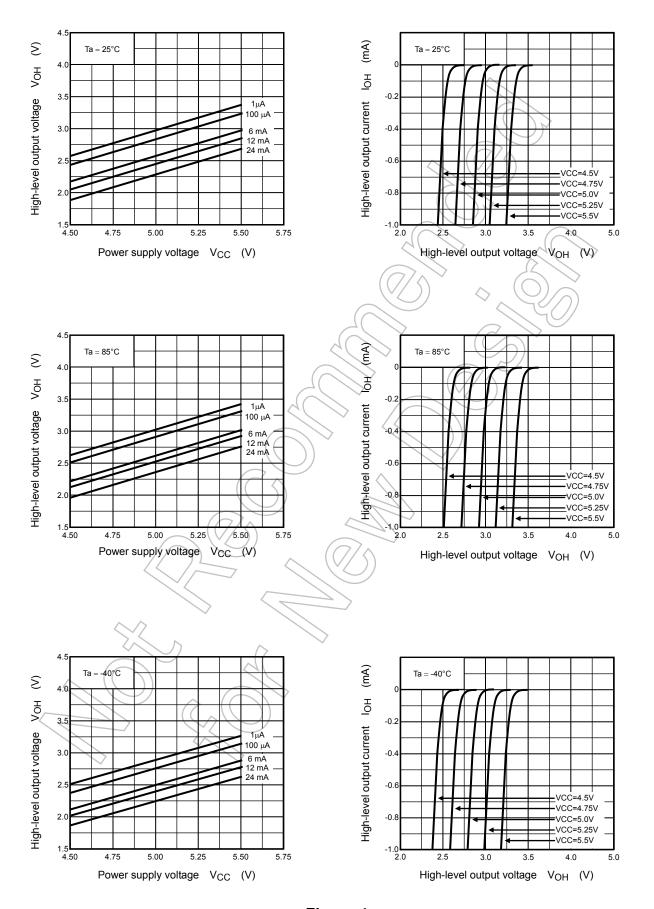
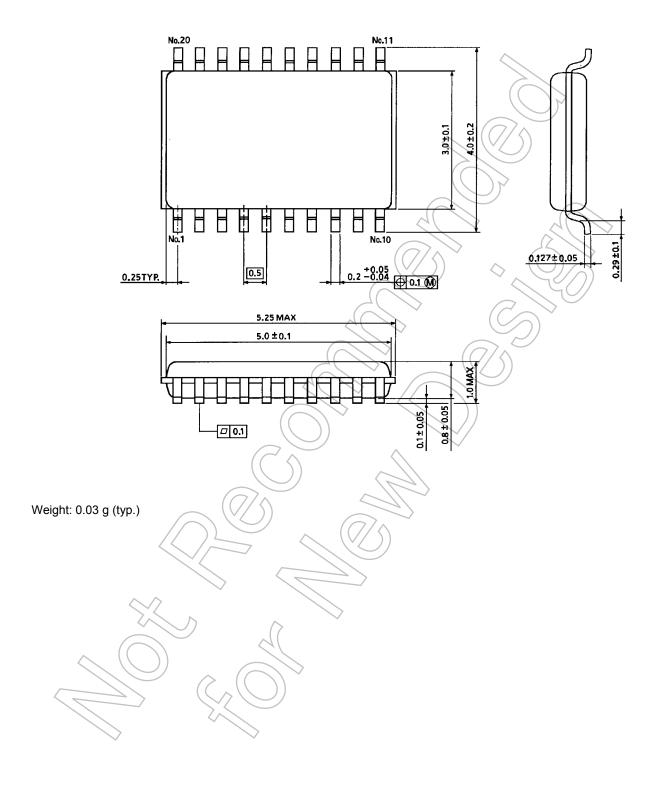


Figure 4

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## **Package Dimensions**

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