

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7S04F, TC7S04FU

Inverter

The TC7S04 is a high speed C²MOS Inverter fabricated with silicon gate C²MOS technology.

It achieves high speed operation similar to equivalent LSTTL while maintaining the C²MOS low power dissipation.

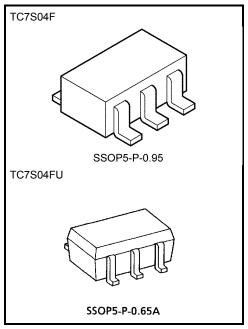
The internal circuit is composed of 3 stages including buffer output, which enables high noise immunity and stable output.

The input is equipped with protection circuits against static discharge or transient excess voltage.

Output currents are 1/2 compared to TC74HC series models.

Features

- High speed: tpd = 7 ns (typ.) at VCC = 5 V
- Low power dissipation: ICC = 1 μA (max) at Ta = 25°C
- High noise immunity: VNIH = VNIL = 28% VCC (min)
- · Output drive capability: 5 LSTTL loads
- Symmetrical output impedance: |IOH| = IOL = 2 mA (min)
- Balanced propagation delays: tpLH ≃ tpHL
- Wide operating voltage range: VCC (opr) = 2 to 6 V



Weight SSOP5-P-0.95: 0.016 g (typ.) SSOP5-P-0.65A: 0.006 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	Vcc	-0.5 to 7	V
DC input voltage	VIN	-0.5 to V _{CC} + 0.5	V
DC output voltage	Vout	-0.5 to V _{CC} + 0.5	V
Input diode current	lıK	±20	mA
Output diode current	lok	±20	mA
DC output current	lout	±12.5	mA
DC Vcc/ground current	Icc	±25	mA
Power dissipation	PD	200	mW
Storage temperature range	T _{stg}	-65 to 150	°C
Lead temperature (10 s)	TL	260	°C

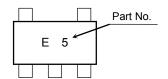
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

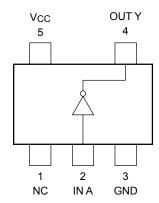
Start of commercial production 1987-08



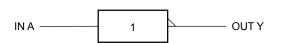
Marking



Pin Configuration (top view)



Logic Diagram



Truth Table

А	Y
L	Н
Н	L

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	2 to 6	V
Input voltage	VIN	0 to VCC	V
Output voltage	Vout	0 to VCC	٧
Operating temperature range	Topr	-40 to 85	°C
		0 to 1000 (V _{CC} = 2.0 V)	
Input rise and fall time	t _r , t _f	0 to 500 (V _{CC} = 4.5 V)	ns
		0 to 400 (V _{CC} = 6.0 V)	



Electrical Characteristics DC Electrical Characteristics

Characteristics Symbol Test Co					Ta = 25°C			Ta = -40 to 85°C			
		Condition	Vcc (V)	Min	Тур.	Max	Min	Max	Unit		
					2.0	1.5	_	_	1.5	_	
High level	High level	VIH	_		4.5	3.15	_	_	3.15	_	
				6.0	4.2	_	_	4.2	_	V	
Input voltage					2.0	_	_	0.5	_	0.5	V
	Low level	VIL	_		4.5	_	_	1.35	_	1.35	
					6.0	_	_	1.8	_	1.8	
			OH VIN = VIL	ΙΟΗ = -20 μΑ	2.0	1.9	2.0	_	1.9	_	-
Output voltage Low level		h level VOH			4.5	4.4	4.5	_	4.4	_	
	High level				6.0	5.9	6.0	_	5.9	_	
				I _{OH} = -2 mA	4.5	4.18	4.31	_	4.13	_	
				I _{OH} = -2.6 mA	6.0	5.68	5.80	_	5.63	_	V
		Low level VoL Vin			2.0	_	0	0.1	_	0.1	V
				I _{OL} = 20 μA	4.5	_	0	0.1	_	0.1	
	Low level		VIN = VIH		6.0	_	0	0.1	_	0.1	
				I _{OL} = 2 mA	4.5	_	0.17	0.26	_	0.33	
				I _{OL} = 2.6 mA	6.0	_	0.18	0.26	_	0.33	
Input leakage of	ut leakage current In VIN = VCC or GND		6.0	_	_	±0.1	_	±1.0	μΑ		
Quiescent sup	Quiescent supply current ICC VIN = VCC or GND		6.0			1.0	_	10.0	μΑ		

Note: Output currents are 1/2 compared to TC74HC series models.

AC Electrical Characteristics (C_L = 15 pF, input t_r = t_f = 6 ns, V_{CC} = 5 V)

Characteristics	Symbol	Toot Condition		l lmit		
		Test Condition	Min	Тур.	Max	Unit
Output transition time	tтLH tтнL	-	-	5	10	ns
Propagation delay time	t _{pLH} t _{pHL}	-	_	7	15	ns



AC Electrical Characteristics ($C_L = 50 \text{ pF}$, input $t_r = t_f = 6 \text{ ns}$)

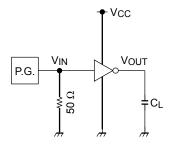
			Ta = 25°0				Ta = -40 to 85°C		
Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Output transition time	tTLH tTHL	_	2.0	1	50	125	-	155	ns
			4.5	١	14	25	1	31	
			6.0	1	12	21	1	26	
Propagation delay time	t _{pLH} t _{pHL}	_	2.0	l	48	100	ı	125	ns
			4.5	ı	12	20	-	25	
			6.0	1	9	17	1	21	
Input capacitance	CIN			1	5	10	1	10	pF
Power dissipation capacitance	CPD		(Note 1)	1	10		-	-	pF

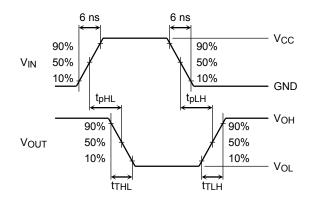
Note 1: CPD defined as the value of internal equivalent capacitance of IC which is calculated from the operating current consumption without load (refer to test circuit).

Average operating current can be obtained by the equation hereunder.

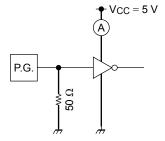
ICC (opr) = CPD • VCC • fIN + ICC

Switching Characteristics Test Circuit





Icc (opr) Test Circuit

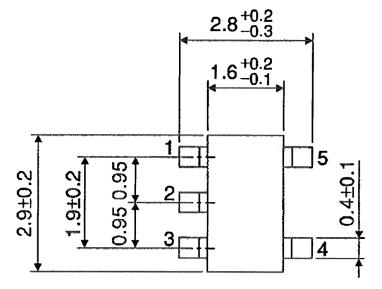


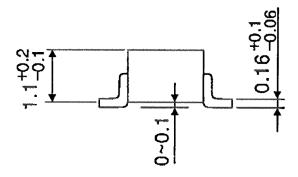
Input waveform is the same as that in case of switching characteristics test.



Package Dimensions

SSOP5-P-0.95 Unit: mm



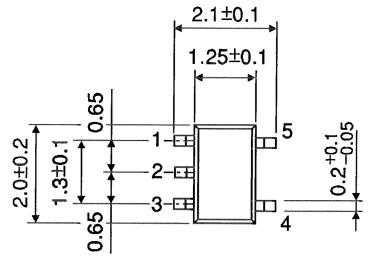


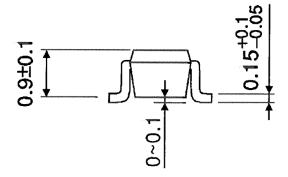
Weight: 0.016 g (typ.)



Package Dimensions

SSOP5-P-0.65A Unit: mm





Weight: 0.006 g (typ.)



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