TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SG32FU

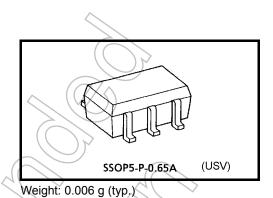
2 Input OR Gate

Features

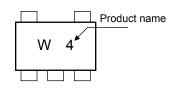
- High output current: ±8 mA (min) at V_{CC} = 3.0 V
- High-speed operation: t_{pd} = 2.4 ns (typ.)

at $V_{CC} = 3.3 \text{ V}, 15 \text{pF}$

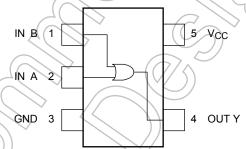
- Operating voltage range: V_{CC} = 0.9 to 3.6 V
- 5.0-V tolerant inputs
- 3.6-V power down protection output.



Marking



Pin Assignment (top view)



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	Vec	-0.5 to 4.6	٧
DC input voltage	V _{IN}	-0.5/to/7.0	٧
DC output valtage	\/	-0.5 to 4.6 (Note 1)	٧
DC output voltage	Vout	-0.5 to V _{CC} + 0.5 (Note 2)	V
Input diode current	I _{IK}	-20	mA
Output diode current	I _{OK} />	-20 (Note 3)	mA
DC output current	I _{OOT}	±25	mA
DC V _{CC} /ground current	tcc	±50	mA
Power dissipation	(PD)	200	mW
Storage temperature	T _{stg}	−65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0V$

Note 2: High or Low State. I_{OUT} abusolute maximum rating must be observed.

Note 3: V_{OUT} < GND

Start of commercial production 2005-02

IEC Logic Symbol



Truth Table

Α	В	Υ
L	L	L
L	Н	Н
Н	L	Н
Н	Н	Н

Operating Rating

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	0.9 to 3.6	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	V _{OUT}	0 to 3.6 (Note 4) 0 to Vcc (Note 5)	V (
Output Current	I _{OH} /I _{OL}	±8.0 (Note 6) ±4.0 (Note 7) ±3.0 (Note 8) ±1.7 (Note 9) ±0.3 (Note 10) ±0.02 (Note 11)	mA
Operating temperature	Topr	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 10 (Note 12)	ns/V

Note 4: $V_{CC} = 0V$

Note 5: High or Low state.

Note 6: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$

Note 7: $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$

Note 8: $V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$

Note 9: $V_{CC} = 1.4 \text{ to } 1.6 \text{ V}$

Note 10: $V_{CC} = 1.1 \text{ to } 1.3 \text{ V}$

Note 11: $V_{CC} = 0.9 V$

Note 12: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test	Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
Characteristics	Symbol	Circuit	Tost Condition		V _{CC} (V)	Min	Тур.	Max	Min	Max	Onit
					0.9	V _{CC}	_ <	H	V _{CC}		
					1.1 to 1.3	V _{CC} × 0.7	_		V _{CC} ×0.7		
High-level input voltage	V _{IH}	_		_	1.4 to 1.6	V _{CC} × 0.65	10) 	V _{CC} × 0.65		V
Voltage					1.65 to 1.95	V _{CC} × 0.65)	V _{CC} × 0.65	١	
					2.3 to 2.7	1.7	(-)	<u> </u>	1.7		
					3.0 to 3.6	2.0)	_	2.0	-	
					0.9	<u> </u>	<u> </u>	GND	4	GND	
					1.1 to 1.3	75		V _{CC} × 0.3	5-	∨ V _{CC} × 0.3	
Low-level input voltage	V _{IL}	_		_	1.4 to 1.6	2	_	V _{CC} × 0.35	(4)	V _{CC} × 0.35	V
voltage					1.65 to 1.95	_	-((V _{CC} × 0.35	<u> </u>	V _{CC} × 0.35	
					2.3 to 2.7	_		0.7		0.7	
				2	3.0 to 3.6	_	(4)	0.8		8.0	
				$I_{OH} = -0.02 \text{ mA}$	0.9	0.75	\ <u> </u>		0.75	_	
				$I_{OH} = -0.3 \text{ mA}$	1.1 to 1.3	V _{CC} × 0.75))_	_	V _{CC} × 0.75	_	
High-level output voltage	V _{OH}	_	V _{IN} = V _{IH} or V _{IL}	$I_{OH} = -1.7 \text{ mA}$	1.4 to 1.6	V _{CC} × 0.75	_	_	V _{CC} × 0.75	_	V
output voltage				I _{OH} = -3.0 mA	1.65 to 1.95	V _{CC} >-0.45	_	_	V _{CC} -0.45		
	/		$(\langle // $	$I_{OH} = -4.0 \text{ mA}$	2.3 to 2.7	2.0		_	2.0	_	
				$I_{OH} = -8.0 \text{ mA}$	3.0 to 3.6	2.48		_	2.48	_	
		1/		$I_{OL} = 0.02 \text{ mA}$	0.9	_	_	0.1	_	0.1	
			>	$I_{OL} = 0.3 \text{ mA}$	1.1 to 1.3	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	
Low-level output voltage	VoL	<i>γ</i> _	$V_{IN} = V_{IL}$	I _{OL} = 1.7 mA	1.4 to 1.6	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	٧
			d	$I_{OL} = 3.0 \text{ mA}$	1.65 to 1.95	_	_	0.45	_	0.45	
))			$I_{OL} = 4.0 \text{ mA}$	2.3 to 2.7			0.4	_	0.4	
		\bigcirc		$I_{OL} = 8.0 \text{ mA}$	3.0 to 3.6	_	_	0.4	_	0.4	
Input leakage current	> IIN	$\frac{1}{\sqrt{2}}$	$V_{IN} = 0$ to	5.5 V	0 to 3.6	_	_	±0.1	_	±1.0	μА
Power off leakage current	l _{OFF}		V _{IN} = 0 to 5.5 V V _{OUT} = 0 to 3.6 V		0	_	_	1.0		10.0	μΑ
Quiescent supply current	Icc	_	V _{IN} = V _{CC}	or GND	3.6	_	_	1.0	_	10.0	μА

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AC Electrical Characteristics (unless otherwise specified, input $t_{r} = t_{f} = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
			C _{L (} pF)	Min	Тур.	Max	Min	Max	
		C _L = 10 pF,	0.9	_	17.0	_	_	_	
			1.1 to 1.3	_	8.8	18.4	1.0	34.2	
			1.4 to 1.6	_	5.0	8.5	1.0	10.0	ns
		$R_L = 1 M\Omega$	1.65 to 1.95	_	3.8	6.2	/1.0	6.7	
			2.3 to 2.7	1	2.7	3.9	1.0	4.4	
			3.0 to 3.6	_	2.1	3.1	1.0	3.7	
		$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	-((20.7	> —		_	
			1.1 to 1.3		10.6	21.5	1.0	37.2	
Propagation delay time	t _{PLH}		1.4 to 1.6	1	5.9	9.3	1,0	11.2	
Propagation delay time			1.65 to 1.95		4.5	6.9	1.0	7.1	113
			2.3 to 2.7	\(\hat{\)}\)	3.0	4.4	1.0	5.0	
			3.0 to 3.6		2.4	3.4	(1.0)	3.9	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	29.6	<u> </u> 	5	_	
			1.1 to 1.3	_	14.8	29.6	1.0	56.0	
			1.4 to 1.6	_	8.0	13.1	1.0	15.9	
			1.65 to 1.95	1(6.0	9.2	1.0	9.6	
			2.3 to 2.7		3.9	5.7	1.0	6.1	
			3.0 to 3.6	_	3.0	4.4	1.0	4.8	
Input capacitance	C _{IN}	((-))	3.6	-	3	_	_	_	pF
Power dissipation capacitance	C_{PD}	(Note 13)	0.9 to 3.6		6	_	_	_	pF

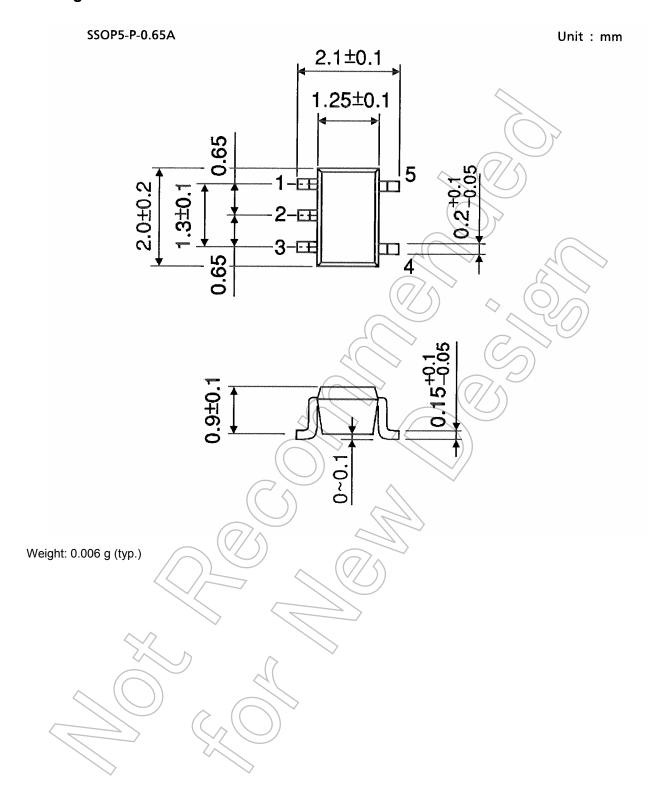
Note 13: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $ICC (opr.) = CPD \cdot VCC \cdot fIN + ICC$



Package Dimensions



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