CMOS Digital Integrated Circuits Silicon Monolithic

TC7SH17FU

1. Functional Description

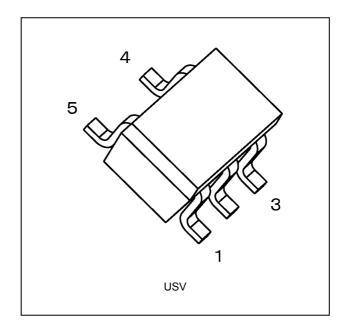
Schmitt Buffer

2. Features

- (1) AEC-Q100 (Rev. H) (Note 1)
- (2) Wide operating temperature range: $T_{opr} = -40$ to 125 °C (Note 2)
- (3) High speed operation: $t_{pd} = 5.5$ ns (typ.) ($V_{CC} = 5.0$ V, $C_L = 15$ pF)
- (4) Low power dissipation: $I_{CC} = 2.0 \ \mu A \ (max) \ (T_a = 25 \ ^\circ C)$
- (5) Wide operating voltage range: V_{CC} = 2.0 to 5.5 V
- (6) High noise immunity: $V_{\text{NIH}} = V_{\text{NIL}} = 28 \% V_{\text{CC}}$ (min)
- (7) 5.5 V tolerant inputs
- Note 1: This device is compliant with the reliability requirements of AEC-Q100. For details, contact your Toshiba sales representative.

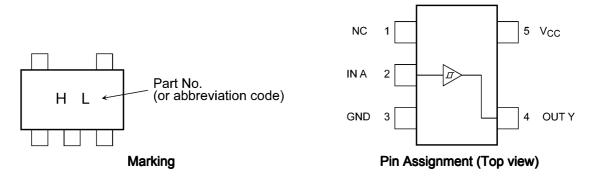
Note 2: For devices with the ordering part number ending in J(CT. T_{opr} = -40 to 85 °C for the other devices.

3. Packaging



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4. Marking and Pin Assignment



5. IEC Logic Symbol



6. Truth Table

A	Y
L	L
Н	Н

7. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V _{CC}		-0.5 to 7.0	V
Input voltage	V _{IN}		-0.5 to 7.0	
DC output voltage	V _{OUT}		-0.5 to V _{CC} + 0.5	
Input diode current	I _{IK}		-20	mA
Output diode current	I _{OK}	(Note 1)	±20	
DC output current	I _{OUT}		±25	
V _{CC} /ground current	I _{CC}		±50	
Power dissipation	PD		200	mW
Storage temperature	T _{stg}		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{OUT} < GND$, $V_{OUT} > V_{CC}$

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8. Operating Ranges (Note)

Characteristics	Symbol	Note	Test Condition	Rating	Unit
Supply voltage	V _{CC}		—	2.0 to 5.5	V
Input voltage	V _{IN}		_	0 to 5.5	
Output voltage	V _{OUT}		—	0 to V _{CC}	
Operating temperature	T _{opr}	(Note 1)	—	-40 to 125	°C
		(Note 2)		-40 to 85	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

Note 1: For devices with the ordering part number ending in J(CT).

Note 2: For devices except those with the ordering part number ending in J(CT.

9. Electrical Characteristics

9.1. DC Characteristics (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Test Conditior	1	V _{CC} (V)	Min	Тур.	Max	Unit
Positive threshold voltage	VP	—		3.0	_	—	2.20	V
				4.5	_	—	3.15	
				5.5	_	—	3.85	
Negative threshold voltage	V _N	—		3.0	0.90	—	_	V
				4.5	1.35	—	_	
				5.5	1.65	—	_	
Hysteresis voltage	V _H	—		3.0	0.30	_	1.20	V
				4.5	0.40	_	1.40	
				5.5	0.50	_	1.60	
High-level output voltage	V _{OH}	V _{IN} = V _{IH}	I _{OH} = -50 μA	2.0	1.9	2.0	_	V
				3.0	2.9	3.0	_	
				4.5	4.4	4.5	_	
			I _{OH} = -4 mA	3.0	2.58	_	_	
			I _{OH} = -8 mA	4.5	3.94	_	_	
Low-level output voltage	V _{OL}	V _{IN} = V _{IL}	I _{OL} = 50 μA	2.0	_	0.0	0.1	V
				3.0	_	0.0	0.1	
				4.5	_	0.0	0.1	
			I _{OL} = 4 mA	3.0	_	_	0.36	
			I _{OL} = 8 mA	4.5		_	0.36	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5		_	±0.1	μA
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND		5.5	_	_	2.0	μA

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9.2. DC Characteristics (Unless otherwise specified, $T_a = -40$ to 85 °C)

Characteristics	Symbol	Test Con	V _{CC} (V)	Min	Max	Unit	
Positive threshold voltage	V _P	_		3.0	_	2.20	V
				4.5	_	3.15	
				5.5	_	3.85	
Negative threshold voltage	V _N	_		3.0	0.90	—	V
				4.5	1.35	—	
				5.5	1.65	—	
Hysteresis voltage	V _H	_		3.0	0.30	1.20	V
				4.5	0.40	1.40	
				5.5	0.50	1.60	
High-level output voltage	V _{OH}	V _{IN} = V _{IH}	I _{OH} = -50 μA	2.0	1.9	_	V
				3.0	2.9	_	
				4.5	4.4	_	
			I _{OH} = -4 mA	3.0	2.48	—	
			I _{OH} = -8 mA	4.5	3.80	_	
Low-level output voltage	V _{OL}	V _{IN} = V _{IL}	I _{OL} = 50 μA	2.0	_	0.1	V
				3.0	_	0.1	
				4.5	_	0.1	
			I _{OL} = 4 mA	3.0	_	0.44	
			I _{OL} = 8 mA	4.5	_	0.44	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5		±1.0	μA
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND		5.5		20.0	μA

9.3. DC Characteristics (Note) (Unless otherwise specified, T_a = -40 to 125 °C)

Characteristics	Symbol	Test Con	dition	V _{CC} (V)	Min	Мах	Unit
Positive threshold voltage	V _P	—		3.0	_	2.20	V
				4.5	_	3.15	
				5.5	_	3.85	
Negative threshold voltage	V _N	_		3.0	0.90	_	V
				4.5	1.35	—	
				5.5	1.65	—	
Hysteresis voltage	V _H	_		3.0	0.30	1.20	V
				4.5	0.40	1.40	
				5.5	0.50	1.60	
High-level output voltage	V _{OH}	H VIN = VIH	I _{OH} = -50 μA	2.0	1.9	_	V
				3.0	2.9	_	
				4.5	4.4	_	
			I _{OH} = -4 mA	3.0	2.40	_	
			I _{OH} = -8 mA	4.5	3.70	_	
Low-level output voltage	V _{OL}	V _{IN} = V _{IL}	I _{OL} = 50 μA	2.0	_	0.1	V
				3.0	_	0.1	
				4.5	_	0.1	
			I _{OL} = 4 mA	3.0	_	0.55	
			I _{OL} = 8 mA	4.5	_	0.55	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	_	±2.0	μA
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND		5.5	_	40.0	μA

Note: For devices with the ordering part number ending in J(CT.

9.4. AC Characteristics (Unless otherwise specified, $T_a = 25$ °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}		—	$\textbf{3.3}\pm\textbf{0.3}$	15	_	8.3	12.8	ns
					50	_	10.8	16.3	
				5.0 ± 0.5	15		5.5	8.6	
					50		7.0	10.6	
Input capacitance	C _{IN}		_			_	4	10	pF
Power dissipation capacitance	C _{PD}	(Note 1)	_			—	14	_	pF

Note 1: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation.

 $I_{CC(opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

9.5. AC Characteristics (Unless otherwise specified, $T_a = -40$ to 85 °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition	V _{CC} (V)	C _L (pF)	Min	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}	—	$\textbf{3.3}\pm\textbf{0.3}$	15	1.0	15.0	ns
				50	1.0	18.5	
			5.0 ± 0.5	15	1.0	10.0	
				50	1.0	12.0	
Input capacitance	C _{IN}	_			_	10	pF

9.6. AC Characteristics (Note) (Unless otherwise specified, $T_a = -40$ to 125 °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition	V _{CC} (V)	C _L (pF)	Min	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}	—	$\textbf{3.3}\pm\textbf{0.3}$	15	1.0	17.0	ns
				50	1.0	20.5	
			5.0 ± 0.5	15	1.0	11.5	
				50	1.0	13.5	
Input capacitance	C _{IN}	—			_	10	рF

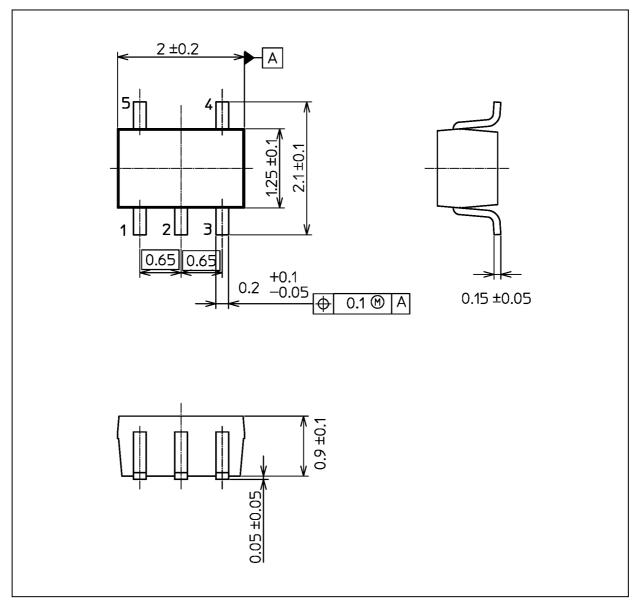
Note: For devices with the ordering part number ending in J(CT.



Package Dimensions

TC7SH17FU

Unit: mm



Weight: 0.006 g (typ.)

	Package Name(s)
JEDEC: SOT-353	
Nickname: USV	

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